

CHAPTER 9. DESCRIPTION OF SCHEMATIC DIAGRAM

[1] DESCRIPTION OF SCHEMATIC DIAGRAM

DESCRIPTION OF SCHEMATIC DIAGRAM


NOTES:

1. The unit of resistance "ohm" is omitted.
($K=k\Omega=1000\Omega$, $M=M\Omega$)
2. All resistors are 1/16 watt, unless otherwise noted.
3. All capacitors are μF , unless otherwise noted.
($P=pF=\mu\mu F$)
4. (G) indicates $\pm 2\%$ tolerance may be used.
5. $\overline{\text{---}}$ indicates line isolated ground.

VOLTAGE MEASUREMENT CONDITIONS:

1. All DC voltages are measured with DVM connected between points indicated and chassis ground, line voltage set at 120V AC and all controls set for normal picture unless otherwise indicated.
2. All voltages measured with 1000 μ V B & W or Color signal.

WAVEFORM MEASUREMENT CONDITIONS:

1. Photographs taken on a standard gated color bar signal, the tint setting adjusted for proper color. The wave shapes at the red, green and blue cathodes of the picture tube depend on the tint, color level and picture control.
2.  indicates waveform check points (See chart, waveforms are measured from point indicated to chassis ground.)

 AND SHADED () COMPONENTS
= SAFETY RELATED PARTS.
 MARK= X-RAY RELATED PARTS.

ORGANES MARQUES  ET HACHURES ():
PIECES RELATIVES A LA SECURITE.
MARQUE  : PIECES RELATIVE AUX RAYONS X.

This circuit diagram is a standard one, printed circuits may be subject to change for product improvement without prior notice.