

# **LED COLOUR TV**

## **MAINTENANCE MANUAL**

**MODEL:** HLS78D-I    **CHASSIS**    MSD6486

JUC7.820.00192431-1

*Please read this manual carefully before maintenance*

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# **Chapter1 Safety and notes**

## **1-1 Installation notes**

- (1) Please don't beat or rub, scratch the surface of the LED screen heavily, don't touch it with your hand casually.
- (2) When the screen is dirty, please clean it with absorbent cotton or cotton cloth slightly.
- (3) Please clean it timely when water or other viscosity pollution fall, which may make the LED face or color change.
- (4) Please don't make the LED screen shake by strong external force.

## **1-2 Attention points of operation and using**

- (1) Please unplug the power cable before moving the LED screen.
- (2) Please don't change the original setting of the main boards, if not, the brightness and white balance etc. may not meet the specification.
- (3) The radiation of a long time using in the room temperature is larger than the low temperature.
- (4) Please note that the long displaying image may remain at the top when shutdown the machine.
- (5) Please avoid the impact from the mobile phone to protect your TV.

## **1-3 Storage notes**

- (1)When stored for a long time, please keep the temperature between 0°C to 40°C, don't expose the TV to the strong sunlight, the humidity should be less than 85%RH.
- (2)Please don't put your TV under high humidity and high temperature environment, for example, the temperature: 60°C, and the humidity: 85%RH.
- (3)Please don't put your TV under low temperature environment, for example, the temperature lower than -25°C.

## **1-4 Dismantling notes**

- (1)As LED screen is easy to be damaged, while dismantle, please attention to protect.
- (2)Please attention the position of each screw when dismantle, in case to beat the wrong position when install, if not, it may lead to crack or slide of the face frame.
- (3) If you need to dismantle the power board or the main board, please attention the position and direction of each line, especially the direction of the screen line, in case of causing accident when install. Before

dismantle, we can take some photos of the line route for the comparison of installing.

(4)After check and maintenance, please assure that there is no foreign body in the machine when install.

## 1-5 High-voltage warning

The high-voltage of the LED screen is generated by the power supply board, without attention to exposure to the high voltage, one may meet a serious electricity shock.

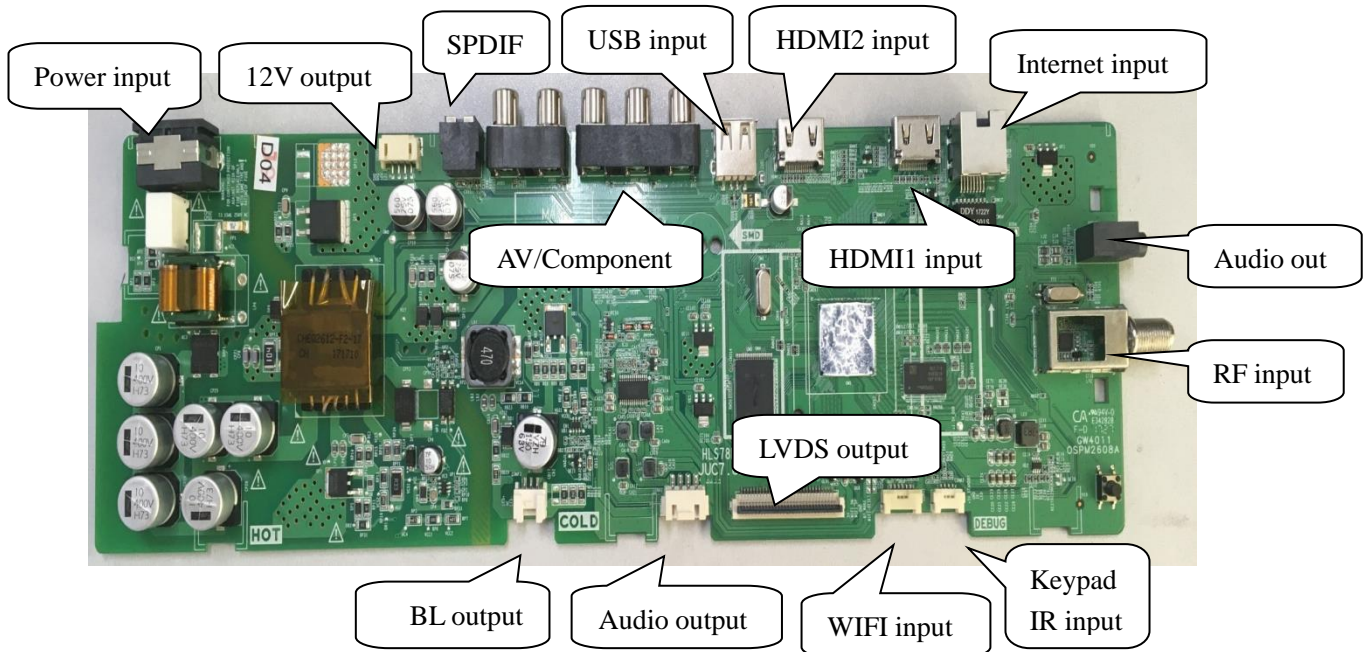
## Chapter2 whole machine standard and terminal functions

### 2-1 Basic standard

Item		Standard	
LED Panel	Active Screen Size	31.5 inches	
	Bezel Opening Area	697.685mm(H) *392.256mm(V)	
	Pixel Pitch	0.17025mm*0.51075mm	
	Pixel Format	1366 horiz. by 768 vert. Pixels, RGB stripe arrangement	
	Color Depth	16.7M(Dithered 8bit )	
	Luminance, White	200 cd/m <sup>2</sup> (Typ.)	
	Viewing Angle (CR>10)	170(H);170(V)	
	contrast ratio	3000:1	
	Driver Element	a-Si TFT active matrix	
	Display Operation Mode	Normally black	
	Surface Treatment	Hard Coating, 3H	
TV function	sound system	M	
	color system	NTSC/PAL-M/PAL-N	
Audio and video signal input	AV	AV x 1	
	HDMI	HDMI x2	support to1080P
	USB	USB x1	support media player
Audio output		Audio output L / R	8W inner speakers for each channel
SPDIF		Optical Fiber	Support Audio output
Internet	RJ-45 terminal		
	WIFI		
Power	power supply		AC100V~240V, 50/60Hz
	power achievement		<55W

requirement for environment	operation temperature	+ 0° ~ + 50° ,
	storage temperature	- 20 ° ~ + 60°
	operation humidity	20% ~ 90%
appearance size	W x H x D	731. 18mm (W) *435. 04mm (H) *53. 8mm (D)

## 2-2 Introduction of terminals(practicality photos)



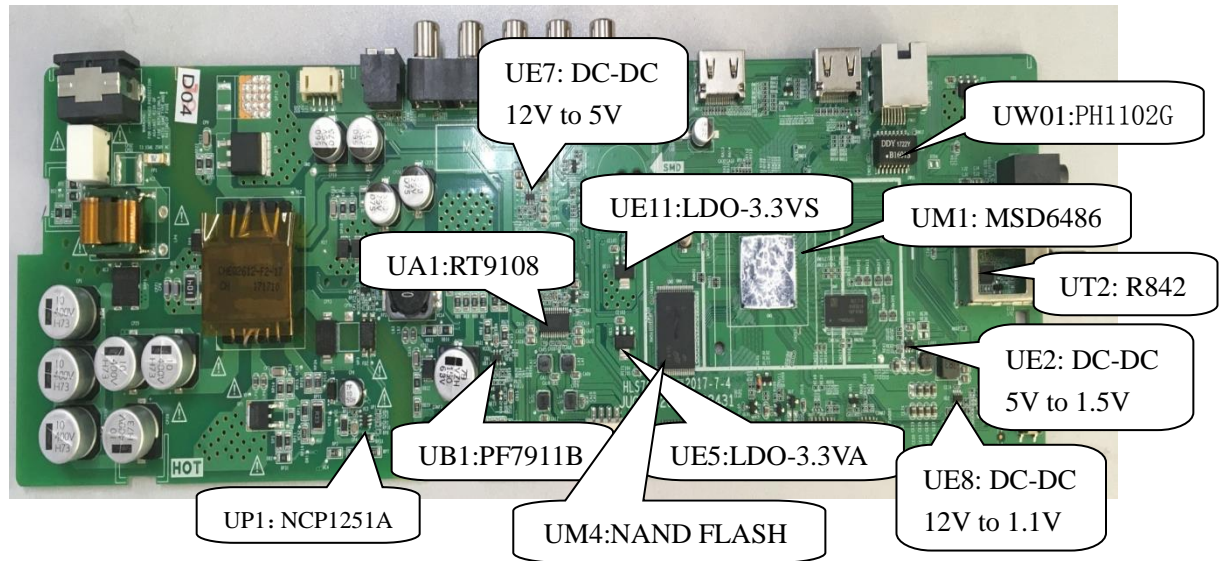
ATTENTION:

1.HDMI support to 1080P;

## Chapter3 Main chip functions and the introductions of power supply

### 3-1 Main IC and functions of HLS78D-I

No.	Item no.	Model	Main function
1	UT2	R842	Tuner output IF signal
2	UM1	MSD6486	Video decoder, image processor, A/D and D/A conversion
3	UM4	MT29F4G08ABADAWP:D	NAND Flash/4Gbit
4	UA1	RT9108	15W Stereo (BTL) Analog Input Audio Amplifier with Power Limiter and DC Detect
6	UE7	TPS563201	12V to 5V DC-DC
7	UE2	SY8088	12V to 1.5V DC-DC/For Main Chip
9	UE5	AMS1117-3.3	5V to 3.3V LDO/Normal For Main Chip& NAND Flash
11	UE11	AMS1117-3.3	5V to 3.3V LDO/Standby For Main Chip
13	UW01	PH1102G	10/100 BASE-T Single Port Surface Mount Magnetics
14	UP1	NCP1251A	Current-Mode PWMController for Off-line Power Supplies
11	UE8	MP2225GJ	12V to 1.1V DC-DC/For Main Chip
15	UB1	PF7911B	LED Voltage BoostController



### 3-2 Pin function description of HLS78D-I chip and description of power supply

#### 3-2-1 MSD6486 recommended operating power conditions

#### Recommended Operating Conditions

Parameter	Symbol	Min	Typ	Max	Unit
3.3V Supply Voltages	$V_{VDD\_33}$	3.14		3.46	V
1.5V Supply Voltages	$V_{VDD\_15}$	1.43		1.57	V
Core Power Voltages	$V_{VDD\_core}$	TBD	TBD	TBD	V
Ambient Operating Temperature	$T_A$	0		70	°C
Junction Temperature	$T_J$			125	°C

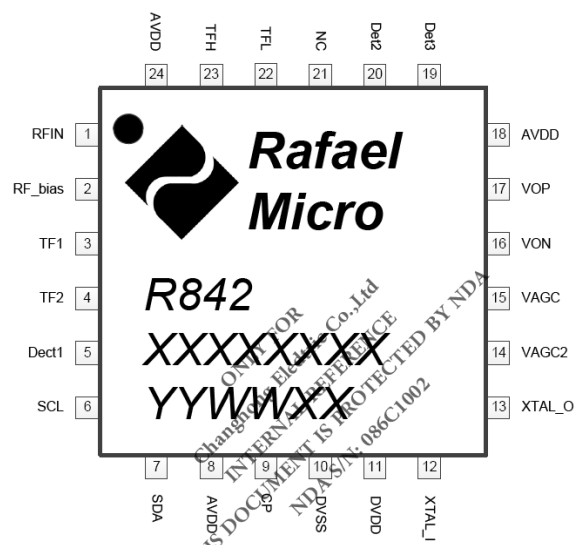
#### Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
3.3V Supply Voltages	$V_{VDD\_33}$		3.6	V
1.5V Supply Voltages	$V_{VDD\_15}$		1.65	V
Core Power Voltages	$V_{VDD\_core}$		TBD	V
Input Voltage (5V tolerant inputs)	$V_{IN5Vtol}$		5.0	V
Input Voltage (non 5V tolerant inputs)	$V_{IN}$		$V_{VDD\_33}$	V
Storage Temperature	$T_{STG}$	-40	150	°C

**Note:** Stresses above those listed in Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only and does not imply functional operation of the device. Exposure to absolute maximum ratings for extended periods may affect device reliability.

### 3-2-2 Pin function of R842

#### Pin description:



Pin Number	Symbol	I/O	Description
1	RFIN	I	RF input
2	RF_bias	-	RF circuit bias
5,19,20	Detx	-	Power detector decoupling capacitor
3,4,22,23	TFxx	-	Tracking filter pin out
6	SCL	I	I <sup>2</sup> C bus, clock input
7	SDA	I/O	I <sup>2</sup> C bus, data input/ output
8	AVDD	S	AVDD for PLL
9	CP	-	PLL Charge Pump decouple
10	DVSS	S	Digital Ground
11	DVDD	S	Digital 3.3V Supply
12	XTAL_I	I	Crystal Driver Input
13	XTAL_O	I	Crystal Driver Output
14,15	VAGC	I	IF automatic gain control input
16,17	VOP, VON	O	Differential IF output
18	AVDD	S	Analog 3.3V supply
21	NC	-	No used
24	AVDD	S	RF 3.3V Supply

Table 2-1. R842 Summary List of Pin Assignment for Hybrid version

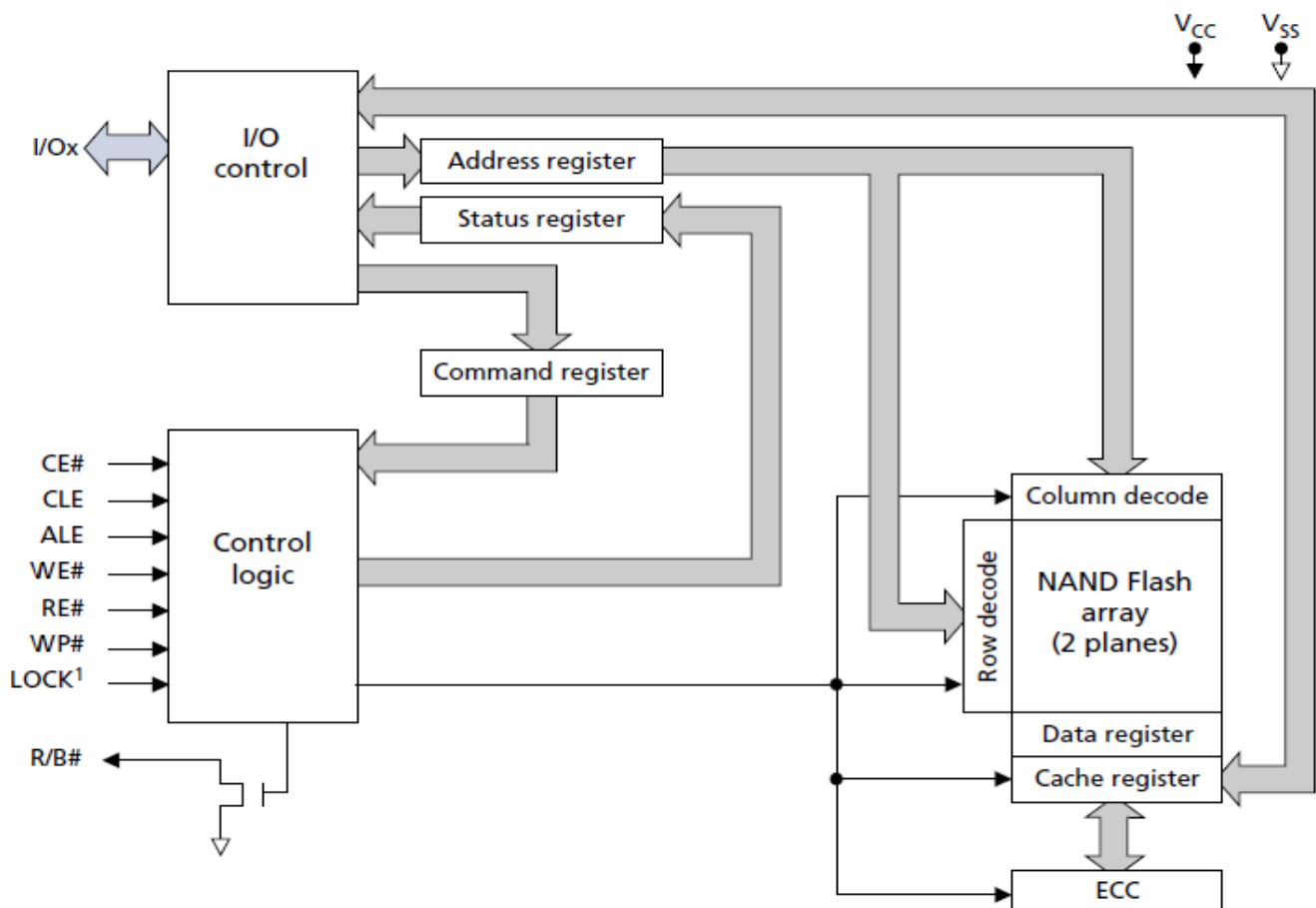
(note: E-Pad is GND)

### 3-2-3 MT29F4G08ABADAWP:D NAND Flash/4Gbit brief introduction:

#### Pin introduction:

Signal <sup>1</sup>	Type	Description <sup>2</sup>
ALE	Input	<b>Address latch enable:</b> Loads an address from I/O[7:0] into the address register.
CE# CE#2	Input	<b>Chip enable:</b> Enables or disables one or more die (LUNs) in a target. For the 16Gb device, CE# controls the first 8Gb of memory; CE2# controls the second 8Gb of memory.
CLE	Input	<b>Command latch enable:</b> Loads a command from I/O[7:0] into the command register.
LOCK	Input	When LOCK is HIGH during power-up, the BLOCK LOCK function is enabled. To disable the BLOCK LOCK, connect LOCK to V <sub>SS</sub> during power-up, or leave it disconnected (internal pull-down).
RE#	Input	<b>Read enable:</b> Transfers serial data from the NAND Flash to the host system.
WE#	Input	<b>Write enable:</b> Transfers commands, addresses, and serial data from the host system to the NAND Flash.
WP#	Input	<b>Write protect:</b> Enables or disables array PROGRAM and ERASE operations.
I/O[7:0] (x8) I/O[15:0] (x16)	I/O	<b>Data inputs/outputs:</b> The bidirectional I/Os transfer address, data, and command information.
R/B# R/B#2	Output	<b>Ready/busy:</b> An open-drain, active-low output that requires an external pull-up resistor. This signal indicates target array activity. For the 16Gb device, R/B# indicates the status of the first 8Gb of memory; R/B# indicates the status of the second 8Gb of memory.
V <sub>CC</sub>	Supply	<b>V<sub>CC</sub>:</b> Core power supply
V <sub>SS</sub>	Supply	<b>V<sub>SS</sub>:</b> Core ground connection
NC	–	<b>No connect:</b> NCs are not internally connected. They can be driven or left unconnected.
DNU	–	<b>Do not use:</b> DNUs must be left unconnected.

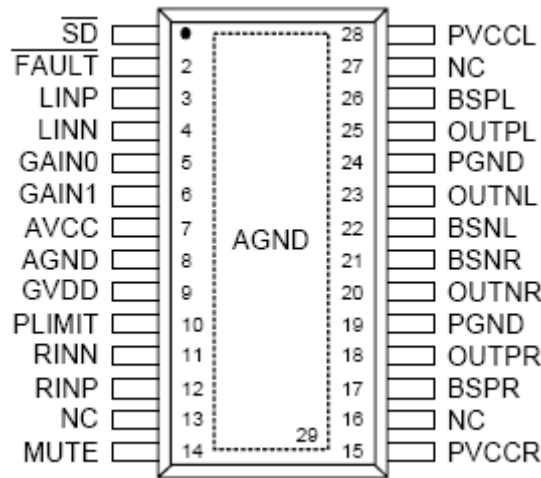
#### Block diagram





### 3-2-4 RT9108 brief introduction:

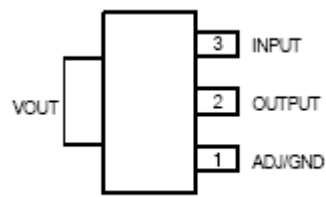
#### Pin introduction:



Pin No.	Pin Name	Pin Function
1	$\overline{\text{SD}}$	Shutdown Logic Input for Audio Amp (High = outputs enabled). TTL logic levels with compliance to AVCC.
2	$\overline{\text{FAULT}}$	Open-Drain Output for Short Circuit Fault Status. Short circuit faults can be set to auto recovery by connecting the FAULT pin to the $\overline{\text{SD}}$ pin.
3	LINP	Positive Audio Input for Left Channel. Biased at 2.3V.
4	LINN	Negative Audio Input for Left Channel. Biased at 2.3V.
5	GAIN0	Gain Select Least Significant Bit.
6	GAIN1	Gain Select Most Significant Bit.
7	AVCC	Analog Supply Input.
8, 29 (Exposed Pad)	AGND	Analog Ground. Connect to the thermal pad. The exposed pad must be soldered to a large PCB and connected to AGND for maximum power dissipation.
9	GVDD	High-Side FET Gate Drive Supply. Nominal voltage is 5V.
10	PLIMIT	Power Limit Level Adjustment.
11	RINN	Negative Audio Input for Right Channel. Biased at 2.3V.
12	RINP	Positive Audio Input for Right Channel. Biased at 2.3V.
13, 16, 27	NC	No Internal Connection.
14	MUTE	Mute Logic Input for Audio Amp (Low = outputs enabled).
15	PVCCR	Power Supply Input for Right Channel H-Bridge. Right channel and left channel power supply inputs are connected internally.
17	BSPR	Bootstrap I/O for Right Channel, Positive High-Side MOSFET.
18	OUTPR	Class-D H-Bridge Positive Output for Right Channel.

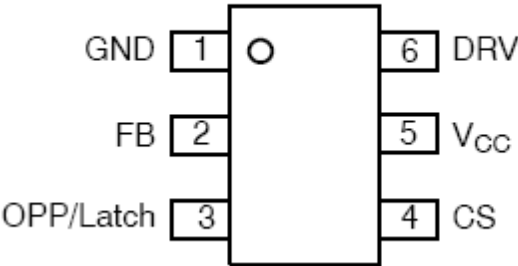
3-2-5 AMS1117-3.3 brief introduction:

Pin introduction:



3-2-6 NCP1251A Current-Mode PWMController for Off-linePower Supplies brief introduction:

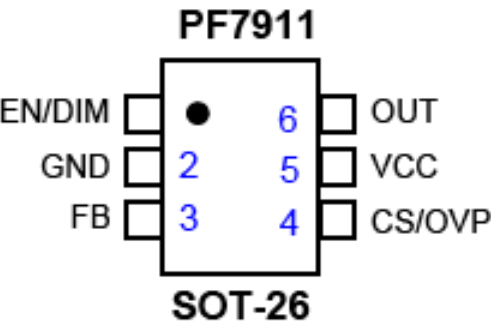
**PIN CONNECTIONS**



Pin N°	Pin Name	Function	Pin Description
1	GND	–	The controller ground.
2	FB	Feedback pin	Hooking an optocoupler collector to this pin will allow regulation.
3	OPP/OVP	Adjust the Over Power Protection Latches off the part	A resistive divider from the auxiliary winding to this pin sets the OPP compensation level. When brought above 3 V, the part is fully latched off.
4	CS	Current sense + ramp compensation	This pin monitors the primary peak current but also offers a means to introduce ramp compensation.
5	V <sub>CC</sub>	Supplies the controller	This pin is connected to an external auxiliary voltage and supplies the controller. When the V <sub>CC</sub> exceeds a certain level, the part permanently latches off.
6	DRV	Driver output	The driver’s output to an external MOSFET gate.

3-2-7 PF7911 High Voltage BoostController brief introduction:

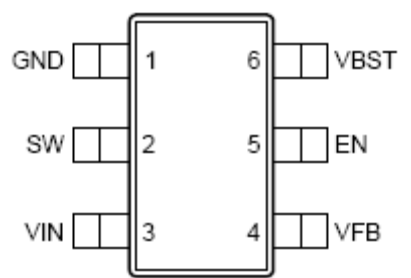
Pin introduction:



NAME	PIN No.	DESCRIPTION
EN/DIM	1	Dual Function Pin for Both Dimming Input Signal and Enable Input
GND	2	Ground
FB	3	Feedback Input, Connecting to the LED Current Sensing Resistor
CS/OVP	4	Dual Function Pin for Both Over Voltage Protection of Boost Output and & Current Sensing of Boost Switch.
VCC	5	Supply Voltage
OUT	6	Output to Drive Boost MOSFET

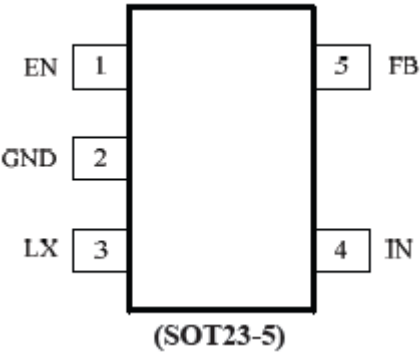
3-2-8 TPS563201 brief introduction:

Pin introduction:



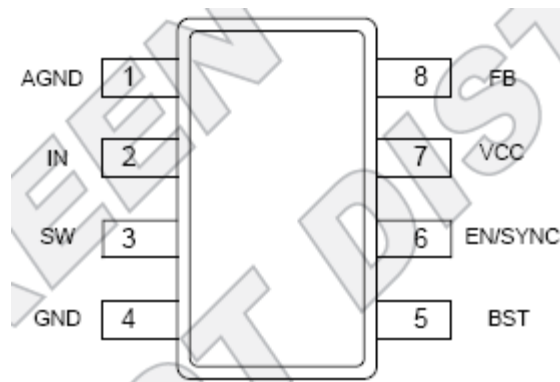
PIN		I/O	DESCRIPTION
NAME	NO.		
GND	1	—	Ground pin Source terminal of low-side power NFET as well as the ground terminal for controller circuit. Connect sensitive VFB to this GND at a single point.
SW	2	O	Switch node connection between high-side NFET and low-side NFET.
VIN	3	I	Input voltage supply pin. The drain terminal of high-side power NFET.
VFB	4	I	Converter feedback input. Connect to output voltage with feedback resistor divider.
EN	5	I	Enable input control. Active high and must be pulled up to enable the device.
VBST	6	O	Supply input for the high-side NFET gate drive circuit. Connect 0.1 $\mu$ F capacitor between VBST and SW pins.

3-2-9 SY8088 brief introduction:



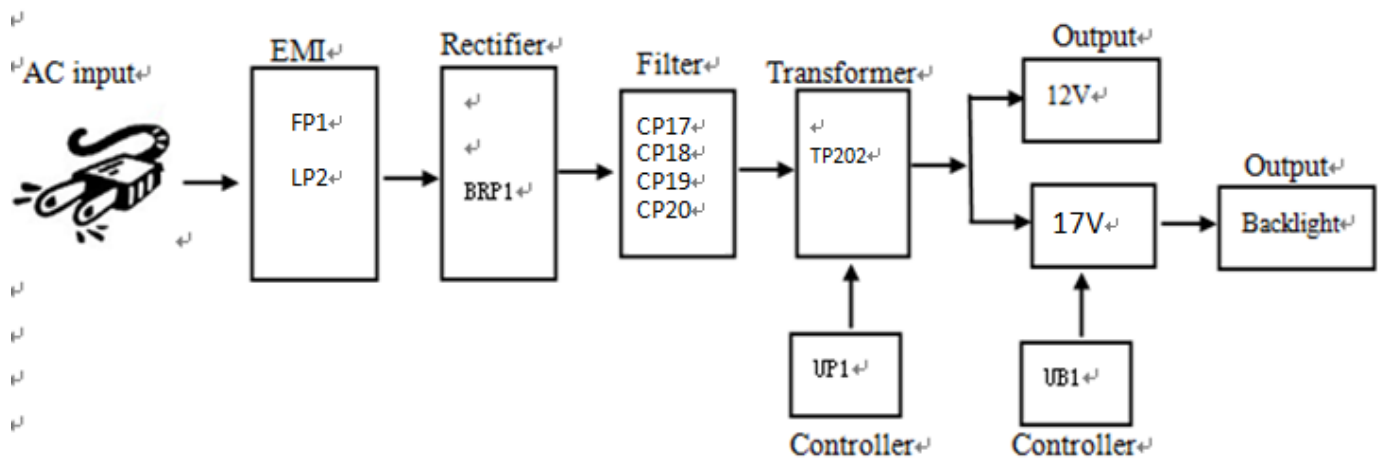
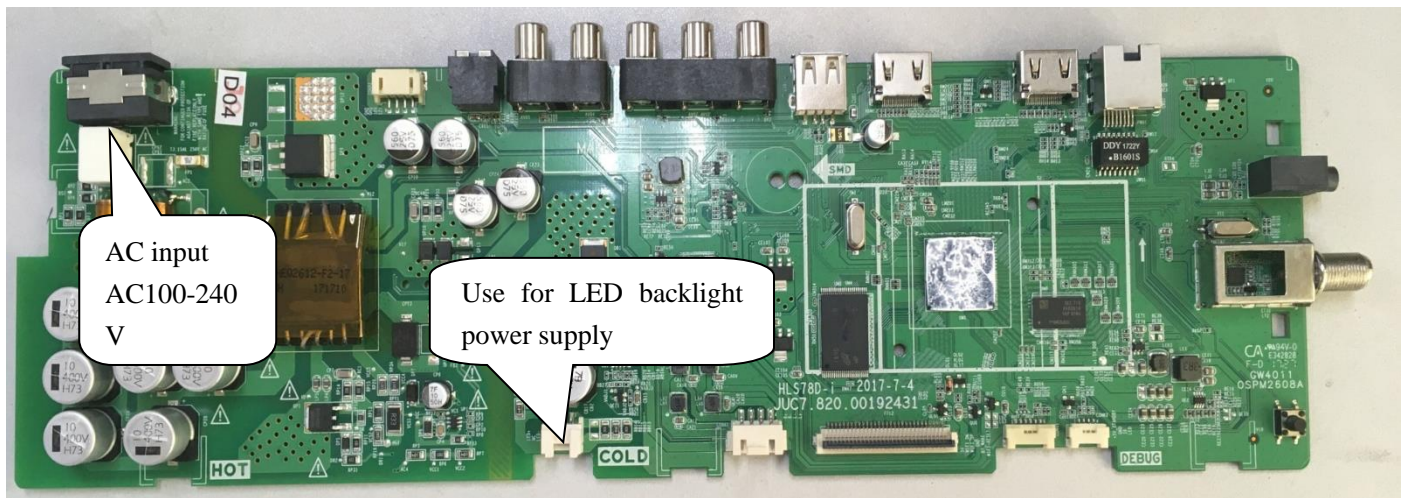
Pin Name	Pin Number	Pin Description
EN	1	Enable control. Pull high to turn on. Do not float.
GND	2	Ground pin.
LX	3	Inductor pin. Connect this pin to the switching node of the inductor.
IN	4	Input pin. Decouple this pin to the GND pin with at least 4.7uF ceramic capacitor.
FB	5	Output Feedback Pin. Connect this pin to the center point of the output resistor divider (as shown in Figure 1) to program the output voltage: $V_{OUT}=0.6*(1+R_1/R_2)$ . Add optional $C_1$ (10pF~47pF) to speed up the transient response.

3-2-10 MP2225GJ brief introduction:



Pin #	Name	Description
1	AGND	Analog Ground. Connect it to GND.
2	IN	Supply Voltage. The MP2225 operates from a 4.5V-to-18V input rail. Requires C1 to decouple the input rail. Connect using a wide PCB trace.
3	SW	Switch Output. Connect using a wide PCB trace.
4	GND	Power Ground. Requires special consideration during PCB layout. Connect to GND with copper traces and vias.
5	BST	Bootstrap. Requires a capacitor between SW and BST pins to form a floating supply across the high-side switch driver.
6	EN/SYNC	EN high to enable the MP2225. Can apply an external clock to the EN pin to change the switching frequency.
7	VCC	Bias Supply. Decouple with a 0.1uF-to-0.22uF capacitor.
8	FB	Feedback. Connect to the tap of an external resistor divider from the output to GND to set the output voltage. The frequency fold-back comparator lowers the oscillator frequency when the FB voltage is below 480mV to prevent current-limit run-away during a short-circuit fault condition.

### 3-3 Brief introduction of power supply



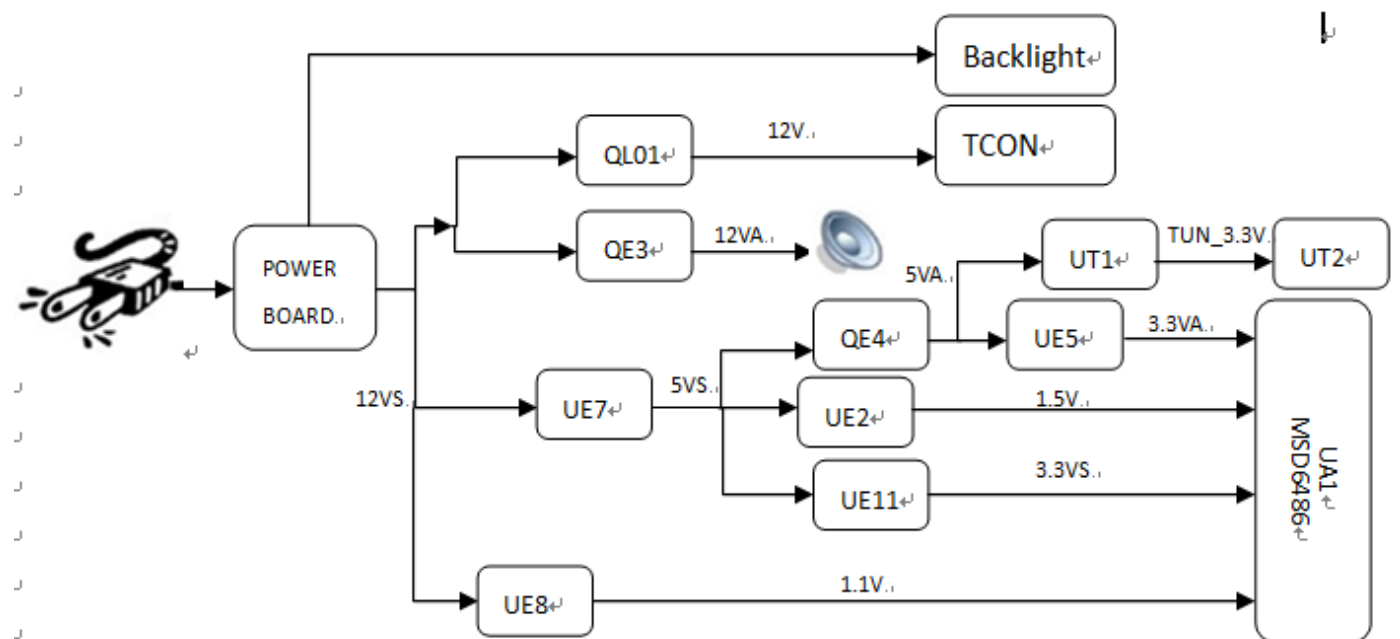
## Chapter4 MSD6486 Power Block Diagram, main board power supply

### systems, main board interface definition and the waveform of key points

This chapter mainly introduce the chassis frame diagram, power supply system, interface definition and each key point's wave shape.



## 4-1 MSD6486 Power Block Diagram

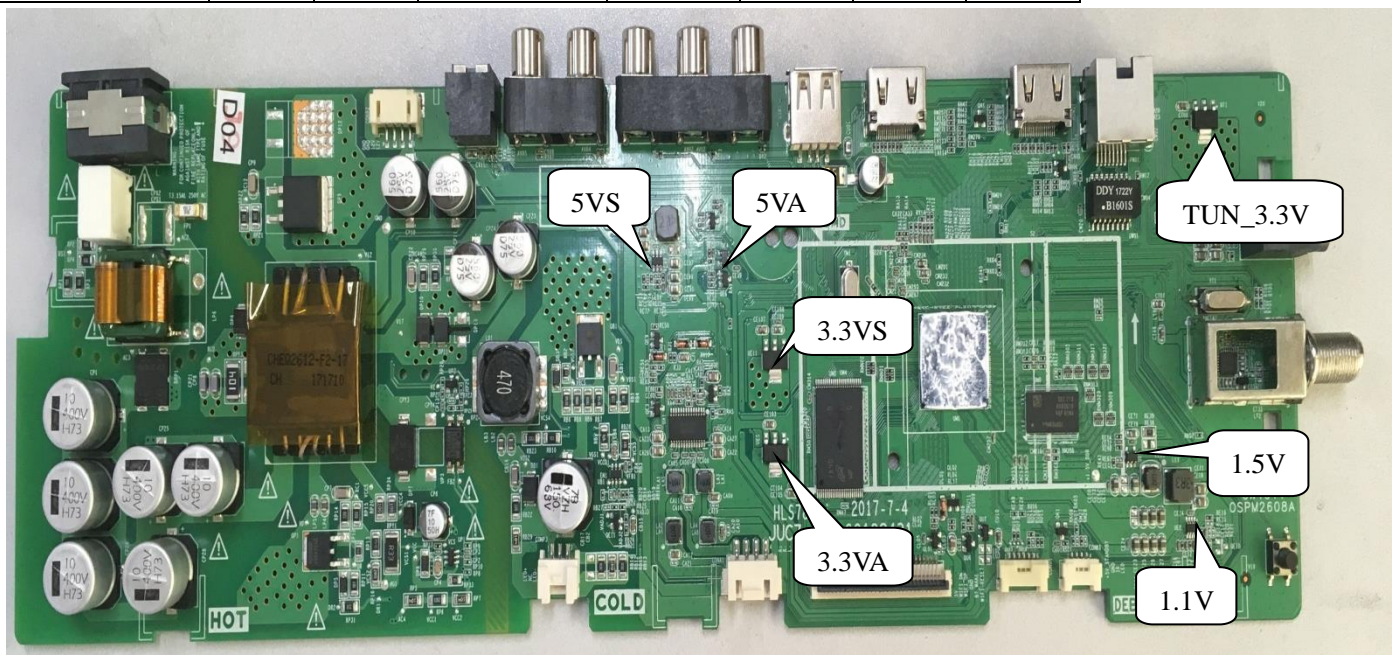


## 4-2 Power supply system

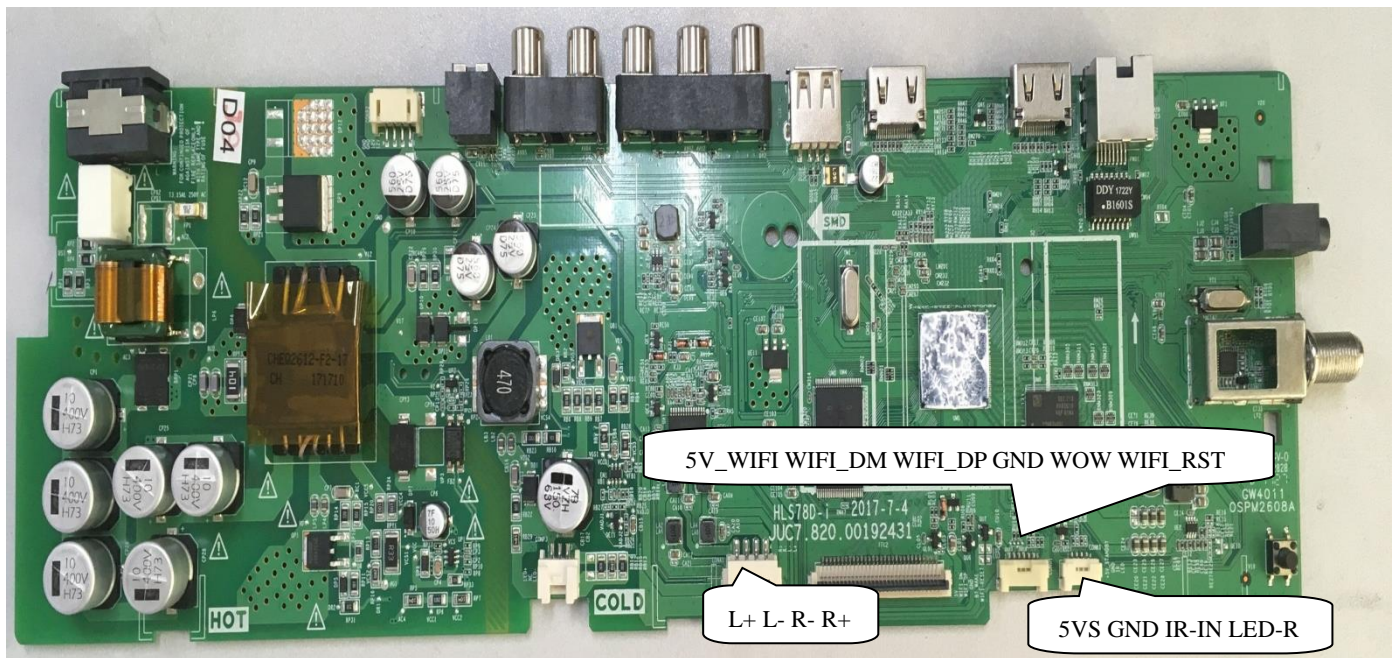
Power board has one kind of output voltage: +12VSTB.

4-2-1 Pin voltage of the voltage adjuster on the main board

item No.	UE7	QE4	UT1	UE5	UE11	UE2	UE8
output voltage	5VS	5VA	TUN_3.3V	3.3VA	3.3VS	1.5V	1.1V



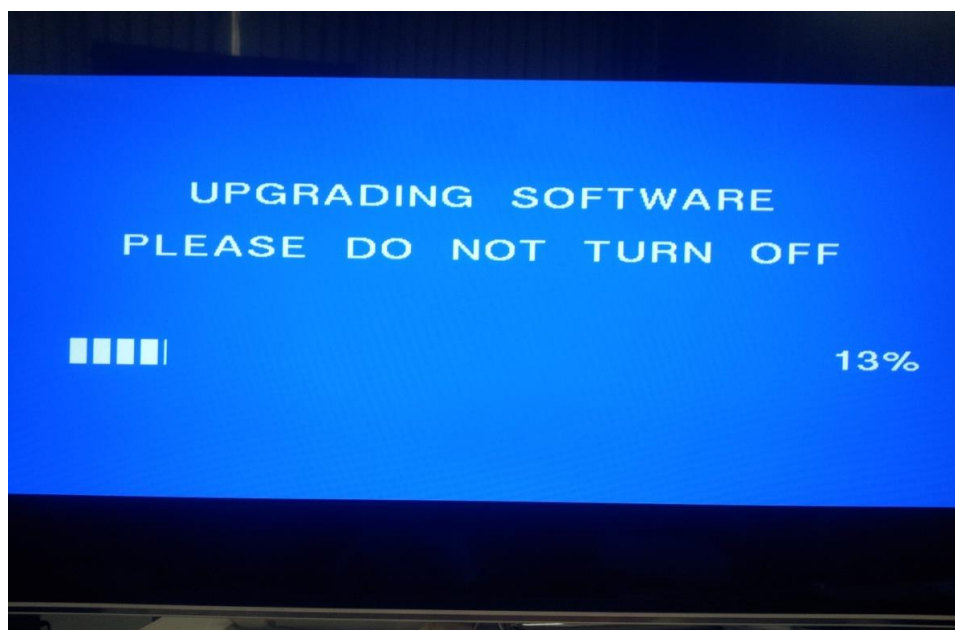
#### 4-2-2 Interface definition



### Chapter5 Software upgrade instructions

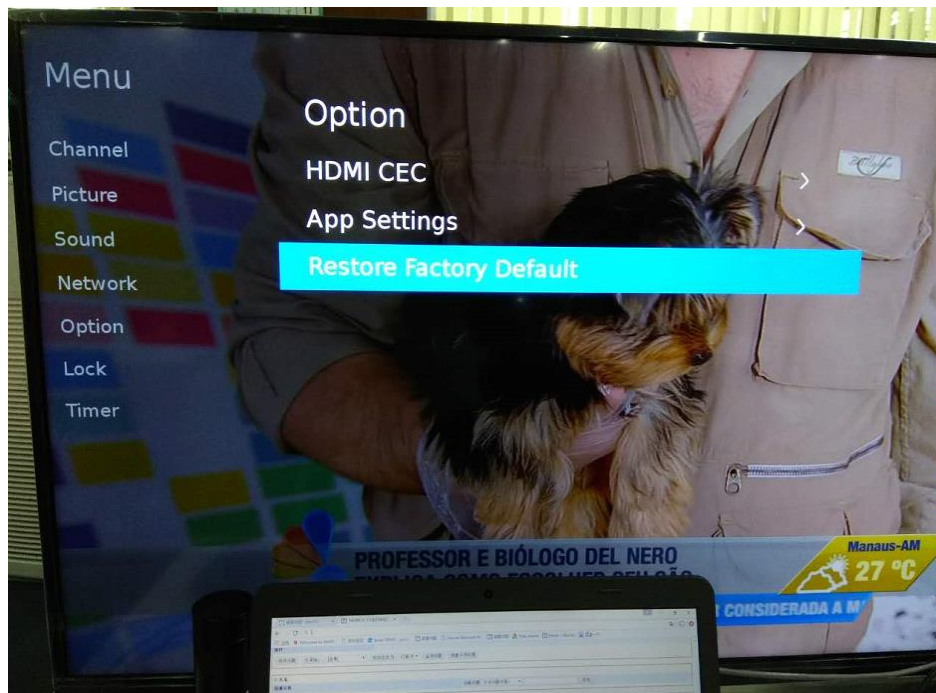
**Software upgrade method:** Use a U disk including the upgrade program directly upgrade

- 1)Download the bin file:To the root directory of your USB device (Do not change the filename "MstarUpgrade.bin");Then insert the USB device to USB interface of TV set.
- 2)Press the “ Setting - Option - Upgrade - USB Upgrade” button on key board, it will upgrade directly as follow.





9、3). When update finished, TV will automatically shut down and then boot. Then Set default the TV: Setting--->Option->Restore Factory Default.



## Chapter6: Classical accident maintenance procedures and examples

### 6-1 The thinking of don't boot

The power is not connected.

### 6-2 Common problems for your reference

To speed you to diagnose and solve problems, the following common problems are offered for your reference.

Symptoms	Possible Reason	Solutions
No picture, no sound, and no indicator light on	1.The power cord is not plugged in 2.The power is off	1.Plug in the power cord 2.Turn the power on
Abnormity Picture and sound with	1.Contrast, sharpness, and color are set improperly 2.Color system is improperly 3.Sound system is improperly	1.Adjust the numerical value of Contrast, sharpness, and color 2.Set the Color system to the country broadcasting standard 3.Set the Sound system to meet the country's broadcasting standard
Picture is spotted or with snow	Signal source is low-grade or the signal cord is in a lower quality	Use the qualified signal cord
No picture, no sound and indicator light is green	Contrast, brightness, color and volume are all in the minimum value or TV is in mute mode.	Adjust the value of contrast, brightness, color and volume
	The signal cable is not correctly connected.	Connect the signal cable correctly
Blue screen, AV or SVIDEO is displayed	There is no signal input or the video cable is not connected or incorrectly connected	Connect the video cable correctly

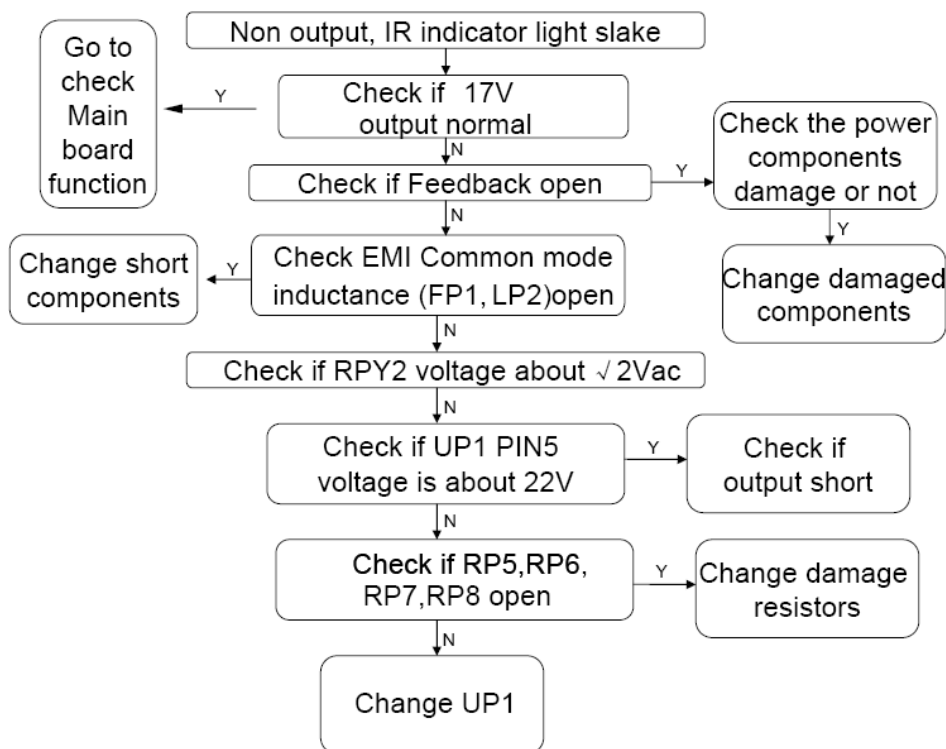


No sound	There is no audio signal input or audio cable is not connected correctly	Connect the audio cable correctly
VGA picture display with improper color	The color temp is adjusted incorrectly by user	Readjust the color temp, or select the original color setting
HDMI source, with snow pixel of full screen	The signal source is not normal	Plug the HDMI cable again
The remote control does not work	Batteries are improperly installed or exhausted	1. Make sure the positive and the negative polarities are correct. 2. Check if there is a loose contact between the batteries and the springs 3. Replace the batteries

## 6-3 Trouble shooting

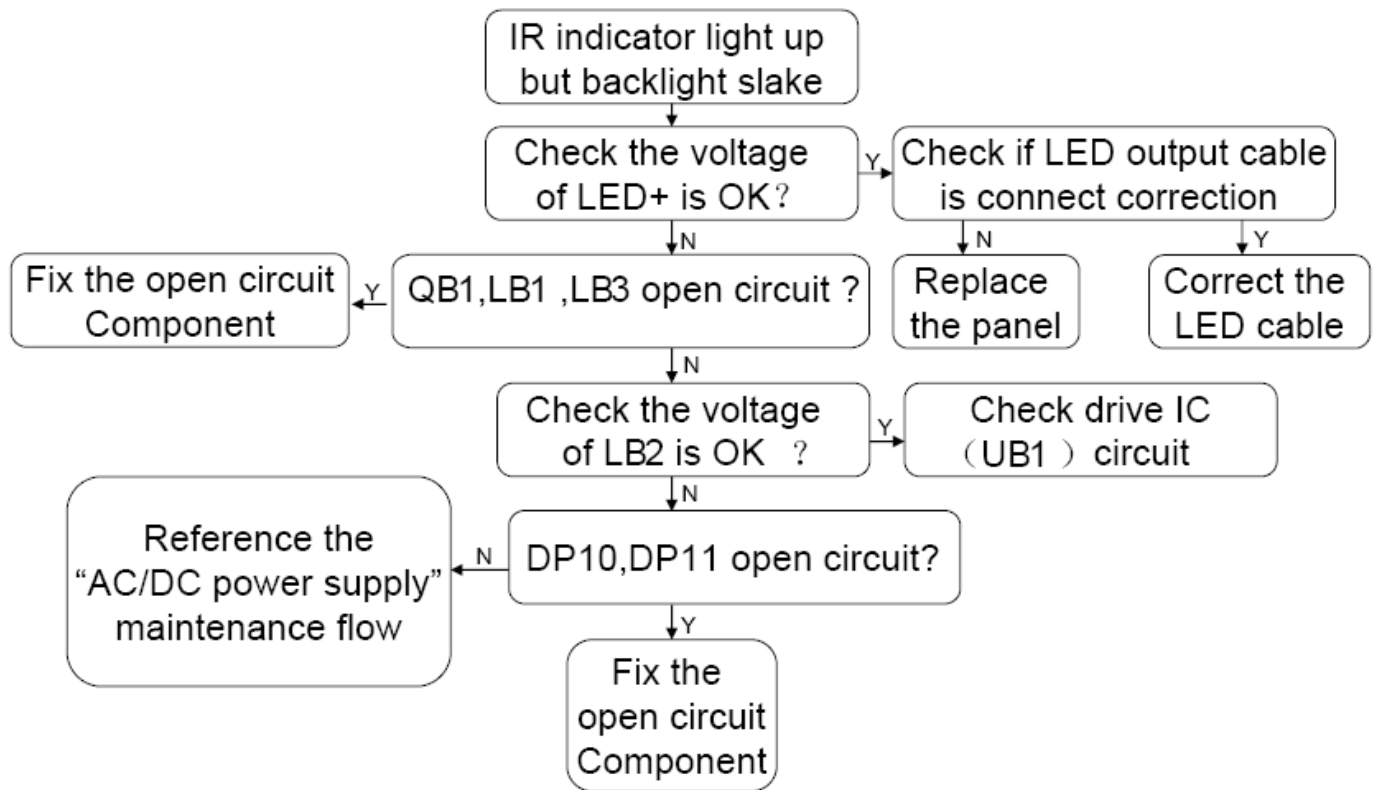
### 1、AC/DC power supply

#### 2.AC/DC power supply Service Guide

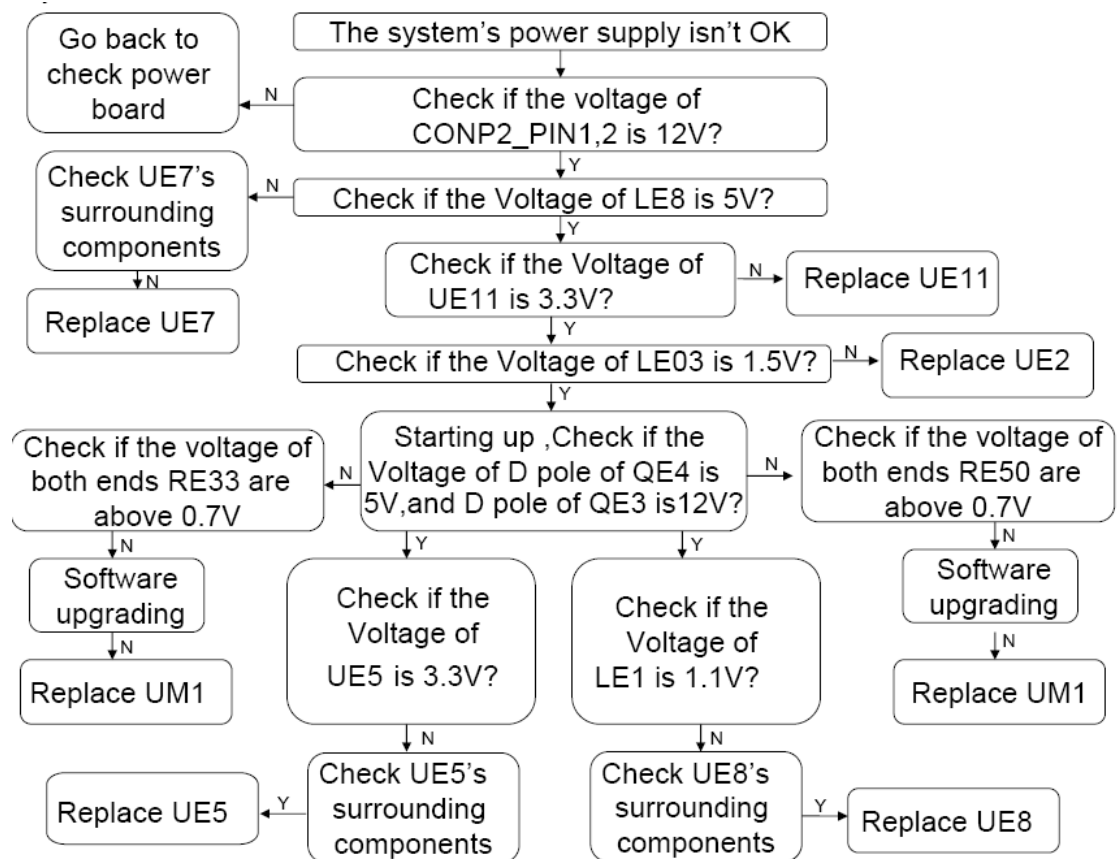


**WARNING:** For safety, please using isolation transformer for power supply when checking circuit of power. And keep GND connection normal.

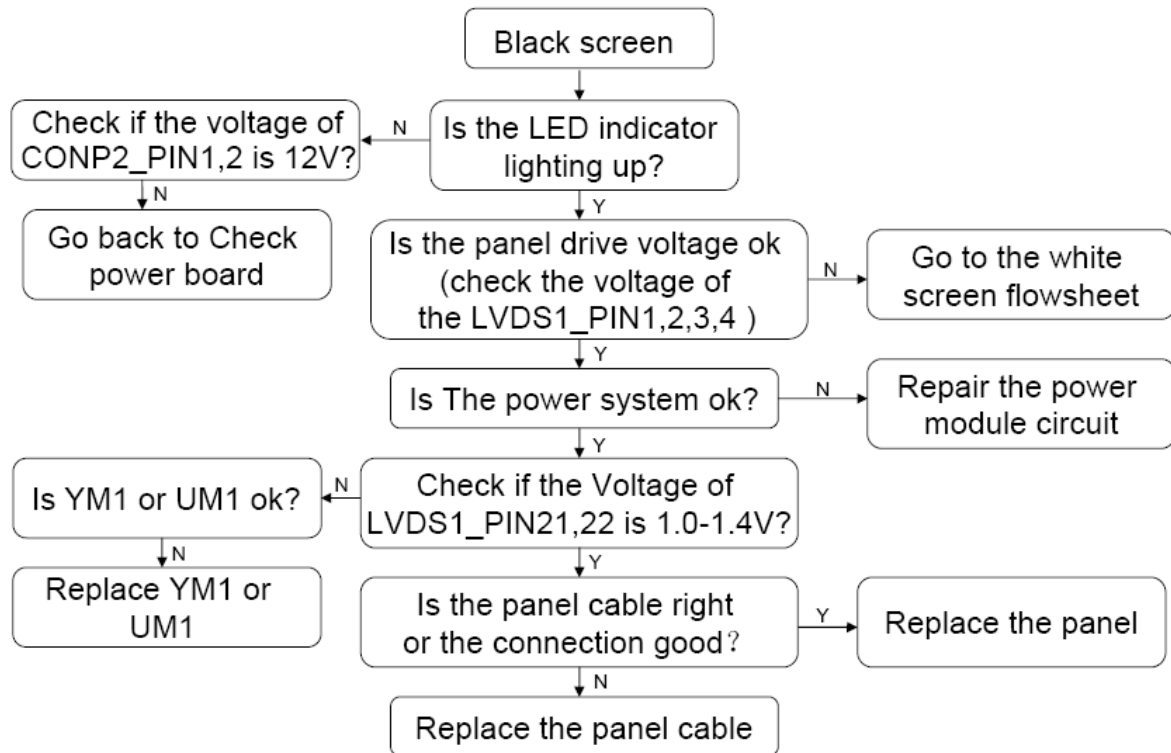
## 2、LED driver power supply



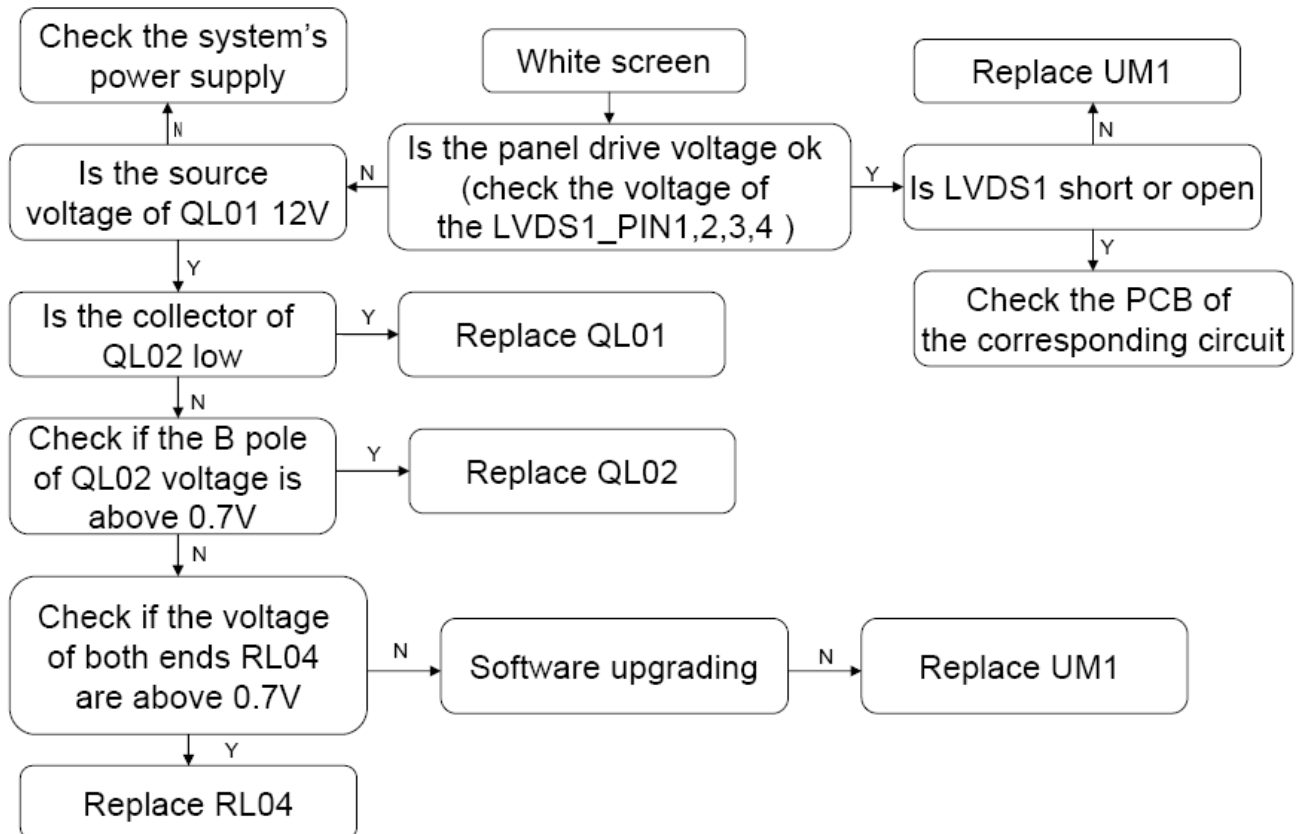
## 3、Power part



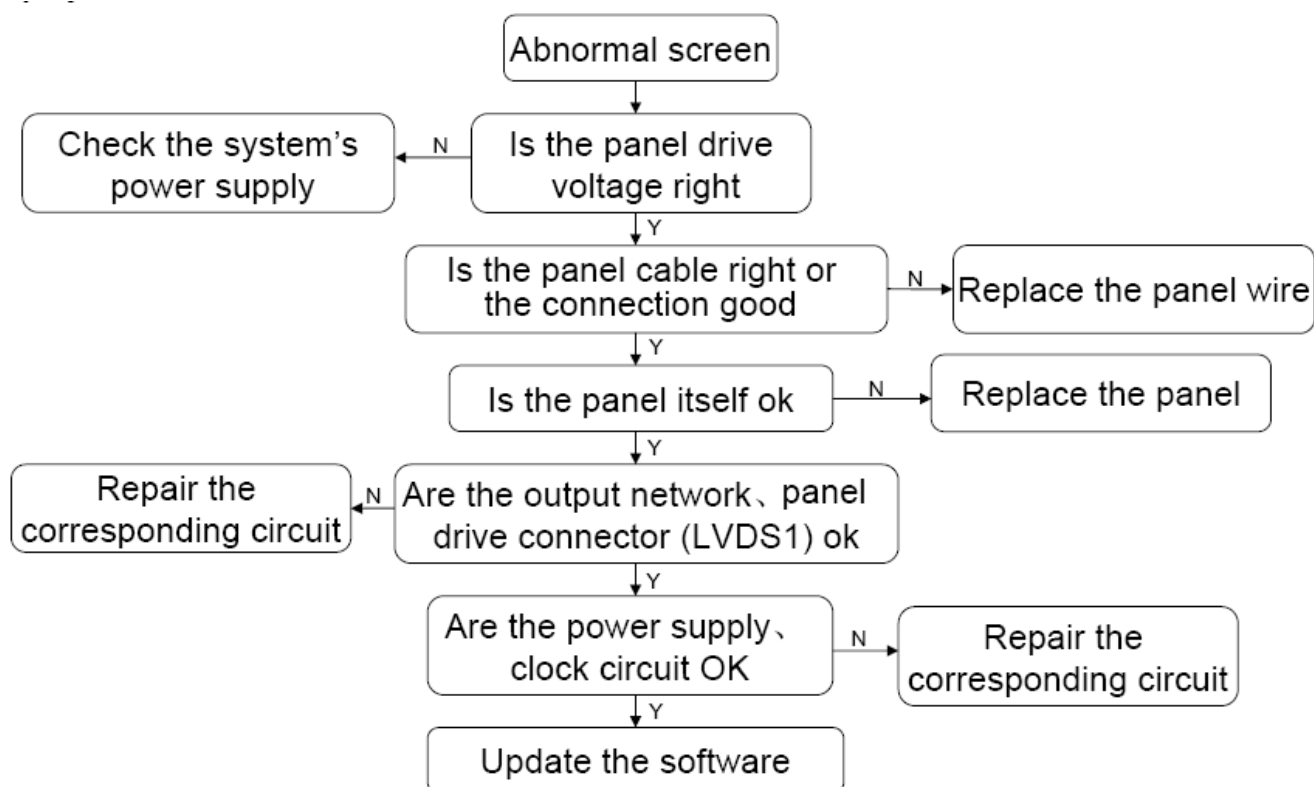
#### 4、Black Screen



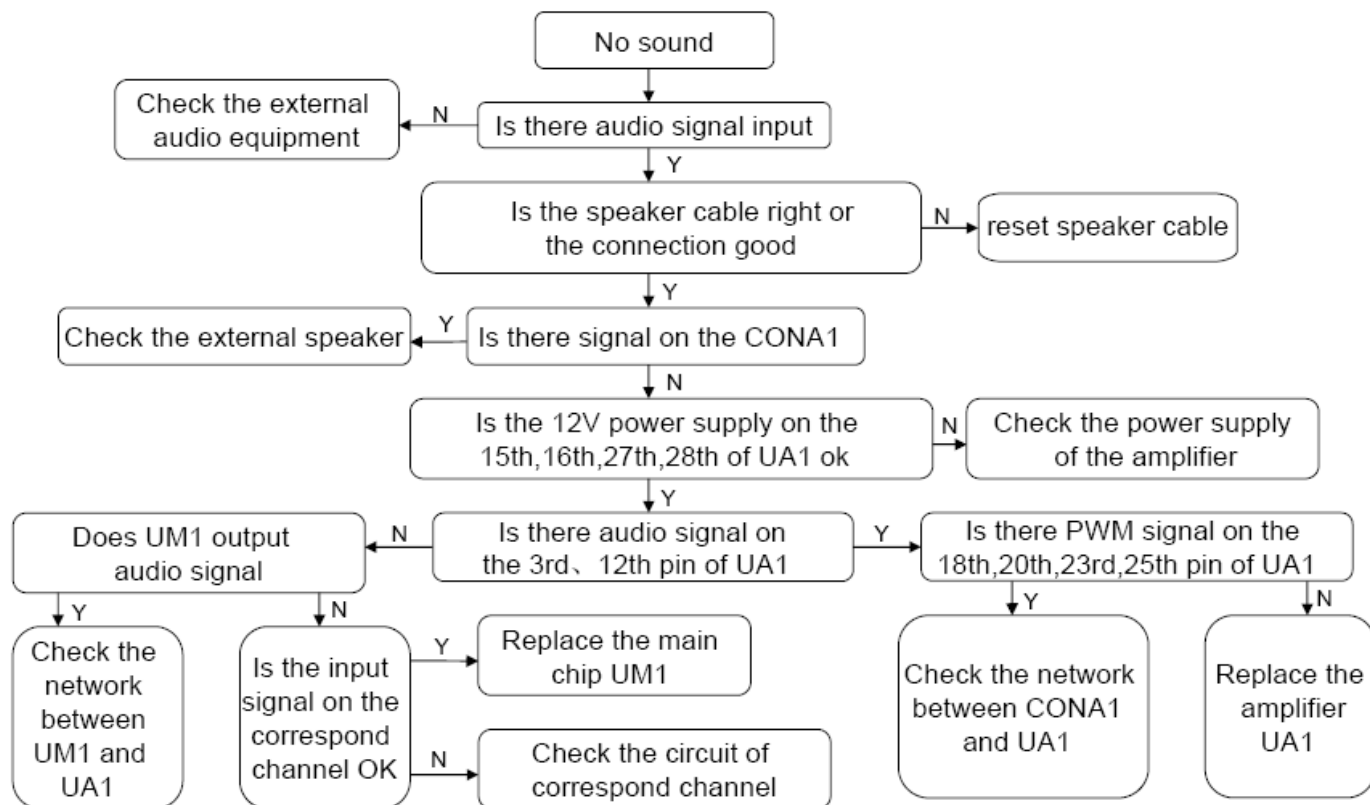
#### 5、white screen or AGP



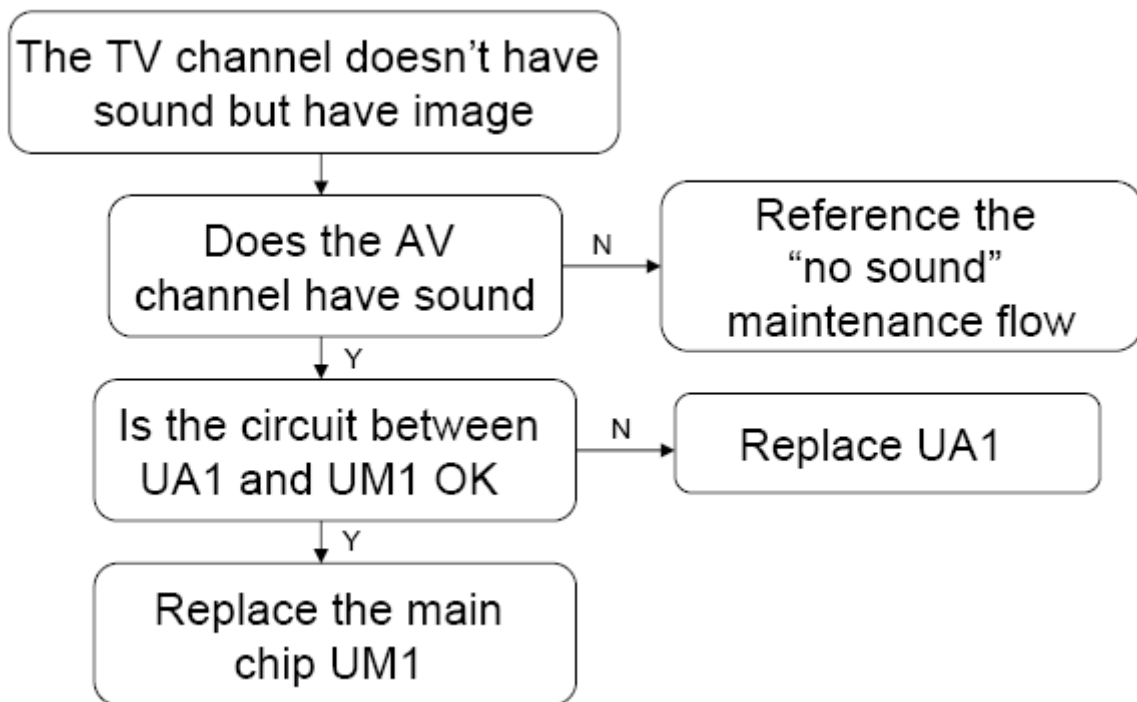
## 6、Abnormal screen



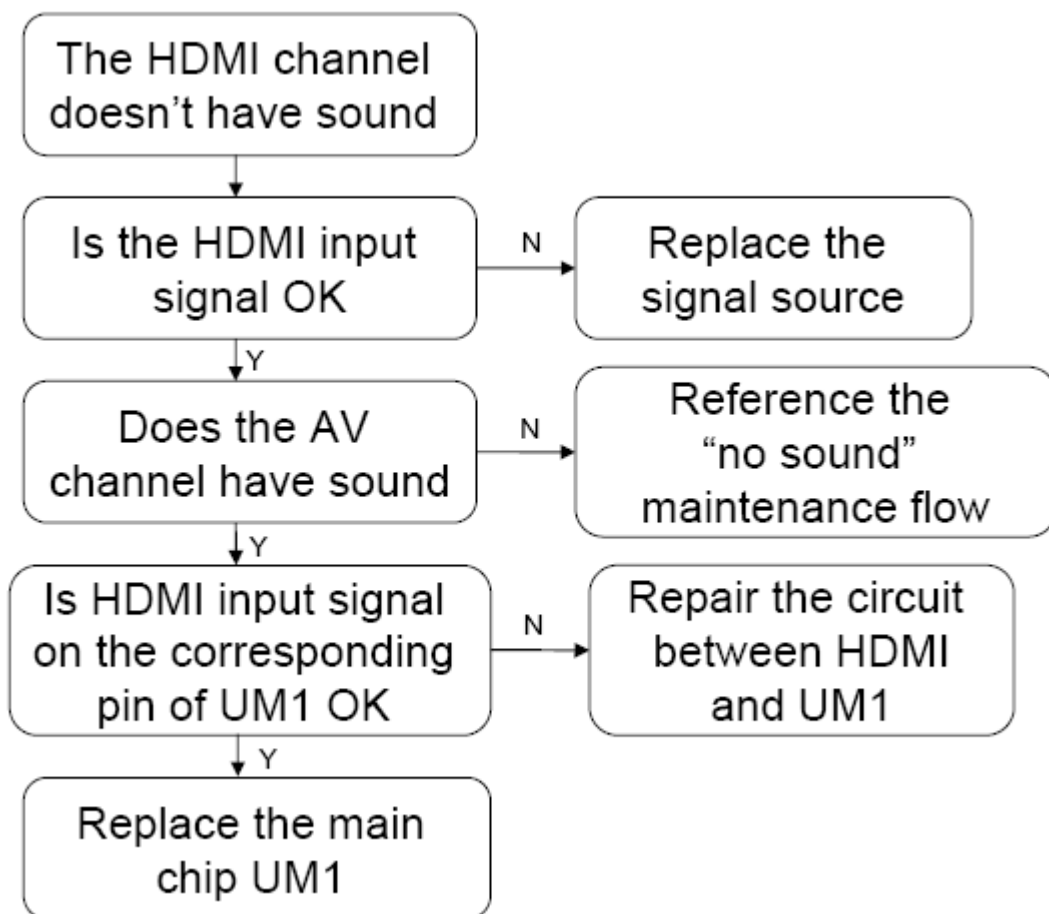
## 7、No sound



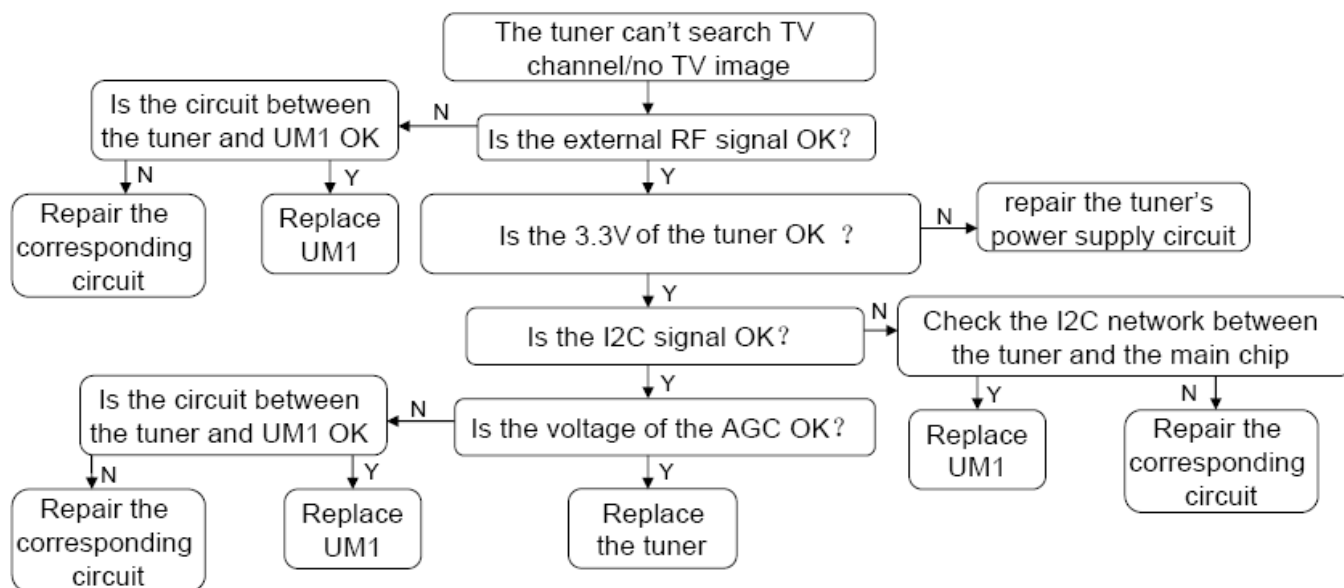
8、TV channel don't have sound



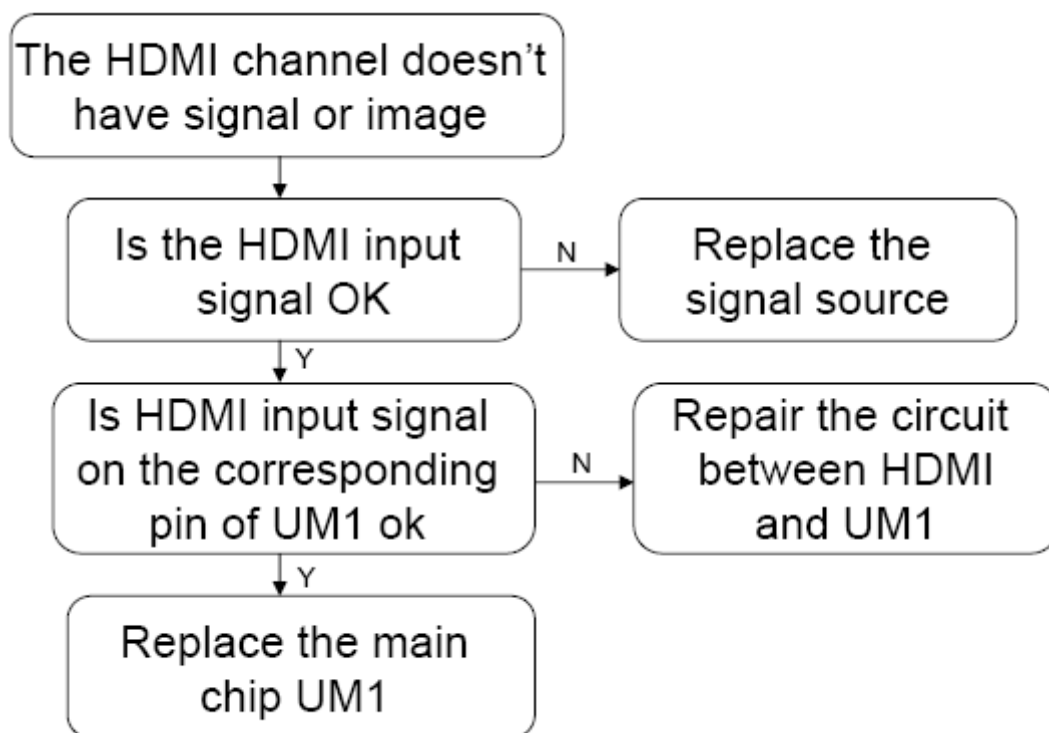
9、HDMI channel doesn't have sound



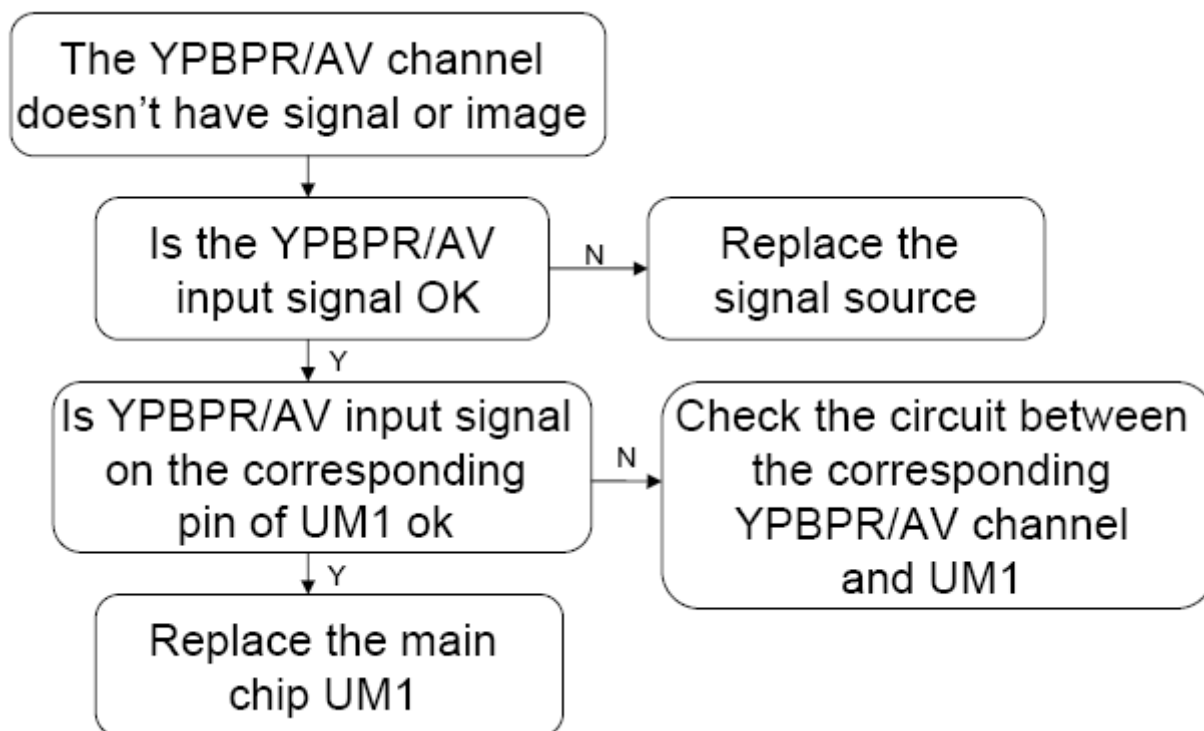
## 10、Function Part (TV failure)



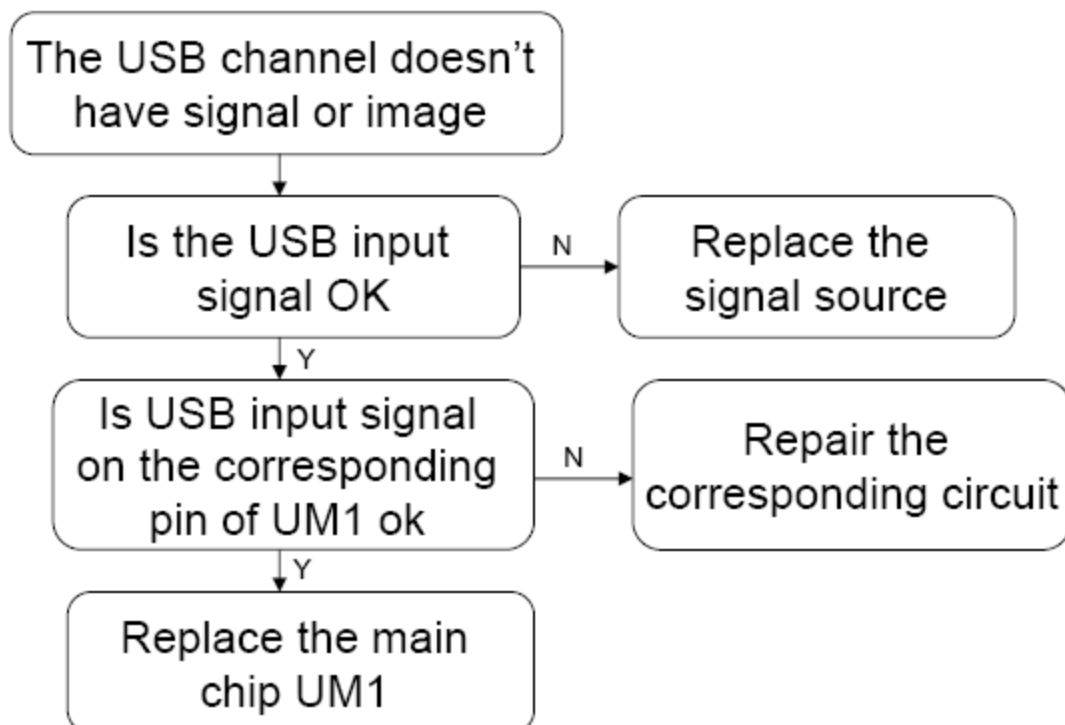
## 11、Function Part (the HDMI channel doesn't have image)



12、Function Part (the YPBPR and AV channel doesn't have signal)



13、Function Part (The USB channel doesn't have signal)



## Chapter7 Factory mode parameter setting instructions and notes

### 7-1 Enter into the factory mode

Switch on TV set, and make it works normally:

Press **【SETTING】** key on the remote control.

Press number keys “3”、“1”、“3”、“8” on remote control to enter password. Finish entering the factory mode.

If you want to quit the factory mode, Please. press **【EXIT】** key to exit source.

### 7-2 The list of factory mode as follow: (only for reference)

First-class navigation	Second-class navigation	Parameter values	Third-class navigation	Parameter values
Factory Setting				
ADC Adjust				
	Mode	PC-RGB		
	R-GAIN	1509		
	G-GAIN	1509		
	B-GAIN	1509		
	R-OFFSET	0		
	G-OFFSET	0		
	B-OFFSET	0		
	AUTO ADC	FAIL		
Customer setting				
	Gamma Table	0		
	Panel Setting	C32X16-E11-H_G0 1		
	Uart Enable	On		
	Aging Mode	Off		
	Ginga	Off		
	Power On Mode	Secondary		
	White Pattern	Off		
	WDT	On		
	PVR-RecordAll	Off		
W/B Adjust				
	Mode	DTV		
	TEMPERATURE	Medium		
	R-GAIN	1024		
	G-GAIN	1005		
	B-GAIN	1006		
	R-OFFSET	1024		
	G-OFFSET	1024		
	B-OFFSET	1024		



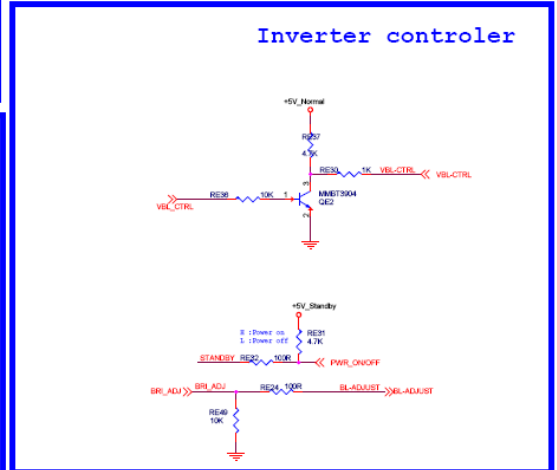
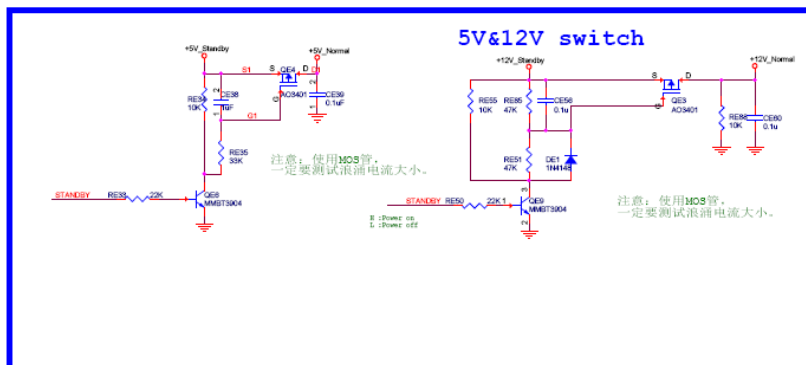
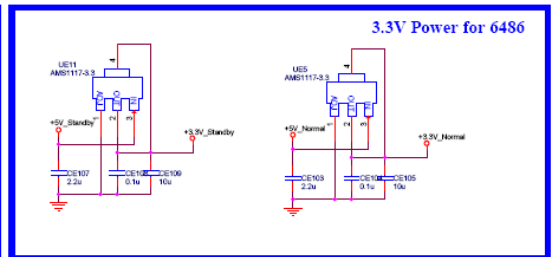
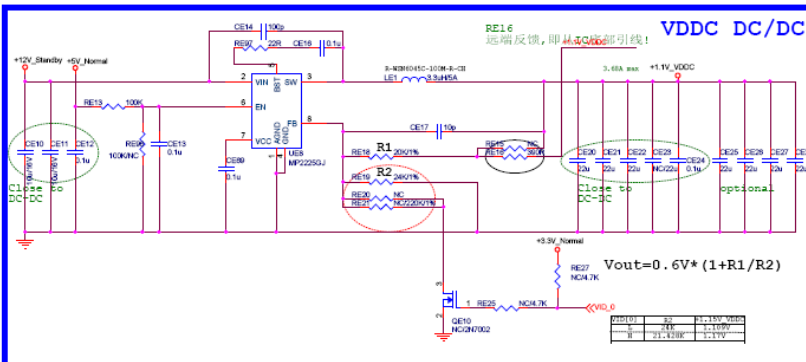
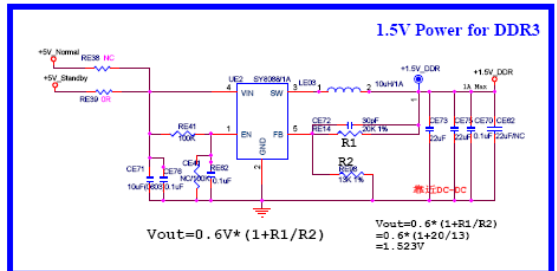
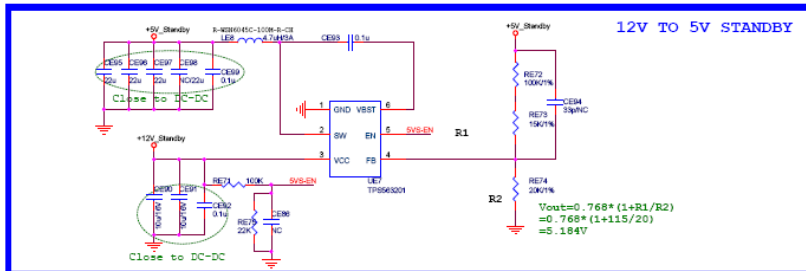
RESET ALL				
RESET SHOP				
Non-linear				
	MODE	Contrast		
	OSD0	50		
	OSD25	70		
	OSD50	89		
	OSD75	109		
	OSD100	128		
OverScan				
	Input Source	DTV		
	Left Crop	16		
	Right Crop	17		
	Up Crop	13		
	Down Crop	12		
SSC Setting				
	SSC MIU	Off		
	MIU0 Span(0.1KHz)	20		
	MIU0 Step(0.01%)	1		
	MIU1 Span(0.1KHz)	20		
	MIU1 Step(0.01%)	1		
	SSC LVDS	Off		
	LVDS Span	350		
	LVDS Step	200		
	LVDS Swing	350		
Others				
	UART BUS	OFF		
	AVD PARAMETER			
	White Balance ADJ	Off		
	DTV Log	Off		
	Key Upgrade Auto	Off		
	Key Upgrade Force	Off		
	Sound Setting			
			SP-Prescale	68
	PQ File...update	off		
Picture Mode				
	INPUT SOURCE	DTV		
	MODE	Standard		
	BRIGHTNESS	50		
	CONTRAST	100		
	COLOR	50		
	SHARPNESS	50		
	TINT	50		
SW INFORMATION				

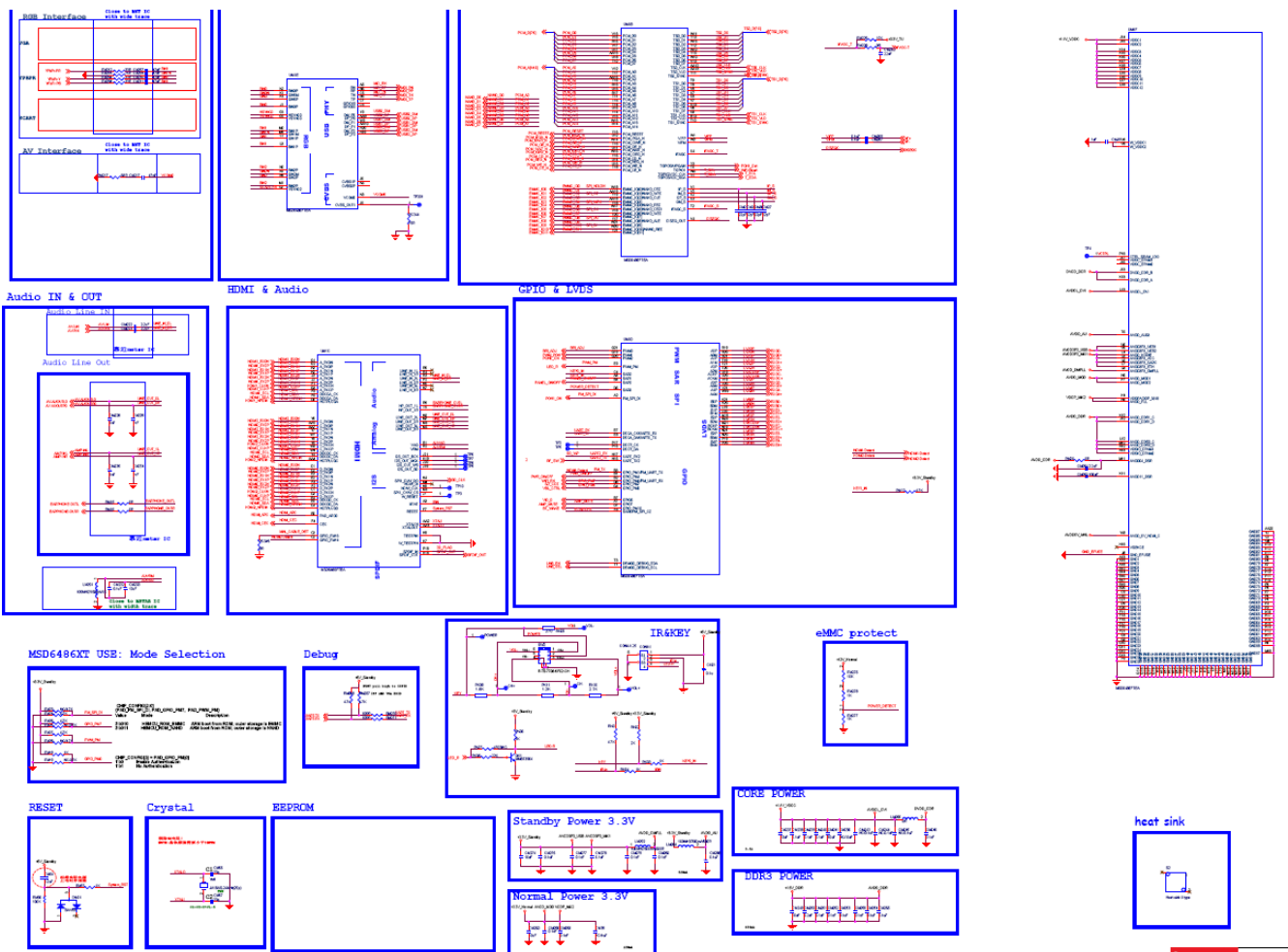
	CLVERSION:	360821		
	MBOOTVERSION			
	KERNELVERSION			
	MAPIVERSION	-. -		
	UTOPIABSPVERSION	Default		
	Backup Database			
	Restore Database			
Key info.				
	HDCP1key is OK!			
	HDCP2key is OK!			
	ETH MAC:00:6c:fd:ea:7e:5d			
	WIFI MAC:00:7e:56:7d:cc:f8			
	ESN is OK!			
	WideVine is OK!			
	Playready is OK!			
	USB ---			
	IP is Error!			
CH_HLS78D-iU_V1.0001_LED32UE3000(P)Ui(T)_C320X16-E11-H(G01)				

## Chapter8 Instructions of HLS78D-I module Circuit Schematic Diagram

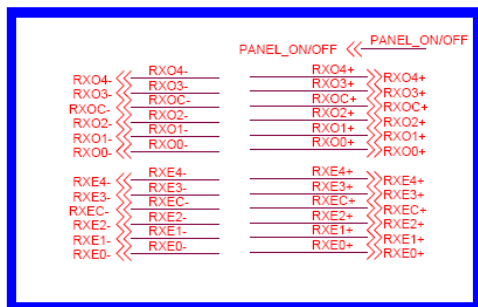
- 8-1 Clear NAND FLASH, and set the parameters after upgrading according to the upgrade instructions.
- 8-2 Check each channel/source to see if the image and sound are normal.
- 8-3 Circuit Schematic Diagram is Available separately as an attachment.

## Appendix: Circuit Schematic Diagram

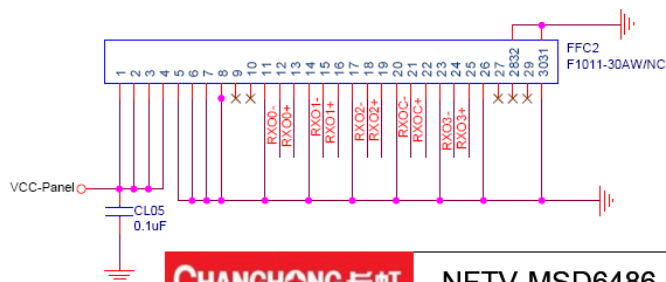
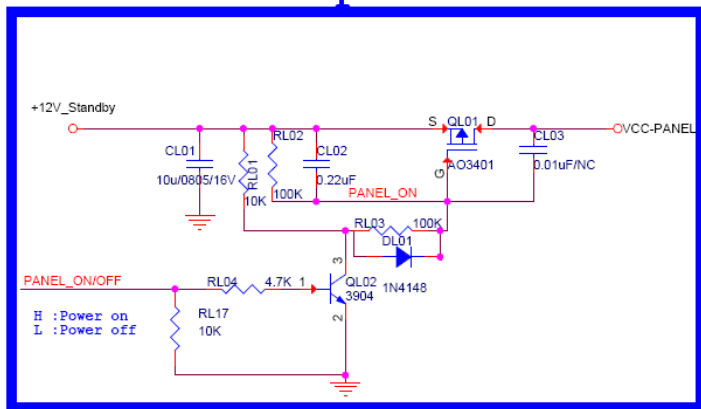




## Net

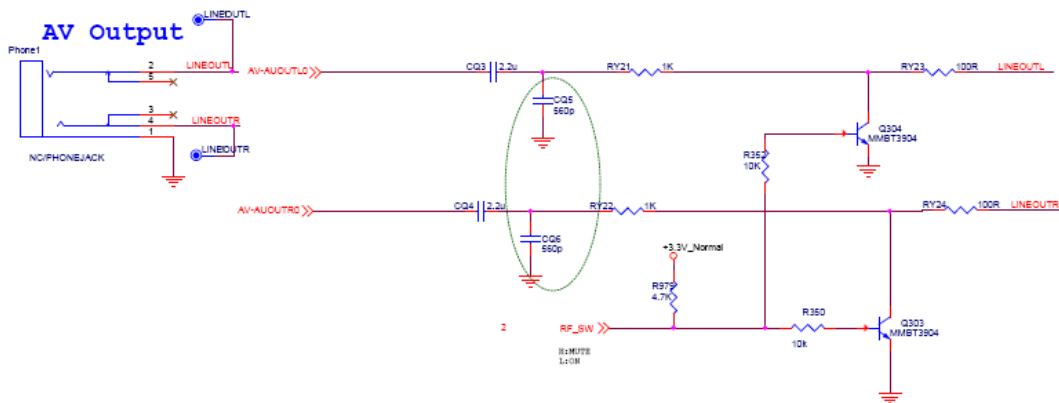
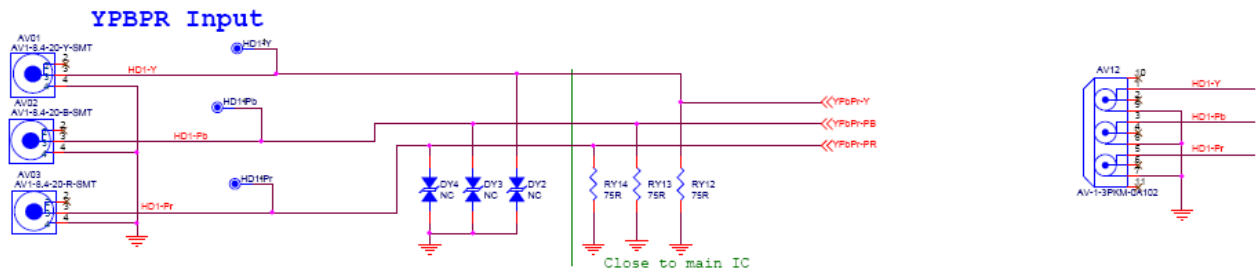
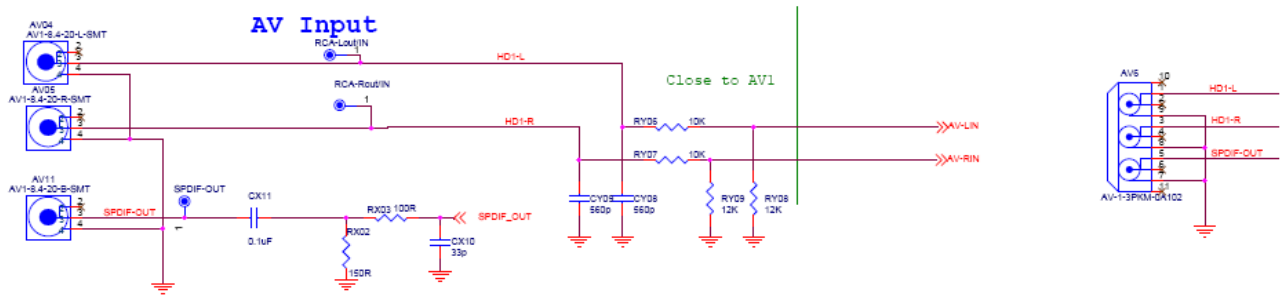


## Power for panel



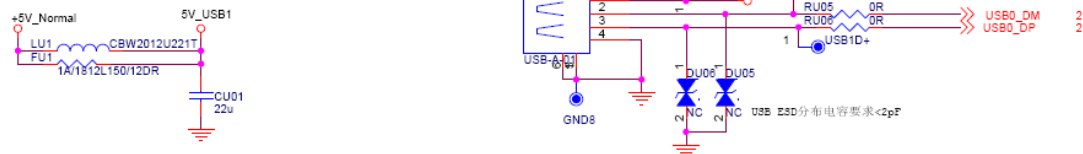
CHANGHONG 长虹 NDTV-MSD6486





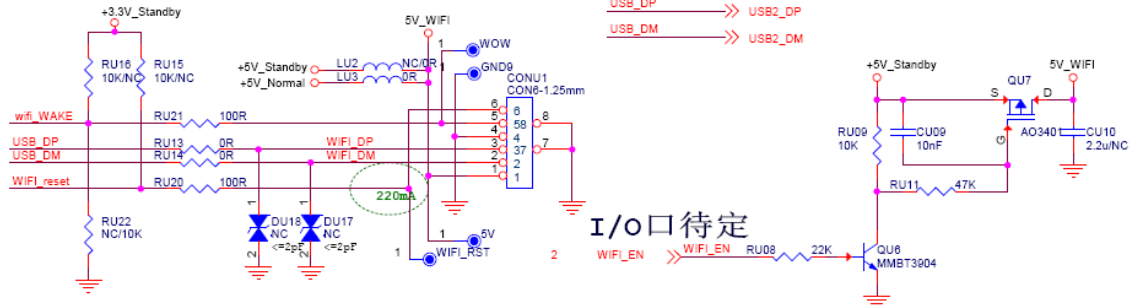
## USB

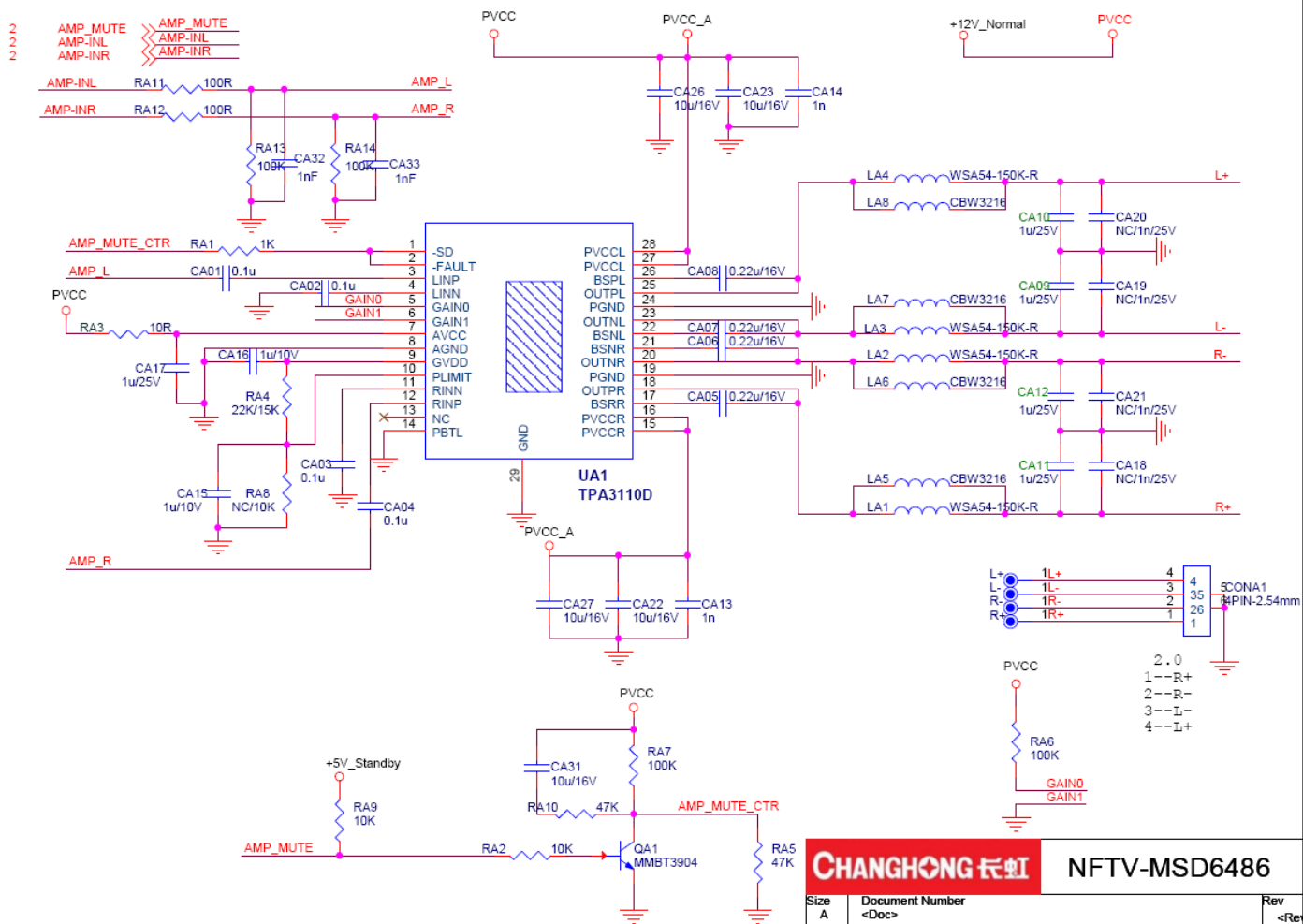
Note: 电容靠近USB端子, 提供usb读写稳定性



## WiFi

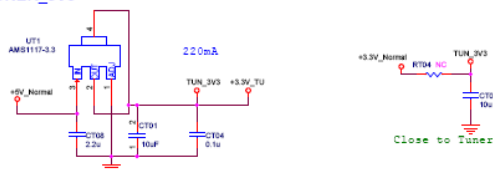
EQUAL LENGTH and  
DIFFERENTIAL  
IMPEDANCE 90ohm



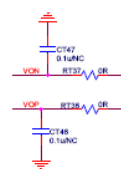


#### R842 power&module

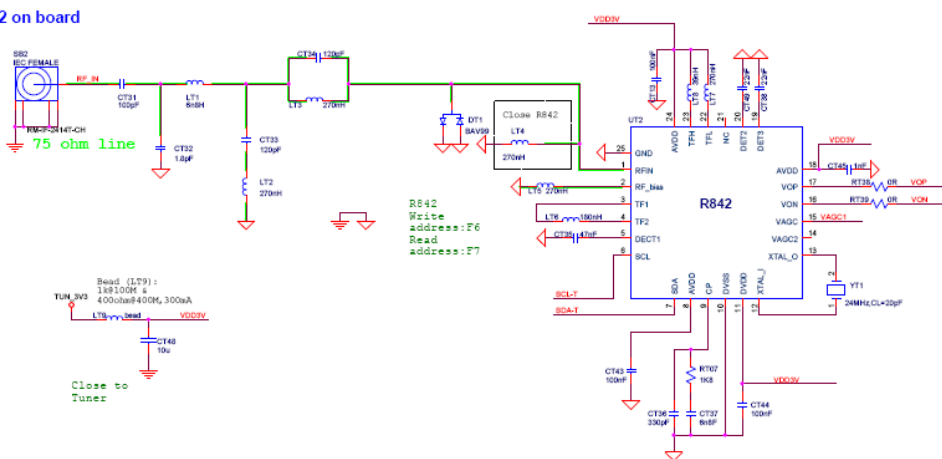
##### TUNER\_3V3



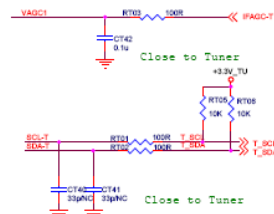
#### ISDB-T DIF



#### R842 on board



#### AGC RC Filter & I2C



#### RF switch control



EMMC\_POWER ~3.3V\_Nand=3.3V\_Normal

CM321 0.1uF

CM313 NC/0.1uF

CM300 NC/0.1uF

CM299 0.1uF

CM298 0.1uF

CM314 2.2uF

[illegible]