

Notas

Para ingresar al menú de service:

MENU->SONIDO->BALANCE->1-9-6-9>MENU

Para salir del menú de service, apagar desde el control remoto o presionar EXIT

Luego de realizar una actualización de firmware se recomienda hacer un reseteo desde el menú de service:

VALORES DE FAB.->FACTORY RESET-> CLEAR UNPROTECTLY

Paneles utilizados:

AUO T315HW04VD (ver archivos adjuntos)

Manual de instrucciones:

(ver archivos adjuntos)

Actualización del firmware por USB:

Grabar el archivo de actualización en el directorio raíz, de un pendrive u otro medio USB.

No cambiar el nombre ni la extensión del archivo provisto

Conectar el medio USB al LCD e ingresar al menú de service:

MENU->SONIDO->BALANCE->1-9-6-9>MENU

Ingresar a Factory Menu/Function/Actualizar firmware y presionar OK

Se mostrara un cartel en pantalla indicando la actualización en curso.
Luego, se recomienda reiniciar el LCD desde el menú de service, seleccionando la opción "Clean Unprotectly".

Menú de service

Factory Menu

White Balance
 Auto Calibrate
 Function
 Init
 Test Pattern
 Clear Protectly
 Clear Unprotectly

Function

TOFAC : M or U
 Power Mode Save
 Software Update
 LOGO: Hisense
 OSD language: English
 PVR RECORD ALL OFF
 WDT: ON
 UART Debug : None

White Balance

RDRV:
 GDRV:
 BDRV
 RCUT:
 GCUT:
 BCUT:
 TEMPERATURE: Normal

3.2.2、Design Menu

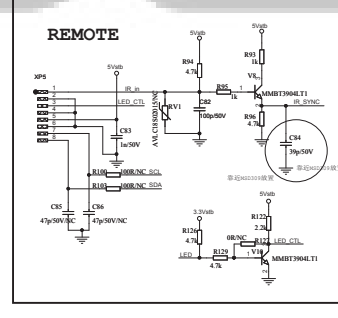
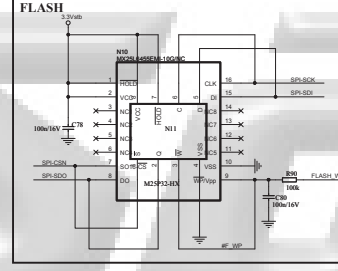
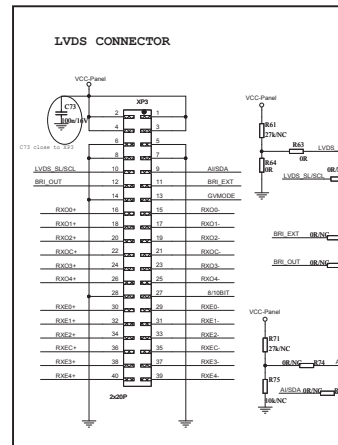
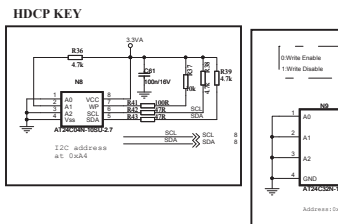
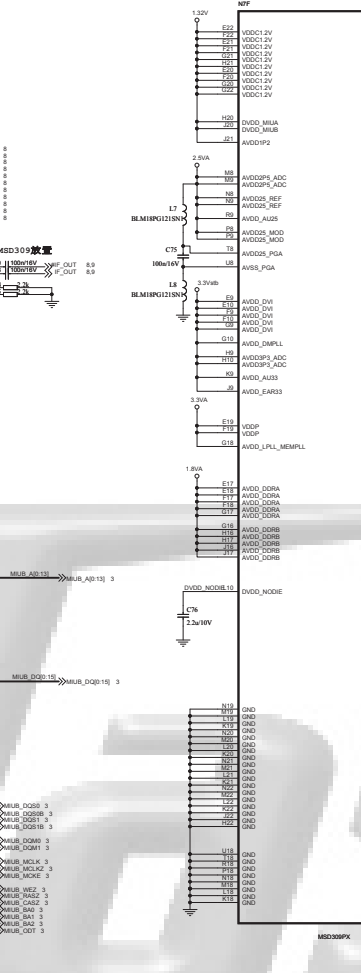
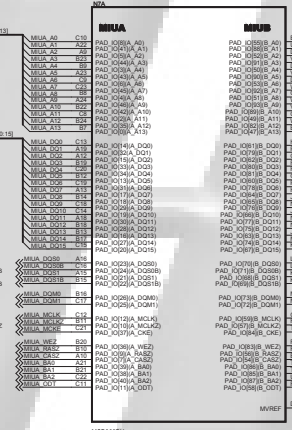
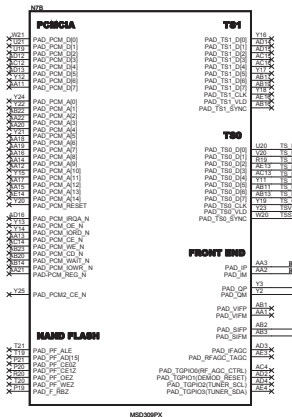
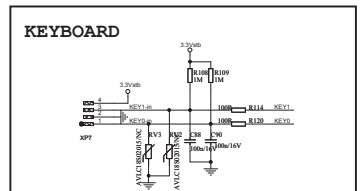
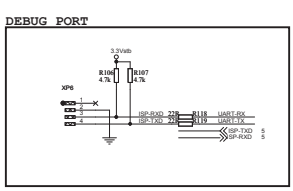
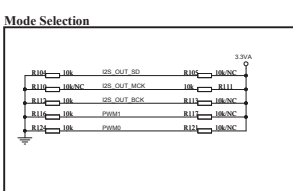
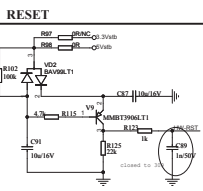
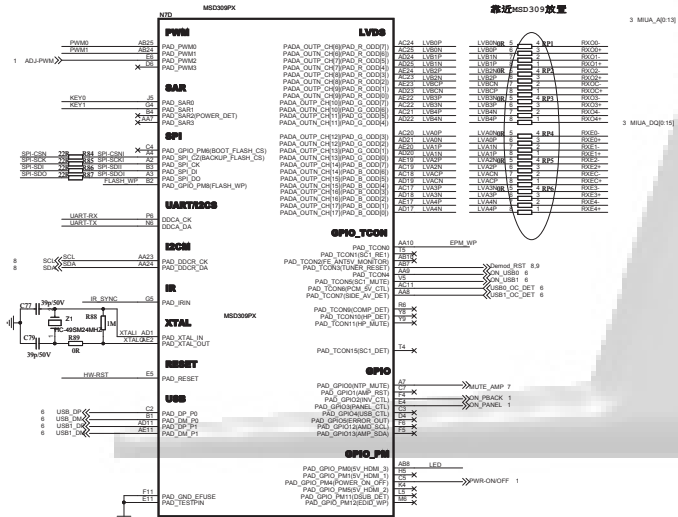
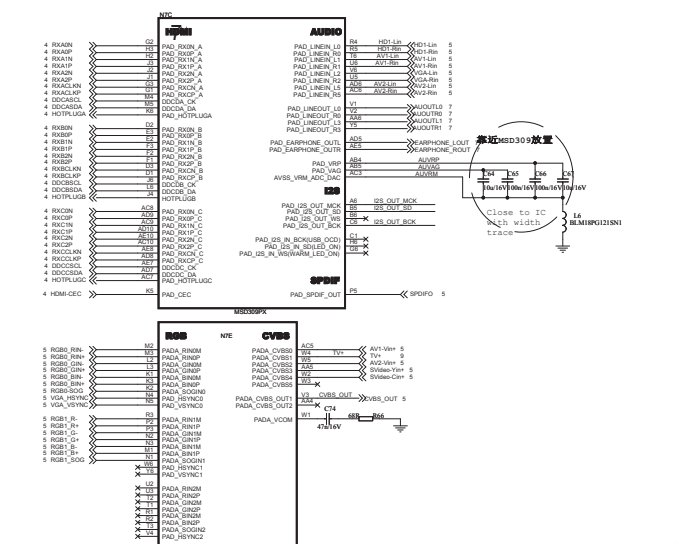
Design Menu

Picture Mode
 Audio Mode
 Picture Curve
 Audio Curve
 SSC Adjust
 Saving Mode
 Overscan
 Not Stand

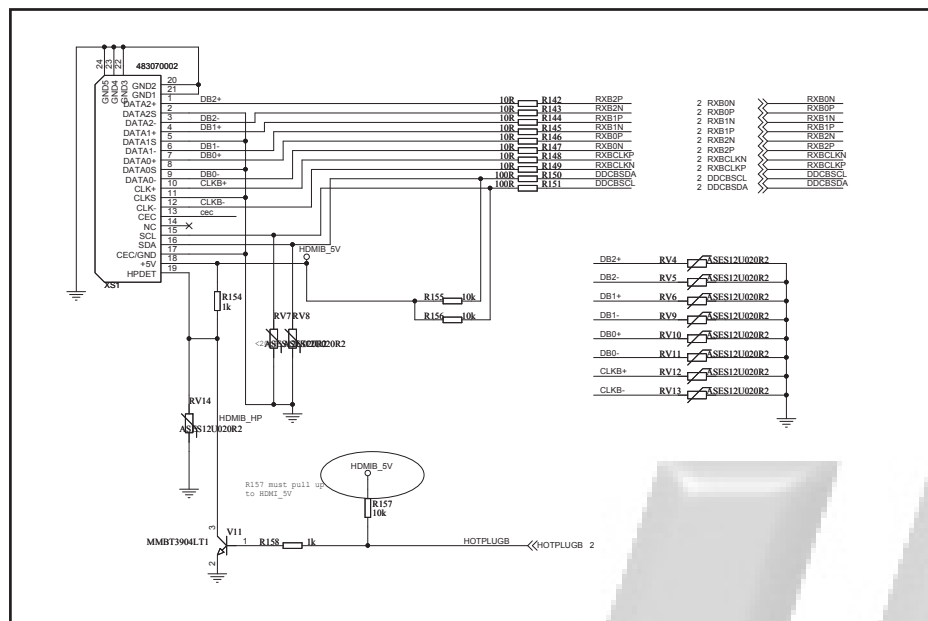
B
C
D
E
F
G
H
I
J
K
L
M
N
O
P
Q
R
S
T



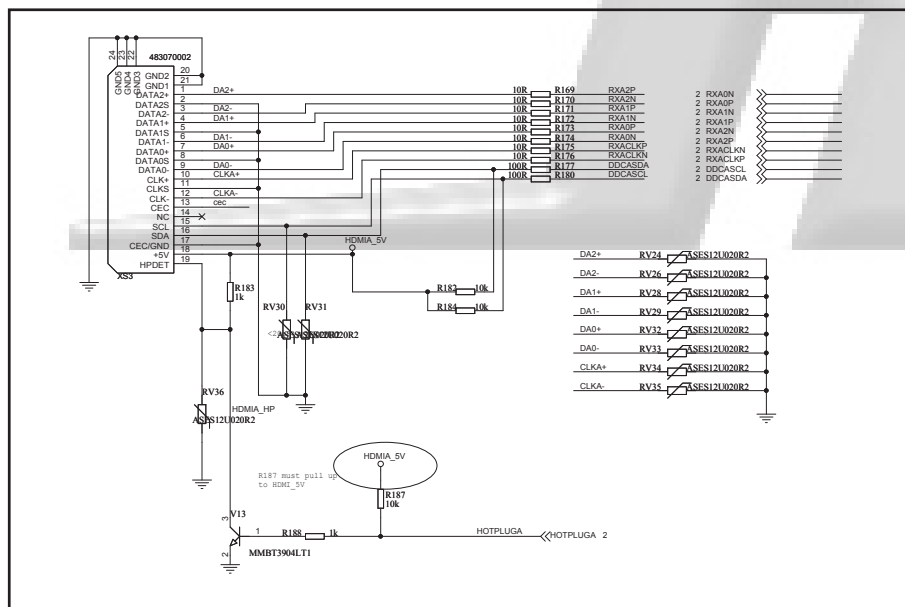




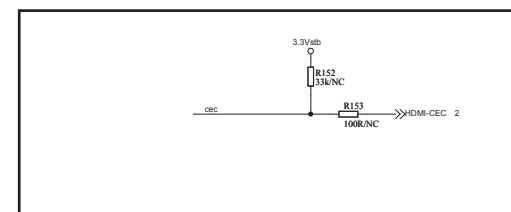




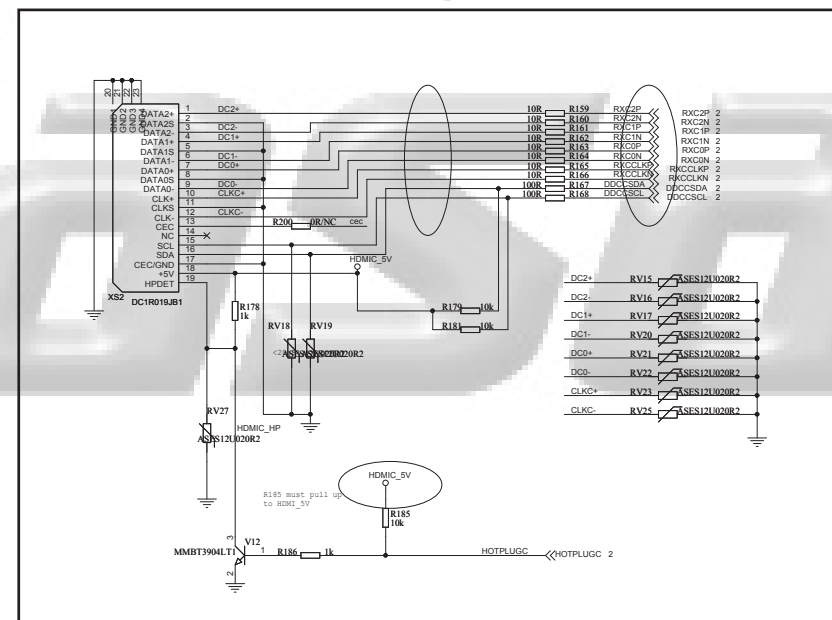
HDMI Input 3 立式



HDMI CEC



HDMI Input 1 卧式



[illegible]

AV1+SVideo

XS10A

Vin 1 AV2-V

Cin 2

DASW-02D

XS10B

Cin 5 SVideo-C

W 7

Yin 3 SVideo-Y

4

GND 6

GND 8

GND 9

XS11

1 AV2-L

2 AV2-R

3 GND

AV2-V

RV55

1K8S02015

C133 100pF/50V

R246 33R

R249 75R

C136 47nF/16V

AV2-Vin+ 2

SVideo-Y

RV58

1K8S02015

C137 200pF/50V

R253 75R

SVideo-Yin+ 2

SVideo-C

RV59

1K8S02015

C139 200pF/50V

R255 75R

C138 47nF/16V

SVideo-Cin+ 2

CLOSED TO NED303

AV2-L

RV60

1K8S02015

R256 10K

C141 22uF/50V

AV2-Lin 2

AV2-R

RV61

1K8S02015

R261 12K

C145 200pF/50V

C143 22uF/10V

AV2-Rin 2

close to 309

COMPONENT

The schematic diagram illustrates the component section of a circuit board. It features an AV3-LYP integrated circuit (IC) with pins 1 through 9 labeled. The IC is connected to various components, including resistors (R230, R231, R233, R239, R240, R241, R244, R250, R251, R247), capacitors (C128, C130, C134, C135), and diodes (RV50, RV51, RV32, RV234, RV237, RV56, RV57). The diagram also shows signal traces for HD1-L, HD1-R, HD1-Lin, and HD1-Rin. The components are labeled with their respective values and part numbers, such as R230 (0R C118), R231 (33R C119), R233 (33R C121), R239 (33R C121), R240 (68R C124), R241 (68R C125), R244 (10k), R250 (10k), R251 (12k), C128 (2.2u/10V), C130 (200p/50V/NC), C134 (2.2u/10V), and C135 (200p/50V/NC). The diagram is labeled with 'CLOSED TO M8D309' and 'close to 309'.

The schematic diagram illustrates the internal circuitry of the AV2 module and its connections to the AV1 module. On the left, the AV2 module is represented by a block with pins X8, X9, and X10. Pin X8 is connected to AV1-V, X9 to AV1-L, and X10 to AV1-R. The internal circuit includes three input buffers (RV49, RV53, RV54) connected to the AV1-V, AV1-L, and AV1-R lines, respectively. Each buffer is followed by a 10kΩ pull-down resistor (R242, R243, R245) and a 100pF/50V/NC capacitor (C126, C127, C132). The AV1-V line is also connected to a 33R resistor (R232) and a 47nF/16V capacitor (C120). The AV1-L and AV1-R lines are connected to 12kΩ resistors (R243, R248) and 100pF/50V/NC capacitors (C127, C132). The AV1-Vin and AV1-Lin lines are connected to the AV1-V and AV1-L lines, respectively, through a 33R resistor (R232) and a 47nF/16V capacitor (C120). The AV1-Rin line is connected to the AV1-R line through a 10kΩ resistor (R245) and a 100pF/50V/NC capacitor (C132). The AV1-Vin and AV1-Lin lines are also connected to the AV1-V and AV1-L lines, respectively, through a 33R resistor (R232) and a 47nF/16V capacitor (C120). The AV1-Rin line is connected to the AV1-R line through a 10kΩ resistor (R245) and a 100pF/50V/NC capacitor (C132).

[illegible]

COAXIAL Output

XS13

50Ω

100Ω

200Ω

300Ω

50Ω

100Ω

200Ω

300Ω

RVK5

100Ω

R272

100Ω

R271

200Ω

R272

100Ω

R271

200Ω

R272

100Ω

C151

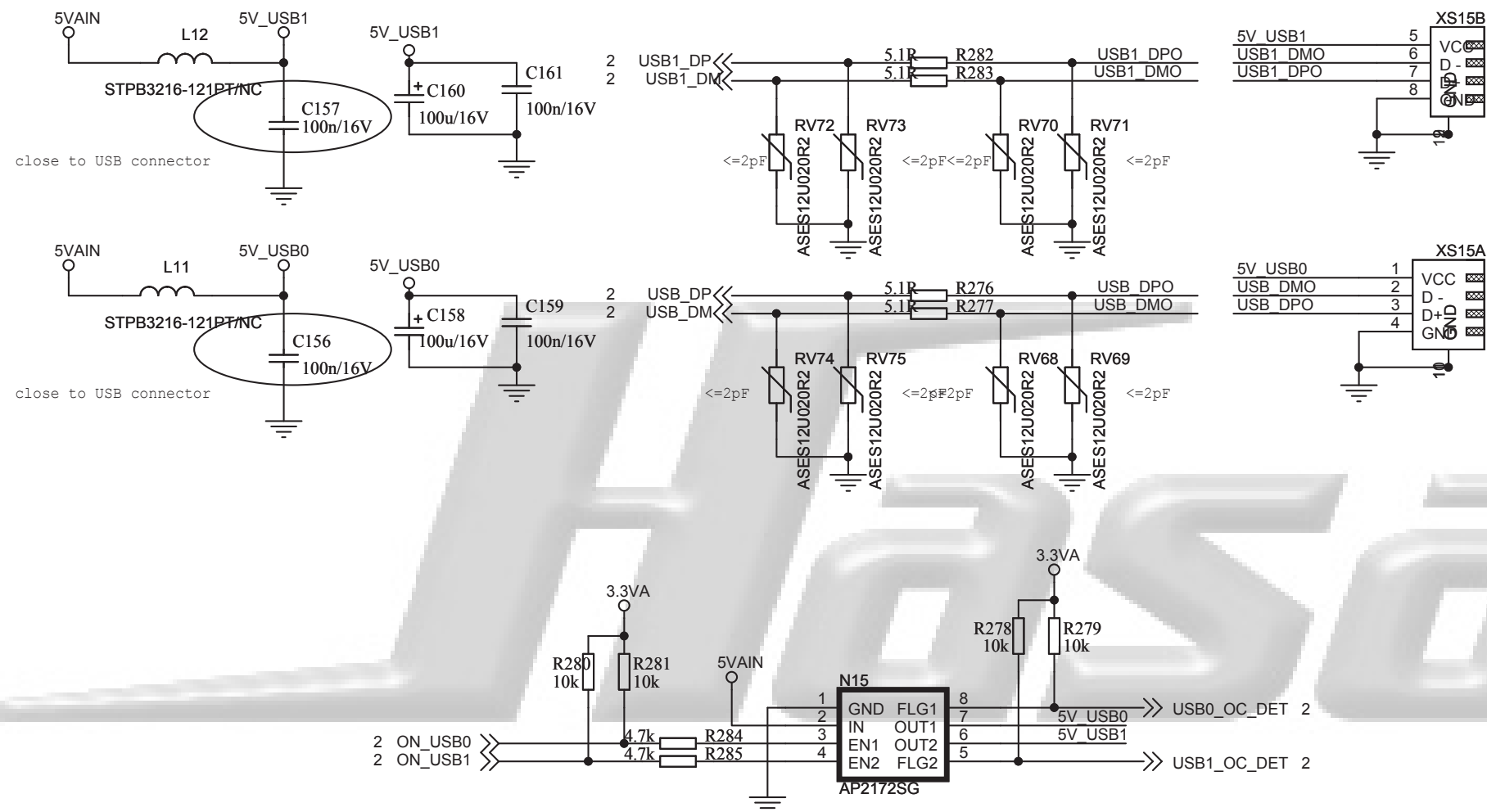
220p/50V/NC

C292

1n/50V

SPDIO 2

CLOSED TO MED309



深圳市高新区南区科技南十路国际技术创新研究院C座4楼

TEL:0755-26996895 FAX:0755-26996830

Title

USB

Size

Document Number

Rev
1.0

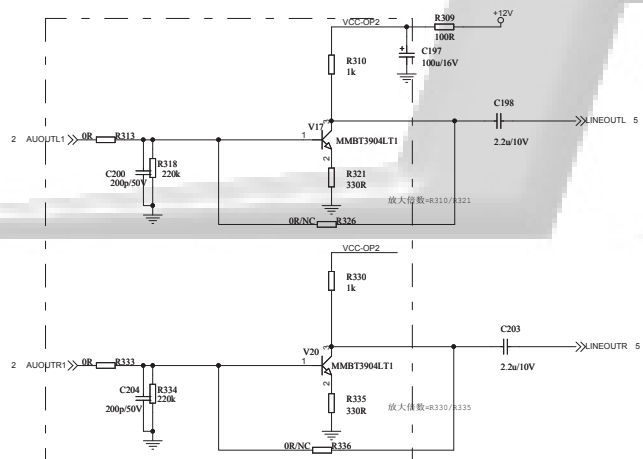
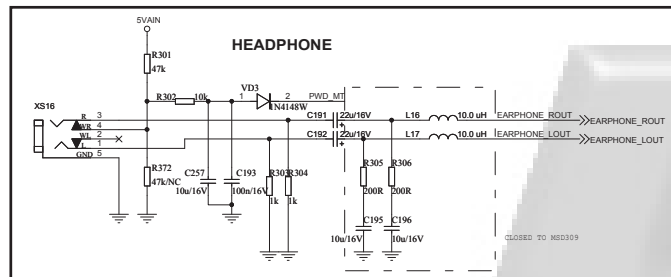
Date: Thursday, March 03, 2011

Sheet 6 of 10

12 AUDIO pre-ap

12 AUDIO pre-ap

IN1 4558 39k 238k 100pF/50V C162 AMP-LIN R288 47k 6V Bui1 C258 100pF/16V C259 100pF/16V R291 33k R292 33k R293 10k R294 10k R295 10k R296 10k R297 10k R298 10k R299 10k R300 10k R301 10k R302 10k R303 10k R304 10k R305 10k R306 10k R307 10k R308 10k R309 10k R310 10k R311 10k R312 10k R313 10k R314 10k R315 10k R316 10k R317 10k R318 10k R319 10k R320 10k R321 10k R322 10k R323 10k R324 10k R325 10k R326 10k R327 10k R328 10k R329 10k R330 10k R331 10k R332 10k R333 10k R334 10k R335 10k R336 10k R337 10k R338 10k R339 10k R340 10k R341 10k R342 10k R343 10k R344 10k R345 10k R346 10k R347 10k R348 10k R349 10k R350 10k R351 10k R352 10k R353 10k R354 10k R355 10k R356 10k R357 10k R358 10k R359 10k R360 10k R361 10k R362 10k R363 10k R364 10k R365 10k R366 10k R367 10k R368 10k R369 10k R370 10k R371 10k R372 10k R373 10k R374 10k R375 10k R376 10k R377 10k R378 10k R379 10k R380 10k R381 10k R382 10k R383 10k R384 10k R385 10k R386 10k R387 10k R388 10k R389 10k R390 10k R391 10k R392 10k R393 10k R394 10k R395 10k R396 10k R397 10k R398 10k R399 10k R400 10k R401 10k R402 10k R403 10k R404 10k R405 10k R406 10k R407 10k R408 10k R409 10k R410 10k R411 10k R412 10k R413 10k R414 10k R415 10k R416 10k R417 10k R418 10k R419 10k R420 10k R421 10k R422 10k R423 10k R424 10k R425 10k R426 10k R427 10k R428 10k R429 10k R430 10k R431 10k R432 10k R433 10k R434 10k R435 10k R436 10k R437 10k R438 10k R439 10k R440 10k R441 10k R442 10k R443 10k R444 10k R445 10k R446 10k R447 10k R448 10k R449 10k R450 10k R451 10k R452 10k R453 10k R454 10k R455 10k R456 10k R457 10k R458 10k R459 10k R460 10k R461 10k R462 10k R463 10k R464 10k R465 10k R466 10k R467 10k R468 10k R469 10k R470 10k R471 10k R472 10k R473 10k R474 10k R475 10k R476 10k R477 10k R478 10k R479 10k R480 10k R481 10k R482 10k R483 10k R484 10k R485 10k R486 10k R487 10k R488 10k R489 10k R490 10k R491 10k R492 10k R493 10k R494 10k R495 10k R496 10k R497 10k R498 10k R499 10k R500 10k R501 10k R502 10k R503 10k R504 10k R505 10k R506 10k R507 10k R508 10k R509 10k R510 10k R511 10k R512 10k R513 10k R514 10k R515 10k R516 10k R517 10k R518 10k R519 10k R520 10k R521 10k R522 10k R523 10k R524 10k R525 10k R526 10k R527 10k R528 10k R529 10k R530 10k R531 10k R532 10k R533 10k R534 10k R535 10k R536 10k R537 10k R538 10k R539 10k R540 10k R541 10k R542 10k R543 10k R544 10k R545 10k R546 10k R547 10k R548 10k R549 10k R550 10k R551 10k R552 10k R553 10k R554 10k R555 10k R556 10k R557 10k R558 10k R559 10k R560 10k R561 10k R562 10k R563 10k R564 10k R565 10k R566 10k R567 10k R568 10k R569 10k R570 10k R571 10k R572 10k R573 10k R574 10k R575 10k R576 10k R577 10k R578 10k R579 10k R580 10k R581 10k R582 10k R583 10k R584 10k R585 10k R586 10k R587 10k R588 10k R589 10k R590 10k R591 10k R592 10k R593 10k R594 10k R595 10k R596 10k R597 10k R598 10k R599 10k R600 10k R601 10k R602 10k R603 10k R604 10k R605 10k R606 10k R607 10k R608 10k R609 10k R610 10k R611 10k R612 10k R613 10k R614 10k R615 10k R616 10k R617 10k R618 10k R619 10k R620 10k R621 10k R622 10k R623 10k R624 10k R625 10k R626 10k R627 10k R628 10k R629 10k R630 10k R631 10k R632 10k R633 10k R634 10k R635 10k R636 10k R637 10k R638 10k R639 10k R640 10k R641 10k R642 10k R643 10k R644 10k R645 10k R646 10k R647 10k R648 10k R649 10k R650 10k R651 10k R652 10k R653 10k R654 10k R655 10k R656 10k R657 10k R658 10k R659 10k R660 10k R661 10k R662 10k R663 10k R664 10k R665 10k R666 10k R667 10k R668 10k R669 10k R670 10k R671 10k R672 10k R673 10k R674 10k R675 10k R676 10k R677 10k R678 10k R679 10k R680 10k R681 10k R682 10k R683 10k R684 10k R685 10k R686 10k R687 10k R688 10k R689 10k R690 10k R691 10k R692 10k R693 10k R694 10k R695 10k R696 10k R697 10k R698 10k R699 10k R700 10k R701 10k R702 10k R703 10k R704 10k R705 10k R706 10k R707 10k R708 10k R709 10k R710 10k R711 10k R712 10k R713 10k R714 10k R715 10k R716 10k R717 10k R718 10k R719 10k R720 10k R721 10k R722 10k R723 10k R724 10k R725 10k R726 10k R727 10k R728 10k R729 10k R730 10k R731 10k R732 10k R733 10k R734 10k R735 10k R736 10k R737 10k R738 10k R739 10k R740 10k R741 10k R742 10k R743 10k R744 10k R745 10k R746 10k R747 10k R748 10k R749 10k R750 10k R751 10k R752 10k R753 10k R754 10k R755 10k R756



Near MST.IC
Ground in the middle of the L/R

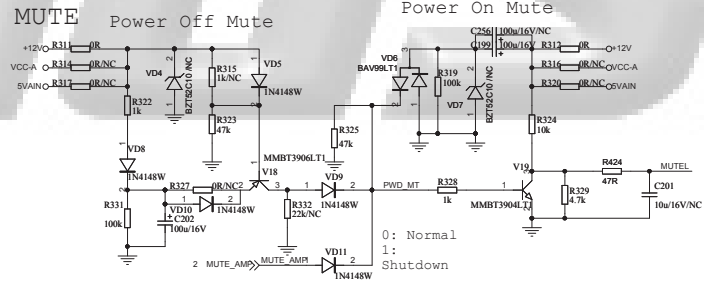
Power Amplifier

注意：PGND 与 AGND 在功率 IC 内部是连通的，并且与地相连。

MUTE Function

MUTE

Power Off Mute

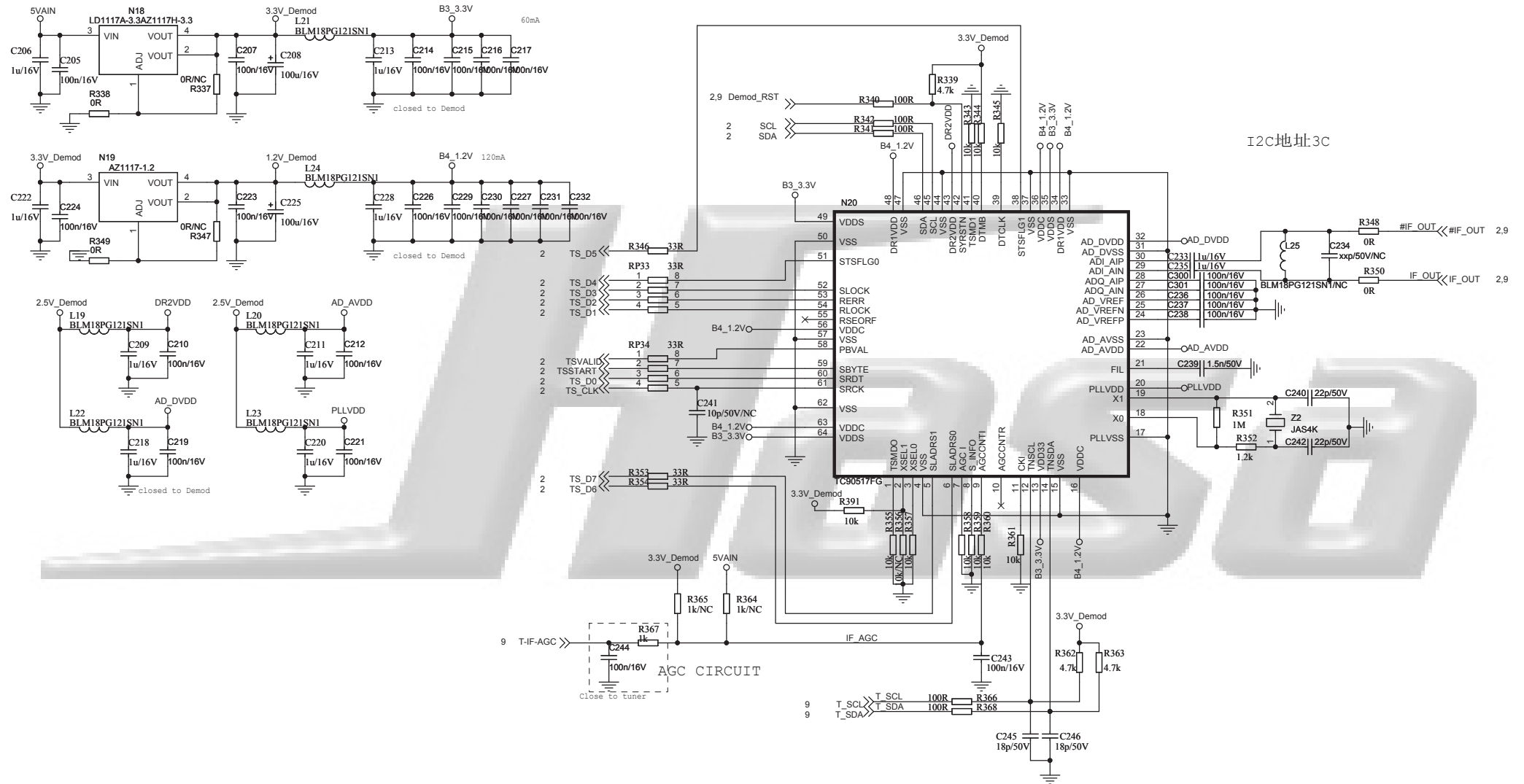


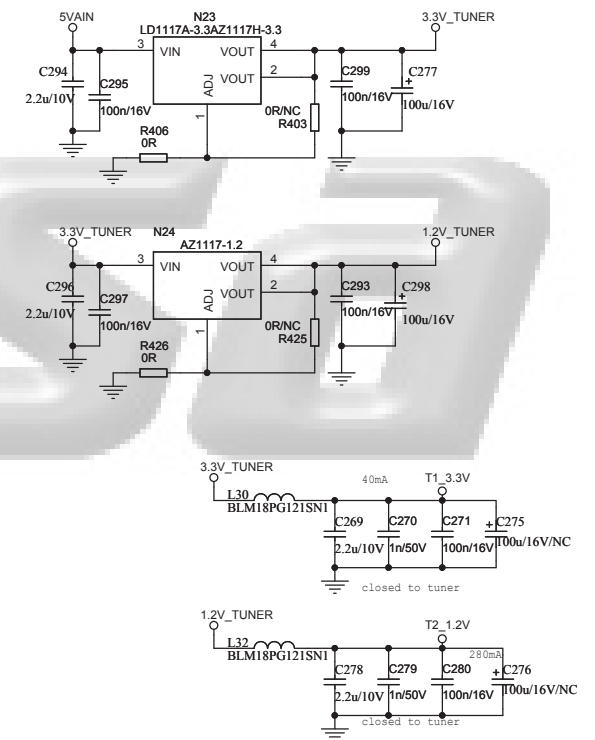
```

0: Normal
1:
2: Shutdown
148W

```

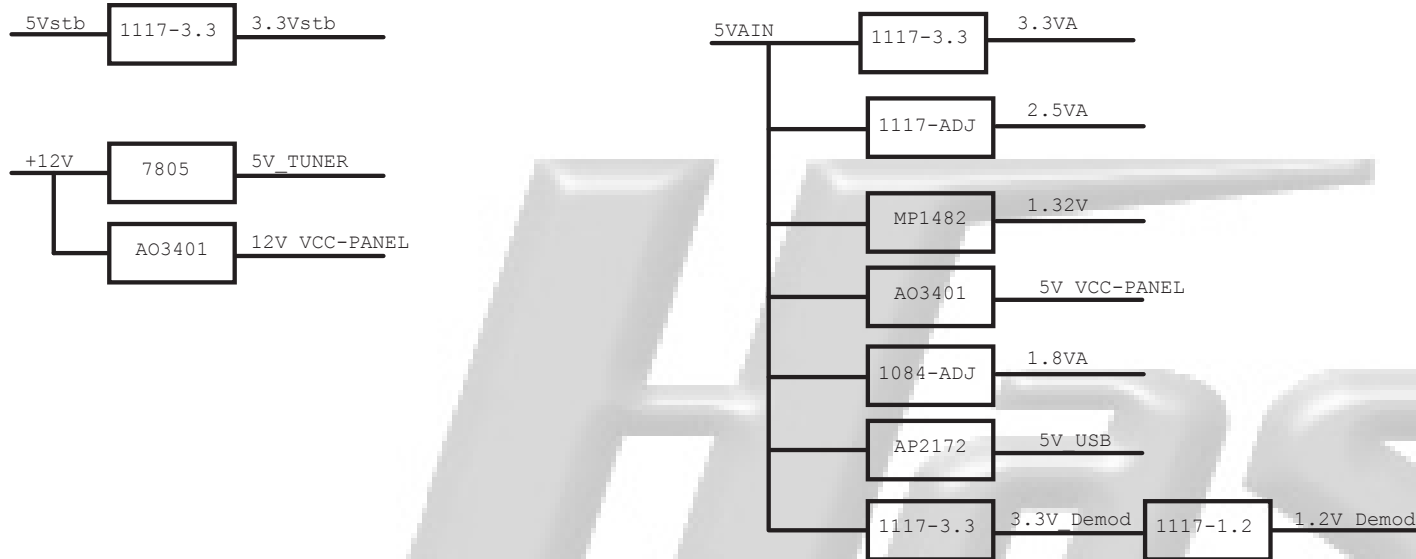
		深圳市高新区南区科技南十路国际技术创新研究院C座4楼	
TEL: 0755-26996895		FAX: 0755-26996830	
File AUDIO			
Size	Document Number		Rev 1.0
Date:	Thursday, March 03, 2011	Sheet	7 of 10





Title				
MSD329ZX+MSB123X				
Size A3	Document Number			Rev 1.0
TUNER				
Date:	Thursday, March 03, 2011	Sheet	9	of 10

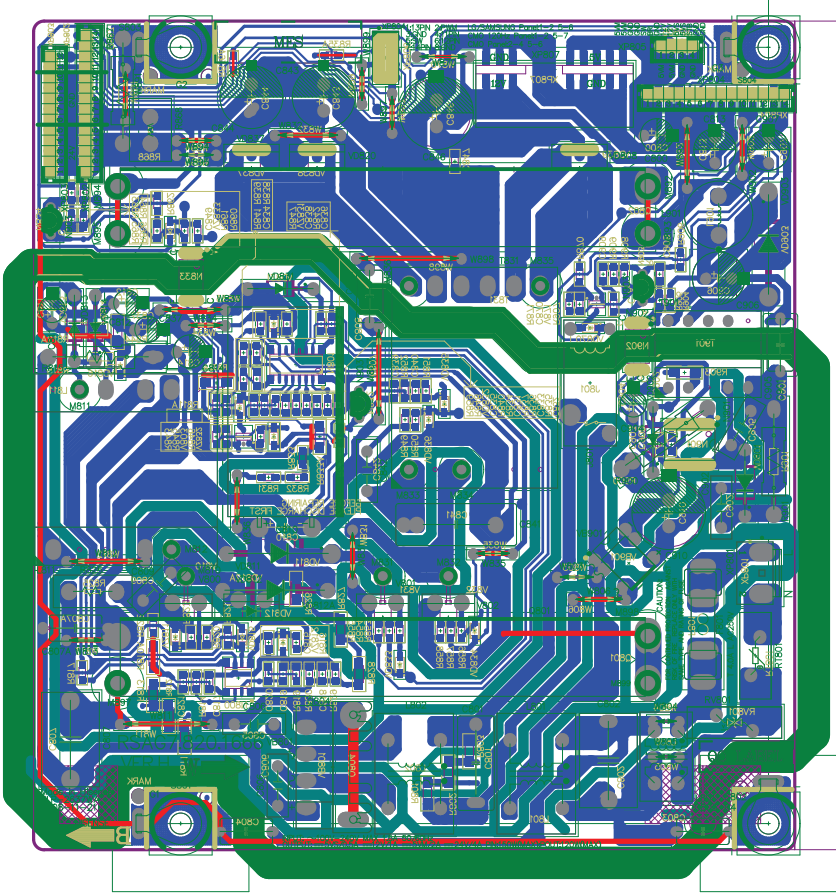
POWER TREE



Title			<Title>
Size	Document Number		Rev
B	<Doc>		<RevCode>
Date:	Thursday, March 03, 2011		Sheet 10 of 10

This document contains proprietary information coping, disclosures to others or other use is prohibited without the express written authorization of Hisense.

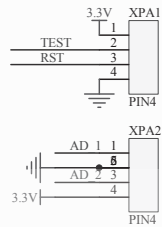
PLACA FUENTE 1666 VER. H



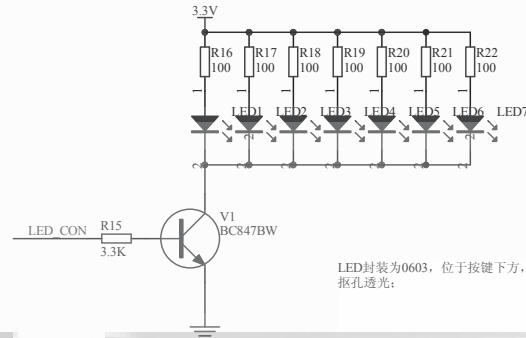
PCB DESCRIPTION: POWER BOARD			
PAGE TITLE: Top Overlay			
PCB TYPE: RSAG7.820.1666		VERSION: H	
PCB LAYOUT		GY Ren 21.Jun.2010	
CHECKED BY		GS Yue 21.Jun.2010	
APPROVED BY		XG Wang 21.Jun.2010	
SIZE: A2	SCALE: 1:1	PAGE 3	OF 9

ESQUEMATICO PLACA CONTROLES

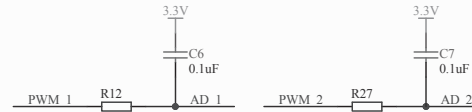
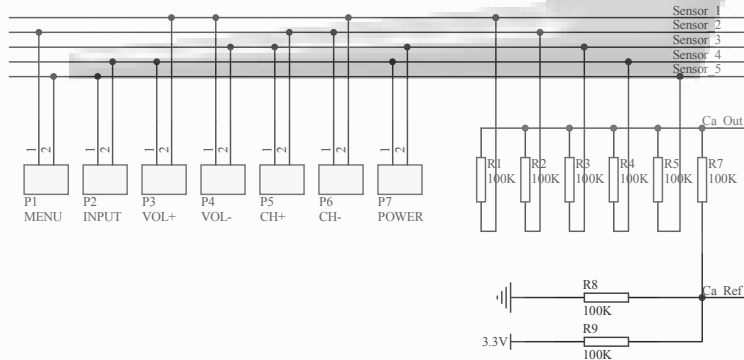
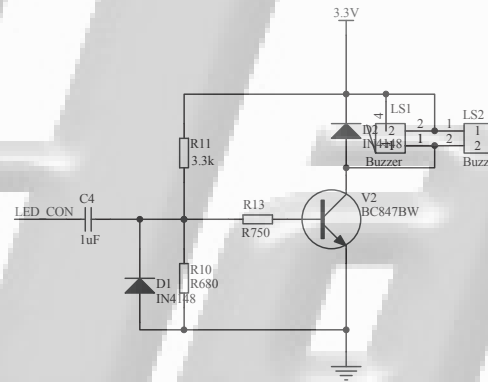
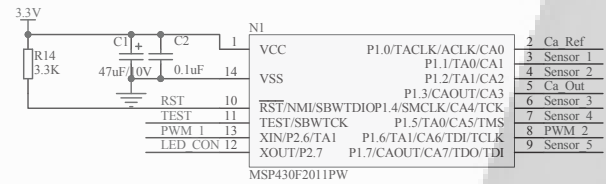
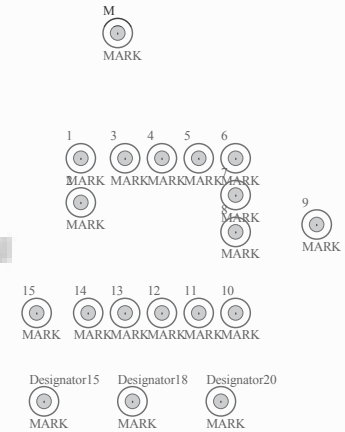
PCB 1833/VER B



接口部分:
 1、XPA2、XPA3接口为2.0间距卧式直插插座;
 2、XPA1为调试接口, 留出焊盘, 方便调试;
 3、I2C接口也为后期调试接口, 最好有; 如果影响PCB布局, 可以不要, 上面的两个电阻也可一并省去;



LED封装为0603, 位于按键下方,
 抠孔透光;



Title		
Size	Number	Revision
B		
Date:	2011-4-6	Sheet of
File:	D:\need\LHD26V8U8_KEY_B(1833)\SCH	By:

