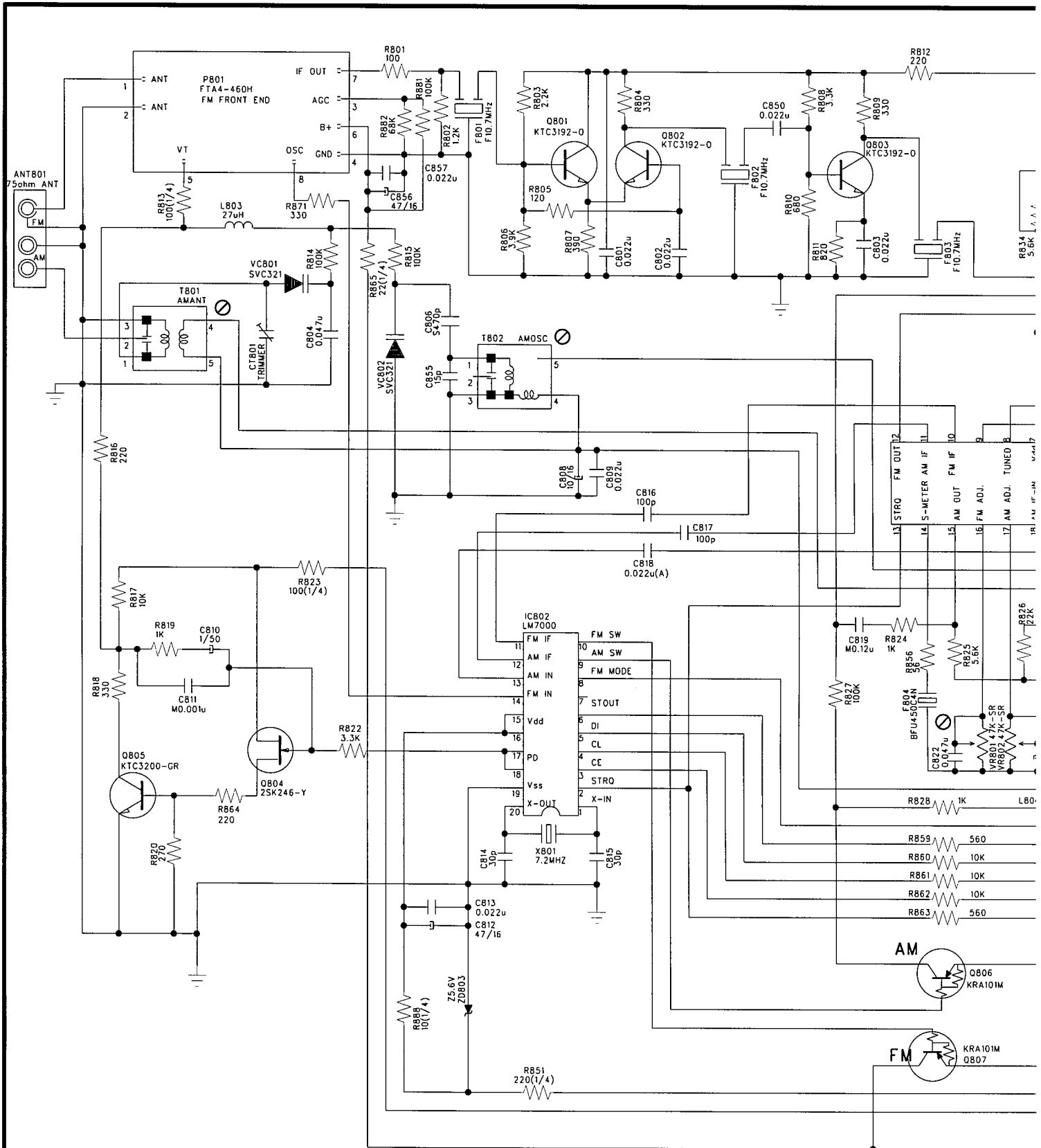
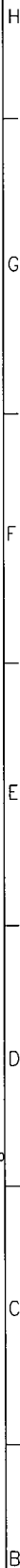


AVR-35RDS TUNER SCHEMATIC

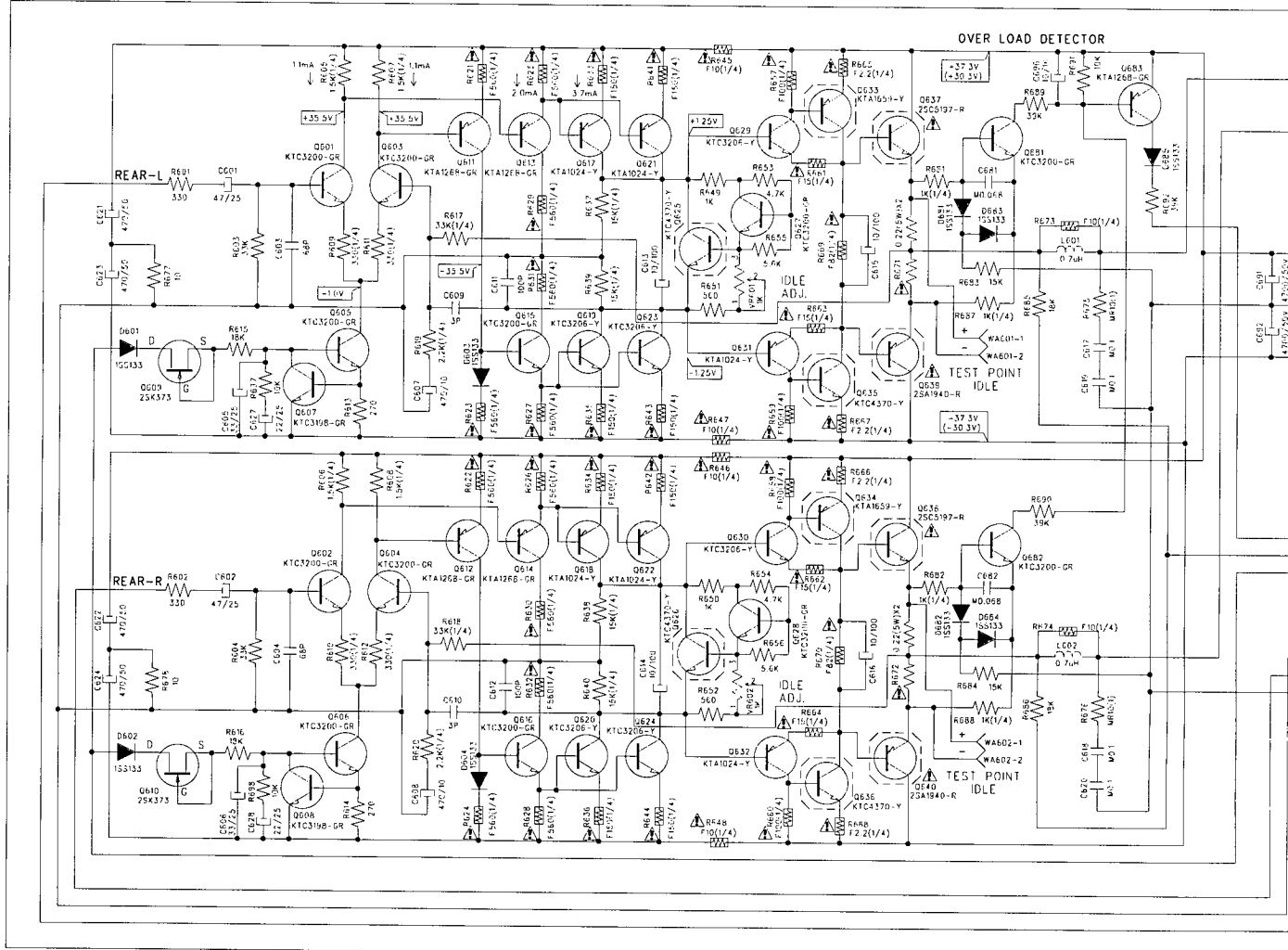


NO	DATE	POS.	CONTENTS	NO	DATE	POS.	CONTENTS

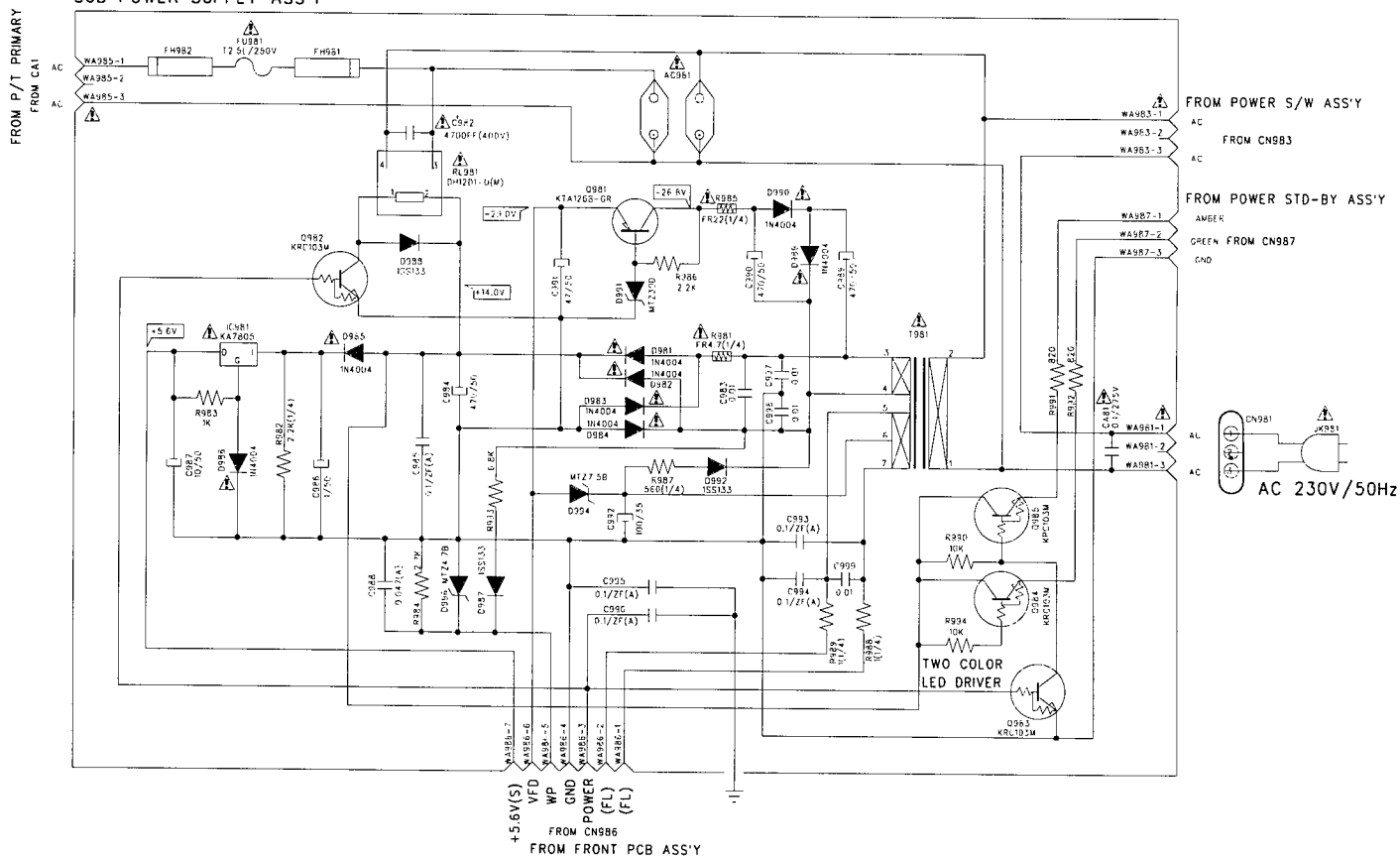


B

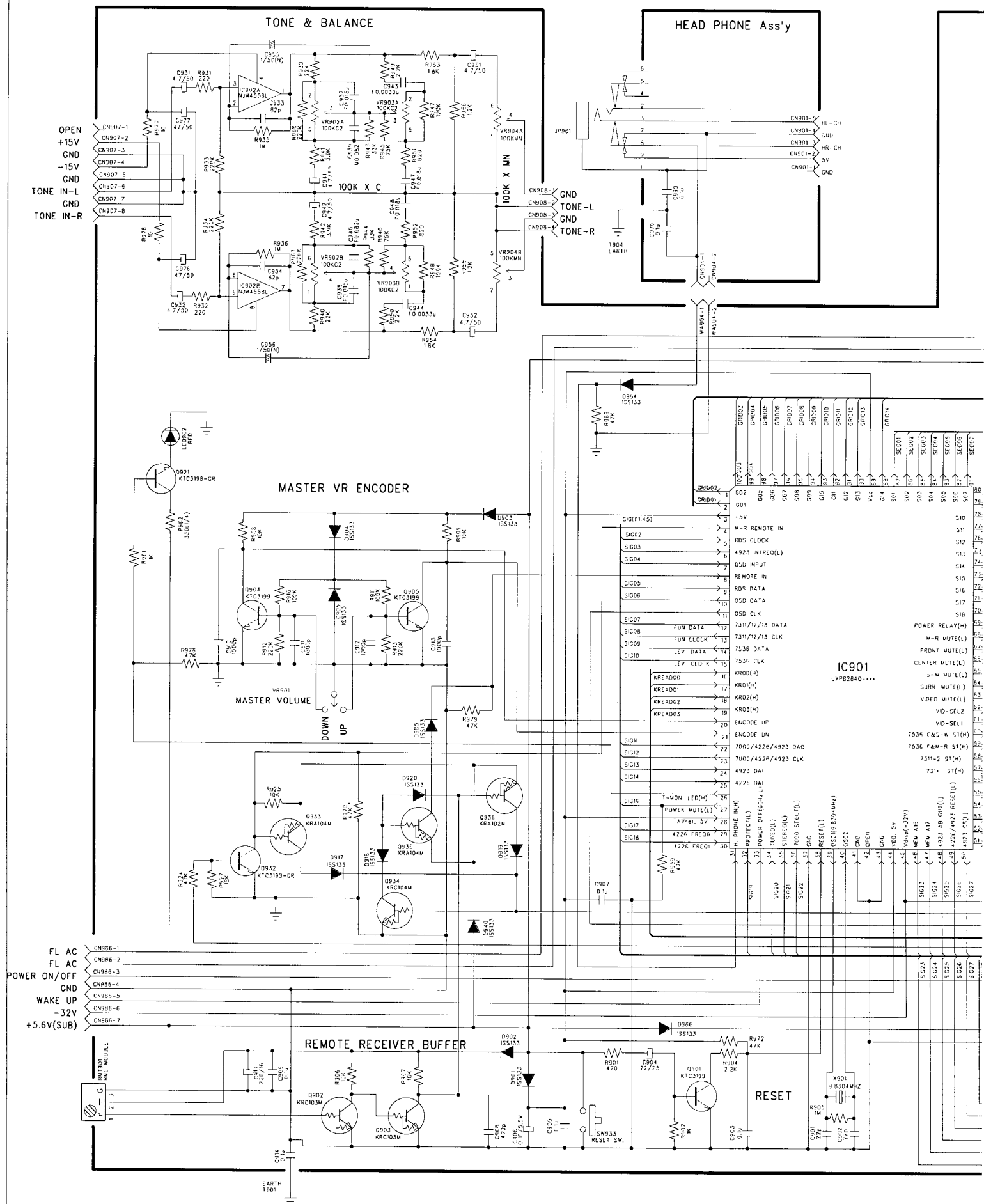
REAR AMP ASS'Y



SUB POWER SUPPLY ASS'Y

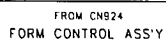


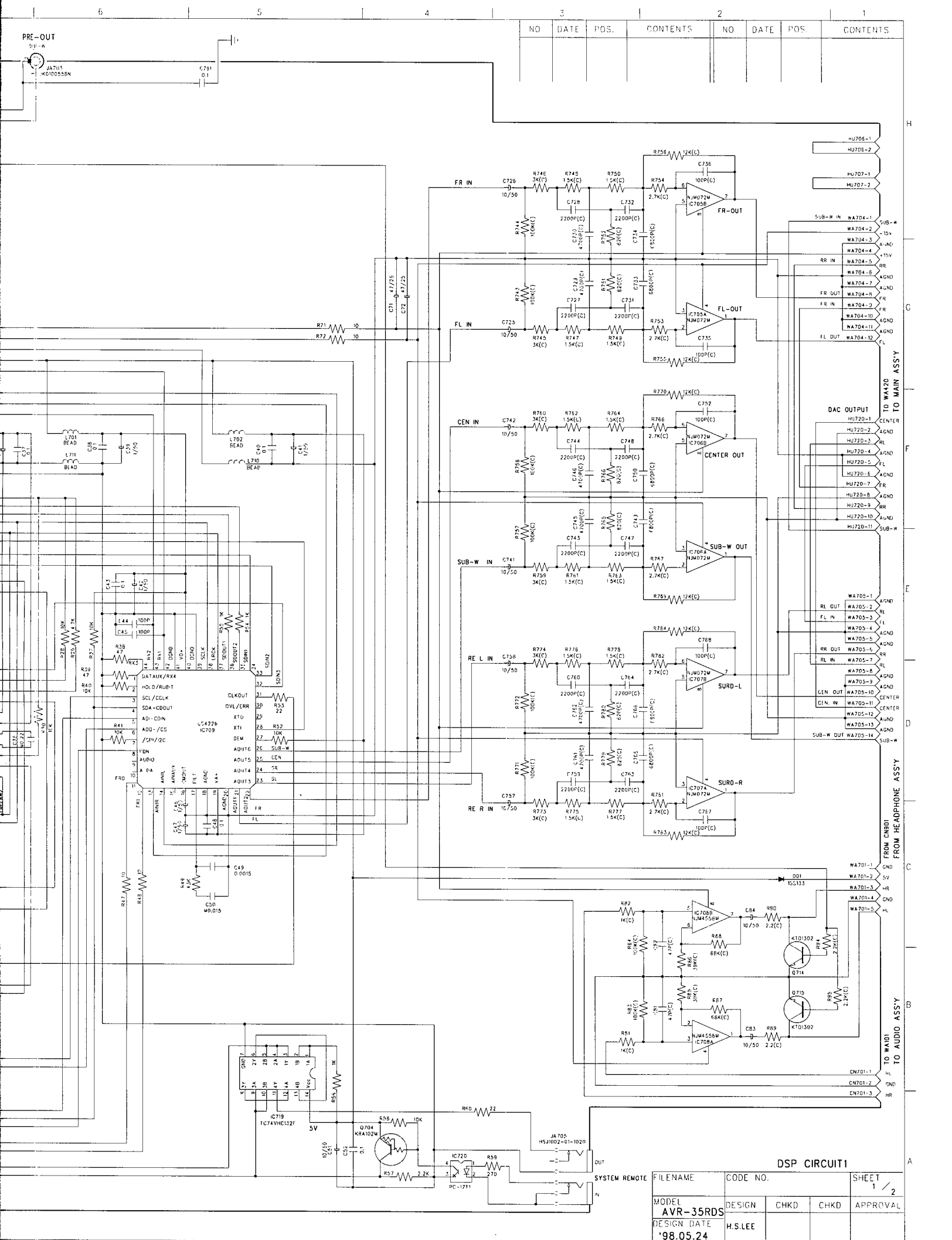
AVR-35RDS u-COM & F.L.DISPLAY SC



3			2				1
NO	DATE	PQS	CONTENTS	NO	DATE	PQS	CONTENTS





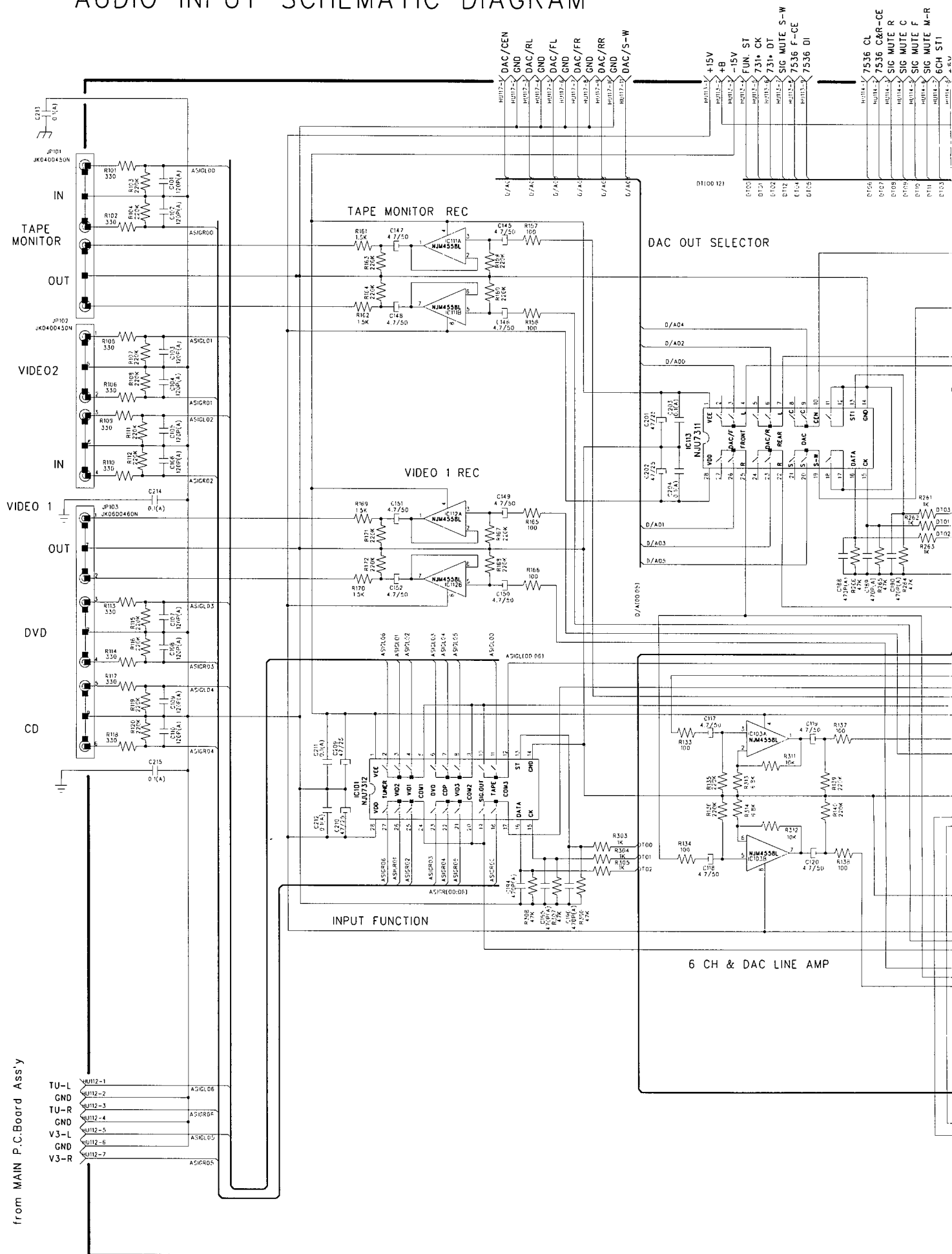


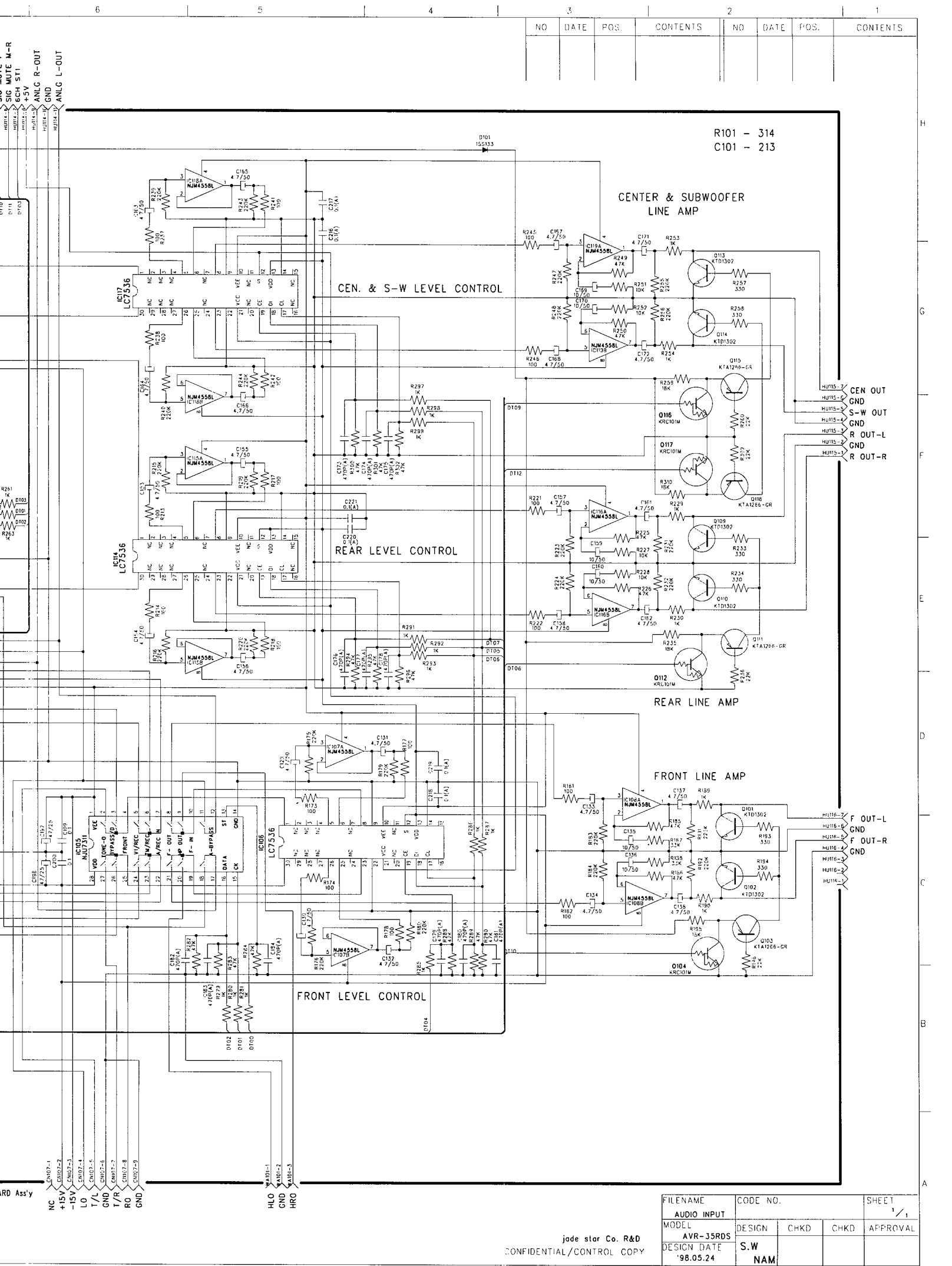
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DSP CIRCUIT1				
MODEL	AVR-35RDS	DESIGN	CHKD	CHKD
DESIGN DATE	'98.05.24	H.S. LEE		
FILENAME		CODE NO.		SHEET 1 / 2
SYSTEM REMOTE		DESIGN	CHKD	CHKD
IN		DESIGN DATE	'98.05.24	APPROVAL
OUT				

JADESTAR CORP.

AUDIO INPUT SCHEMATIC DIAGRAM





(FONT & CENTER) MAIN AMP ASS'Y

