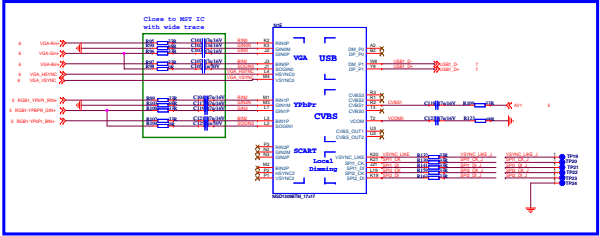
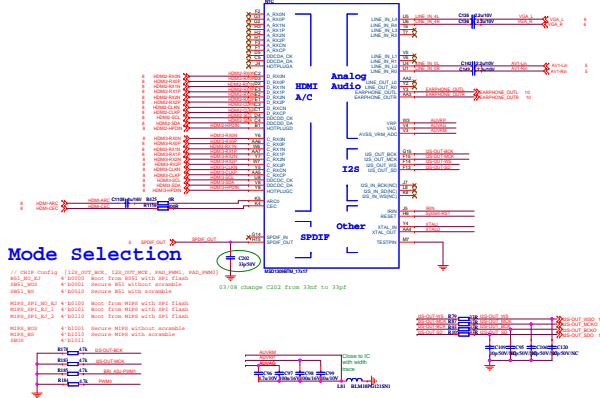


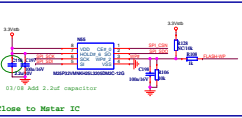
RGB & CVBS & USB



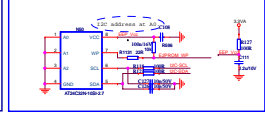
HDMI & Audio



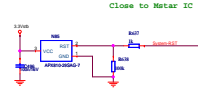
Boot Loader SPI Flash



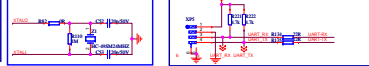
EEPROM & HDPC



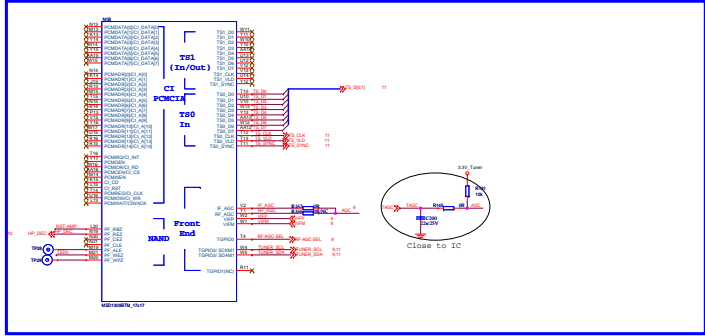
RESET



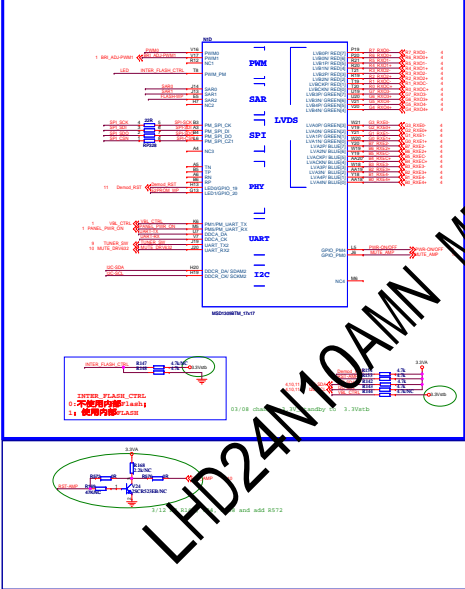
System XTAL



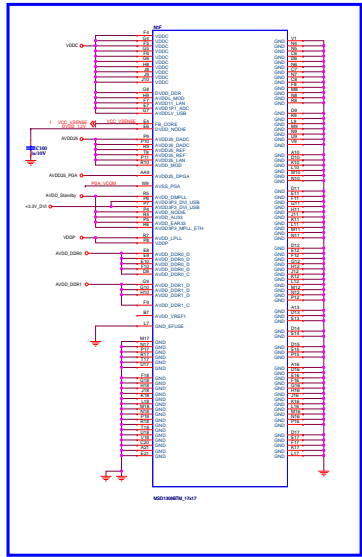
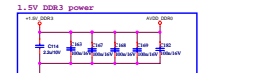
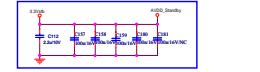
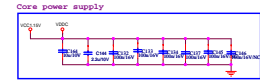
Front End



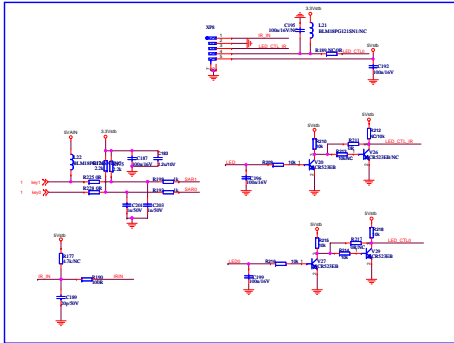
GPIO & LVDS

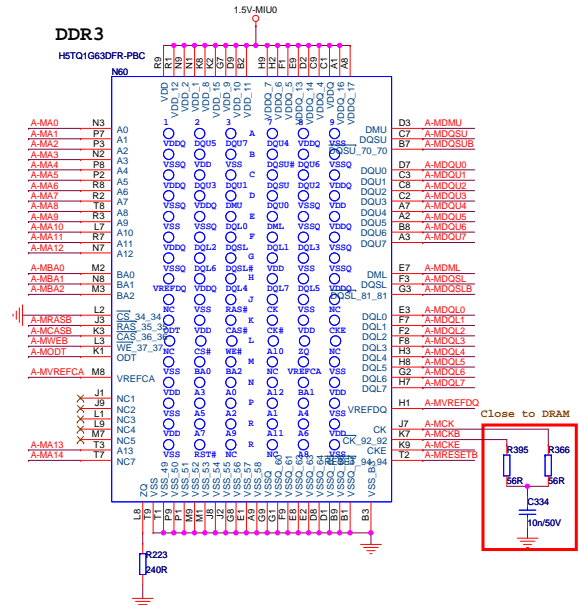
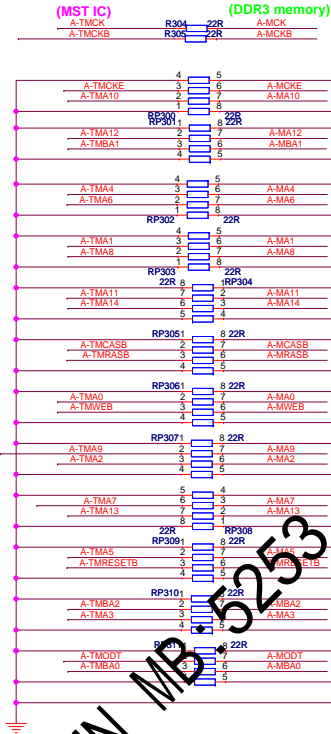
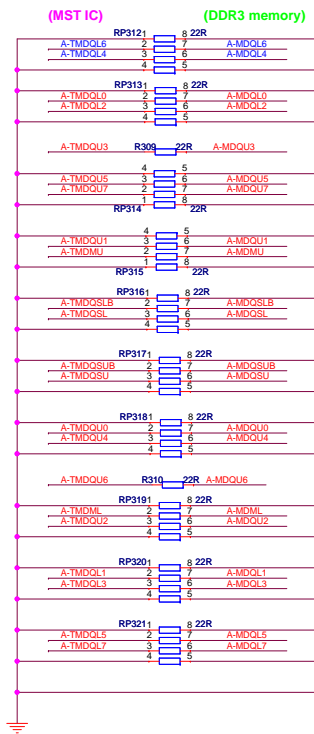
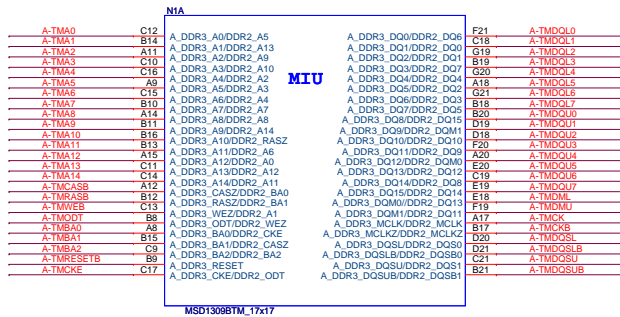


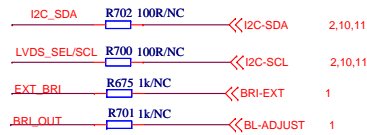
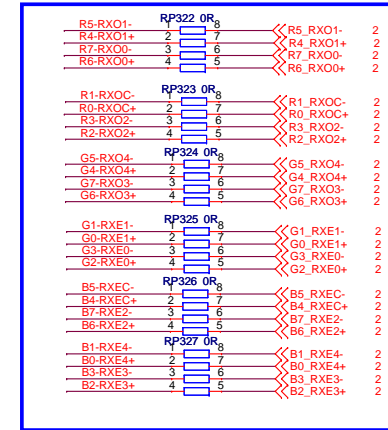
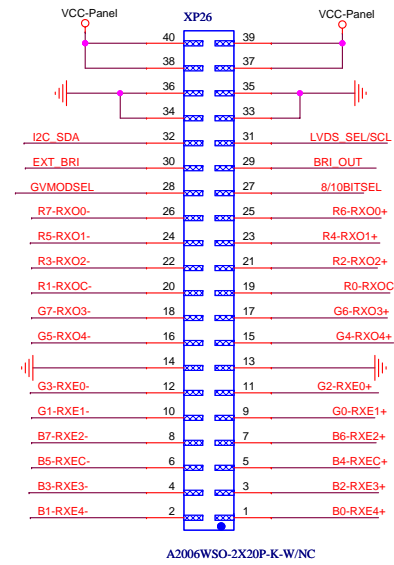
Decouple Capacitors Close to IC pins



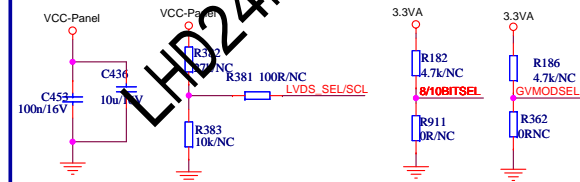
KEY PAD& IR



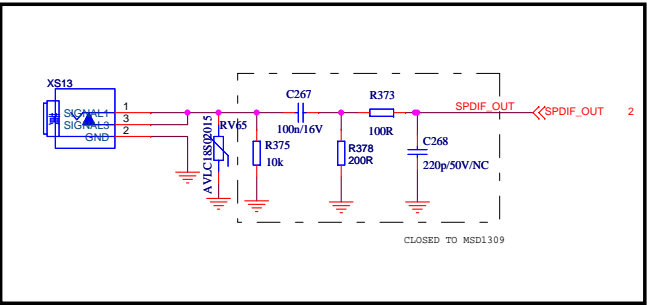




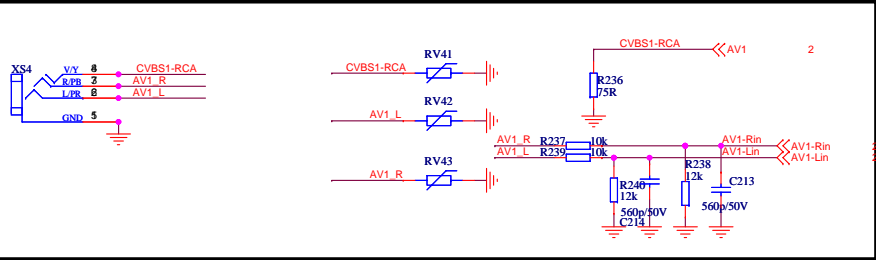
Power decouple



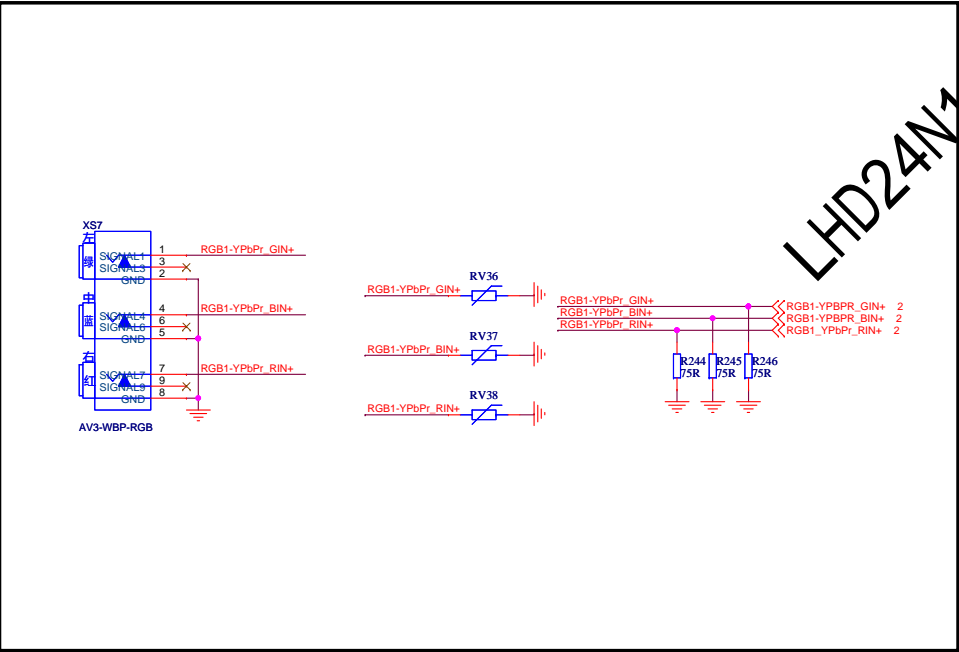
COAXIAL OUT



AV Input

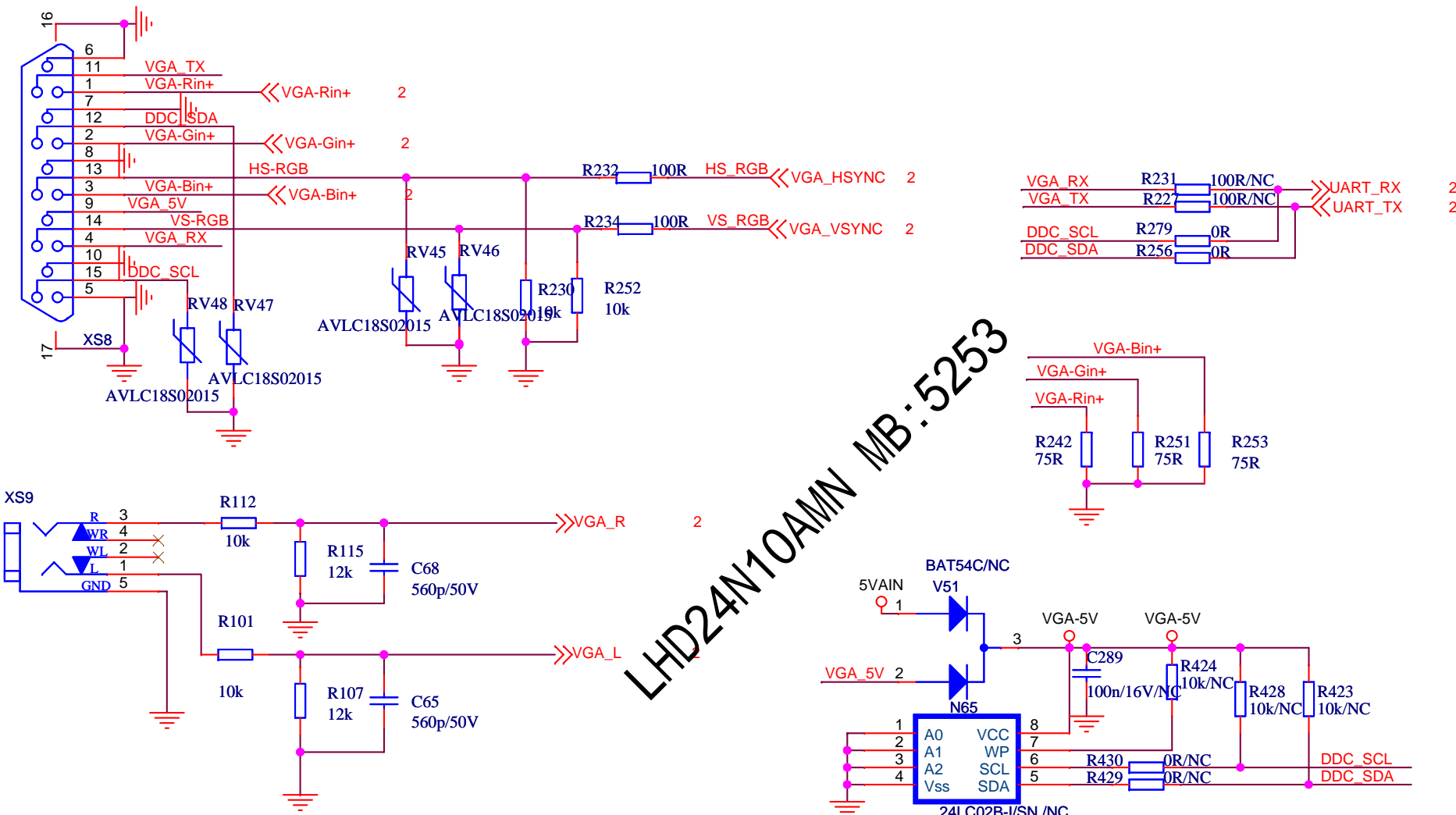


HDTV Input




LHD24N10AMN MB:5253

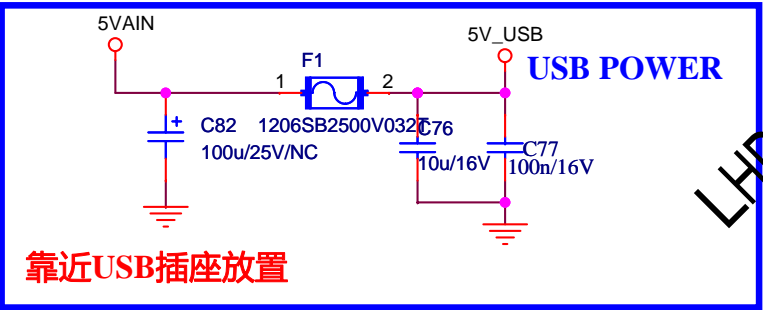
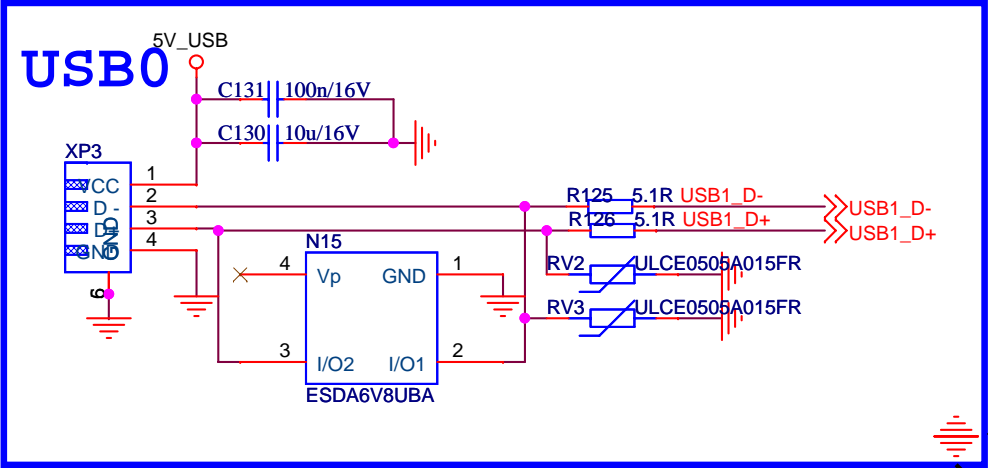
VGA




LHD24N10AMN MB:5253

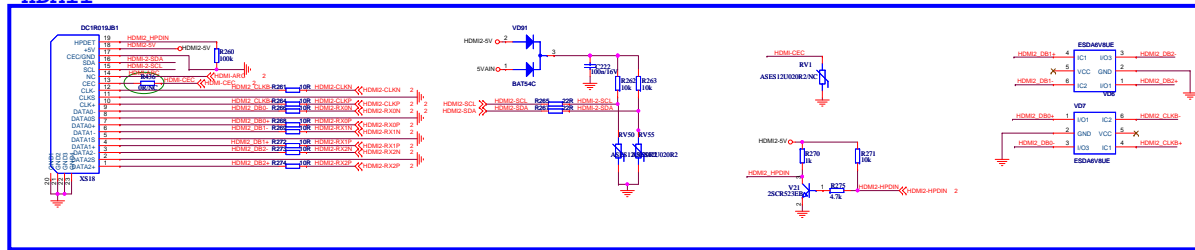
			Hisense Electric Co.,Ltd. No.11,Jiangxi Road,Qingdao,China WWW.HISENSE.COM	
Title			Hisense MSD30XBT	
Size	Schematic Name		Rev	
A	<Doc>		1.0	
Date:	Friday, January 04, 2013		Sheet 6	of 11

USB INTERFACE

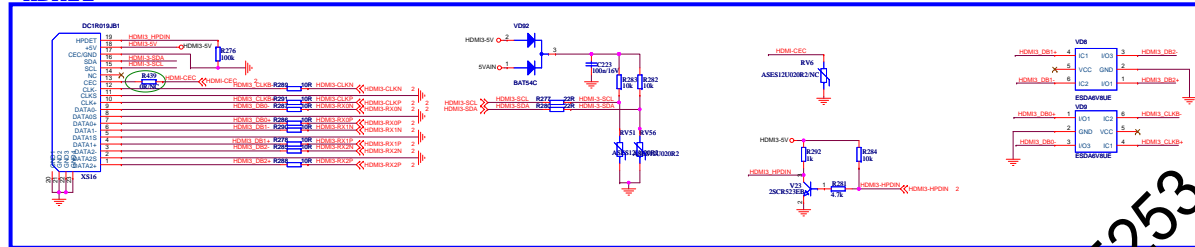


				Hisense Electric Co.,Ltd. No.11,Jiangxi Road,Qingdao,China WWW.HISENSE.COM			
Title				Hisense MSD30XBT			
Size		Schematic Name			Rev		
A		<Doc>			1.0		
Date:		Friday, January 04, 2013			Sheet 7 of 11		

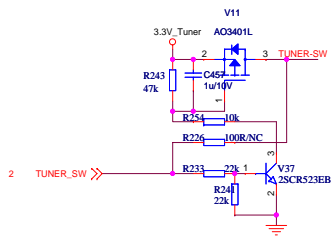
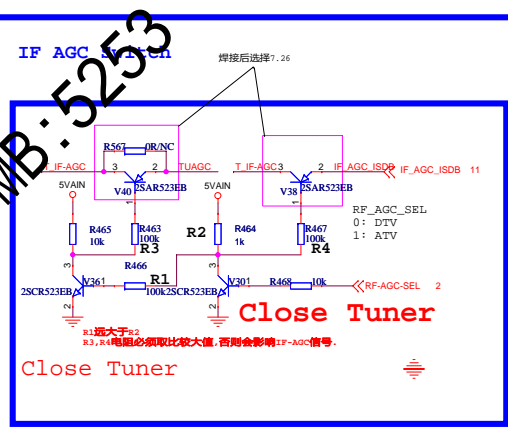
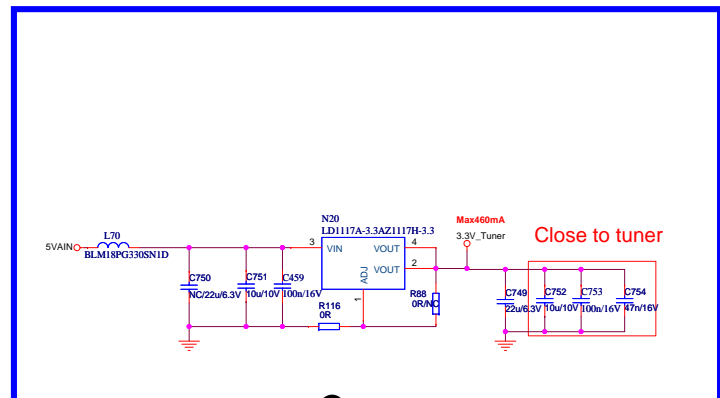
HDMI1

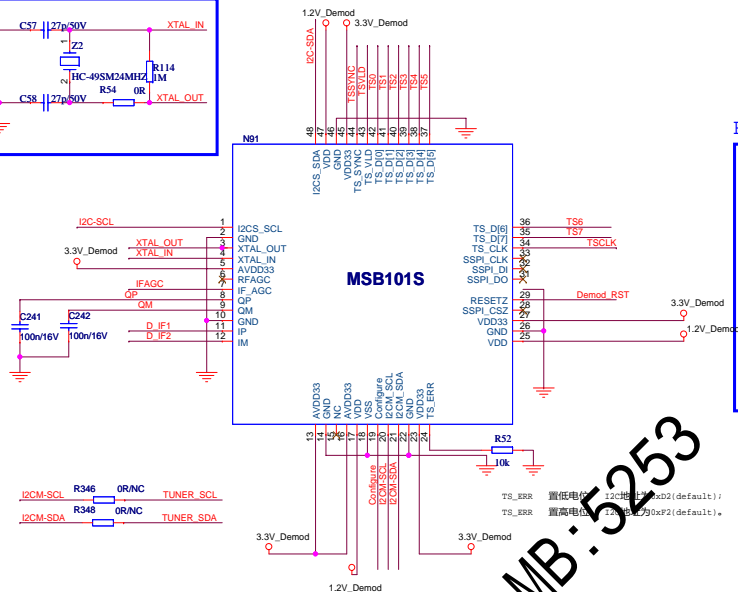


HDMI 2



LHD24N10AMN MB:5253





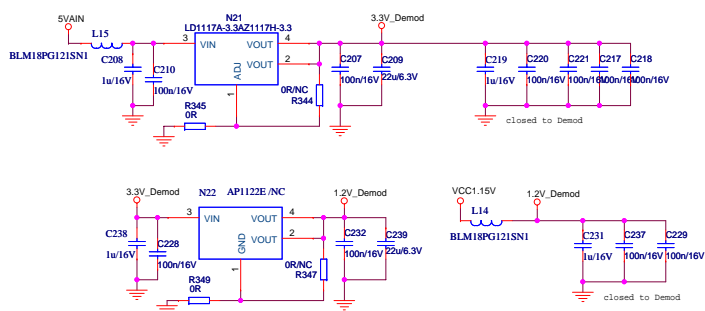
3.3V Demod

R53
10k

Demod_RST

C340
2.2u/10V

A circuit diagram showing a red line labeled 'Configure' entering a blue rectangular component labeled 'R25' and '10k'. The other end of the resistor is connected to a ground symbol.



The diagram shows the internal wiring of the LHD24N10AMN module. The T8_ERR pin is configured with a 10k pull-down resistor (R52) to ground. It is also connected to the 3.3V_Demod signal line. Other pins shown include SCL, SDA, 1.2V_Demod, and a group of pins labeled 22, 21, 20, 19, 18, 17, 16, 15, 14, 13, 12, 11, 10, 9, 8, 7, 6, 5, 4, 3, 2, 1. A large diagonal watermark 'LHD24N10AMN MB:5253' is present across the image.