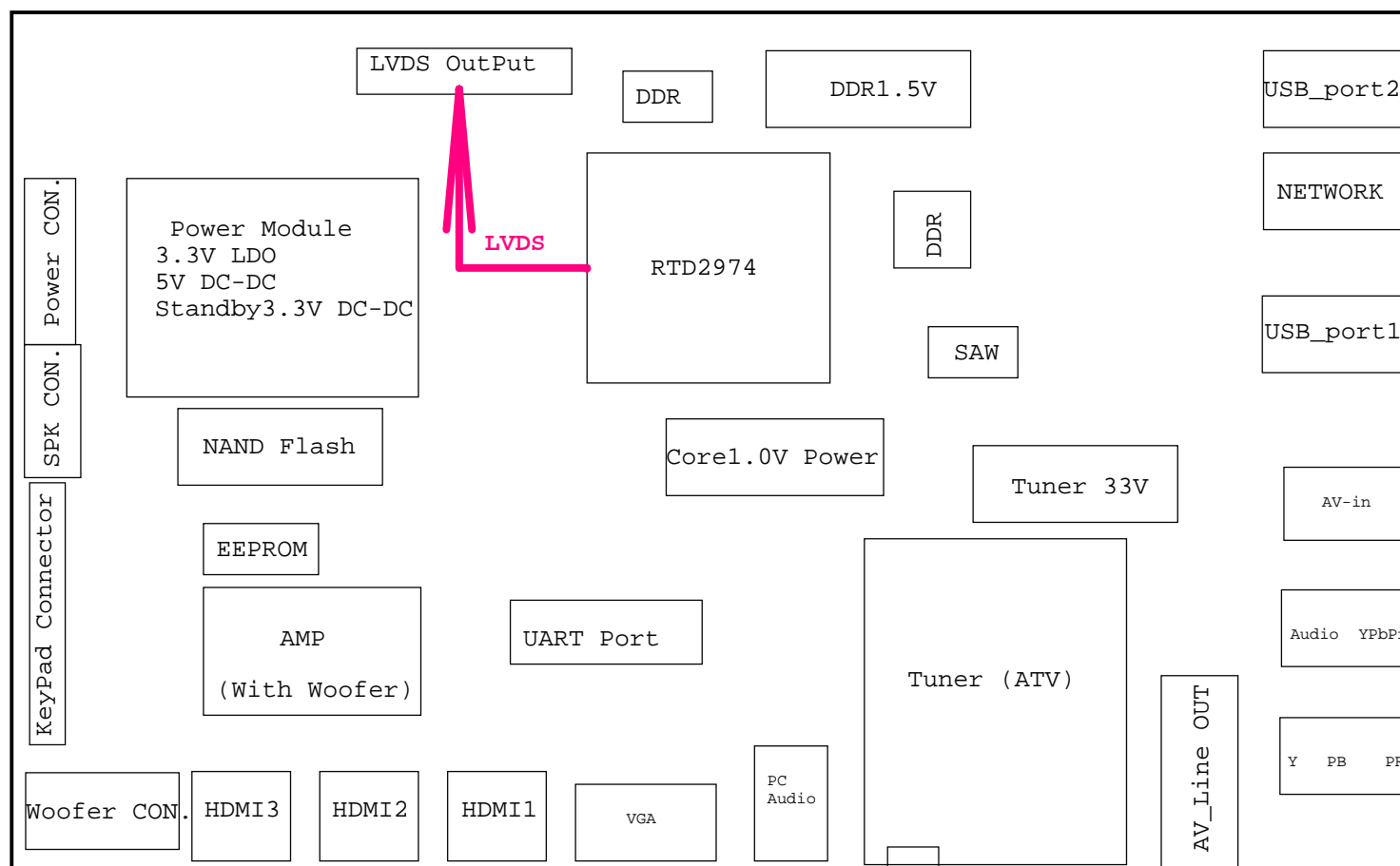


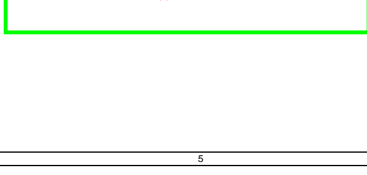
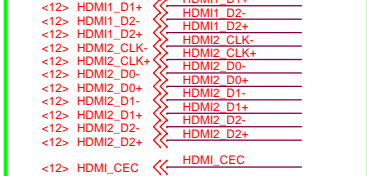
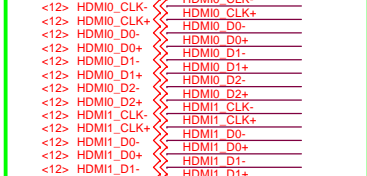
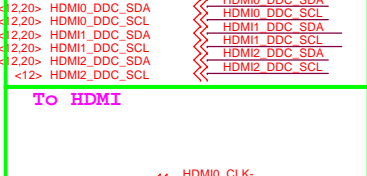
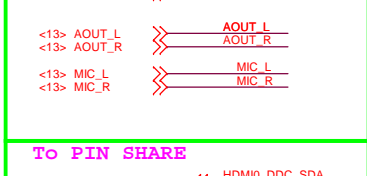
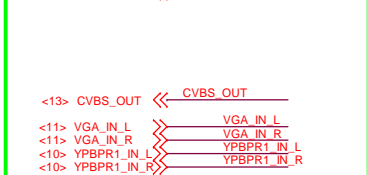
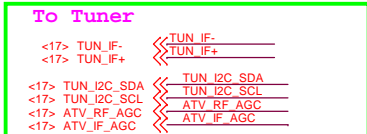
该表格是U16输出的3V3各电流

	Normal	Standby
LS3		1mA
LT2	137mA	2mA
LT3	37mA	2mA
L98	8mA	2mA/NAND FLASH
L69	LS3	7mA/Front Panel

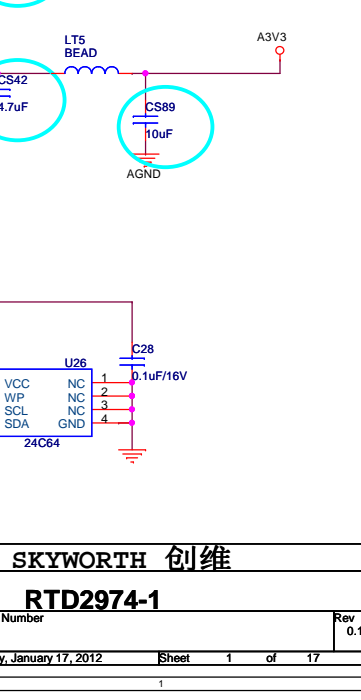
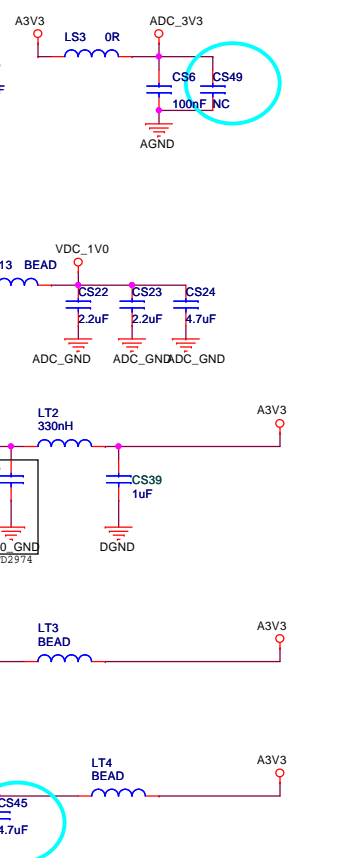
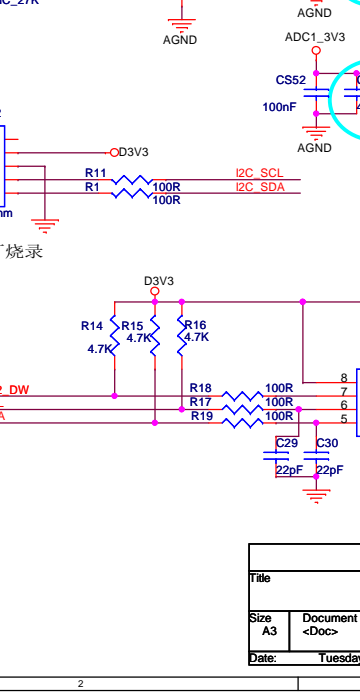
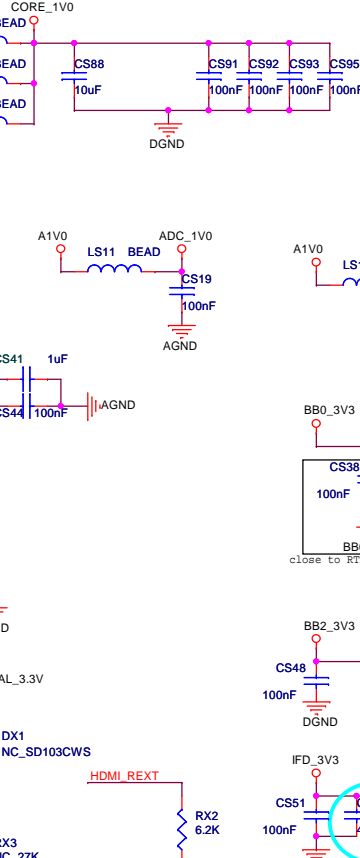
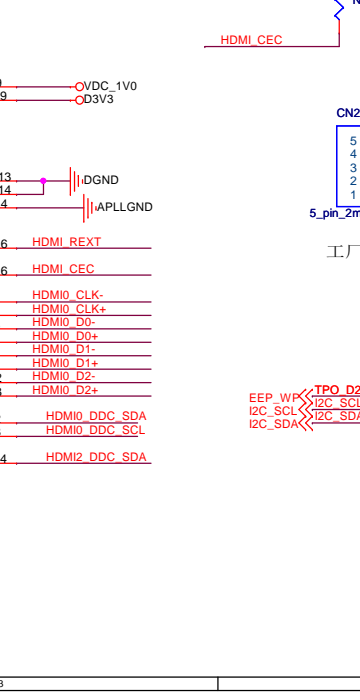
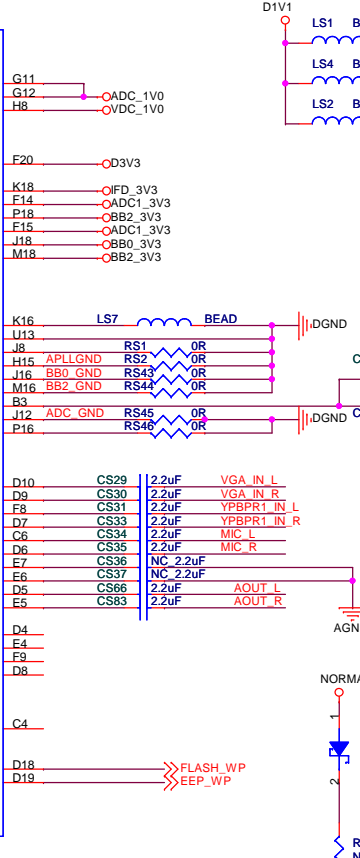
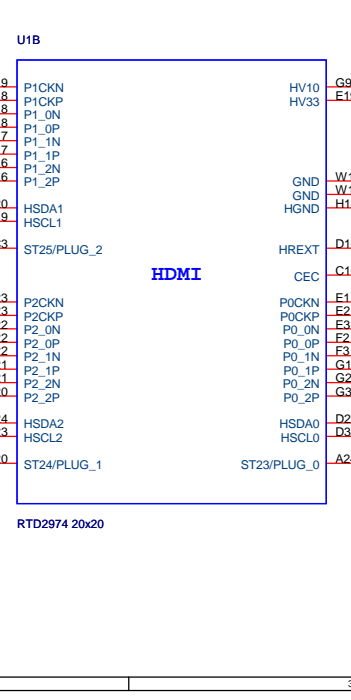
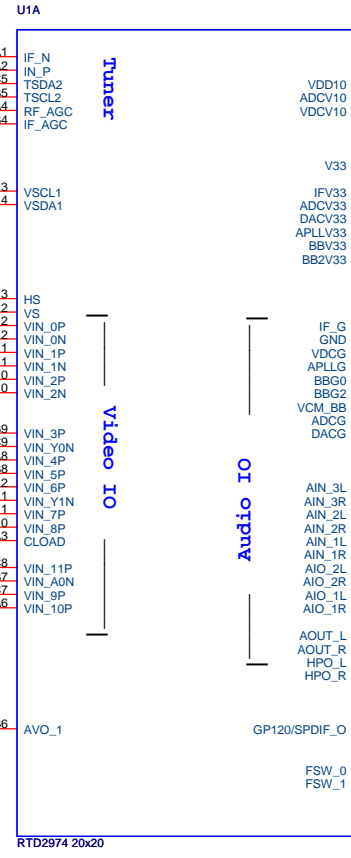
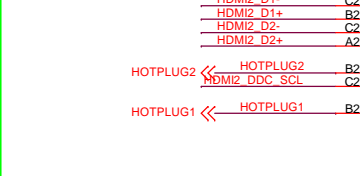
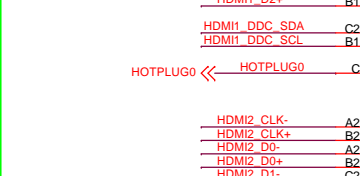
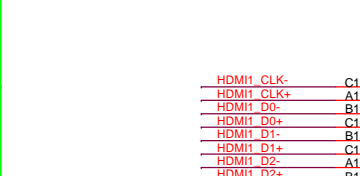
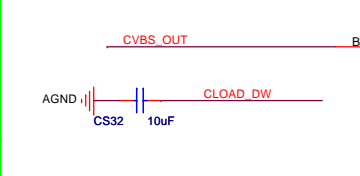
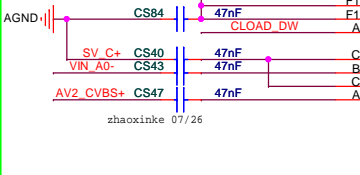
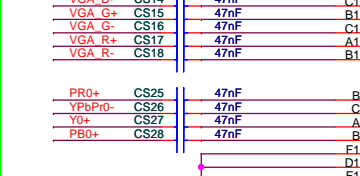
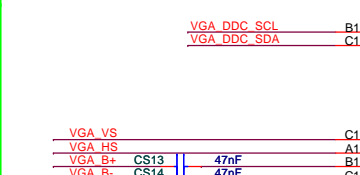
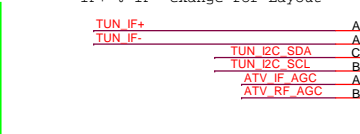
SKYWORTH 创维		
Title	RTD2692D POWER TREE	
Size	Document Number	Rev
A3	POWER TREE	0.1
Date:	Tuesday, January 17, 2012	Sheet 17 of 17



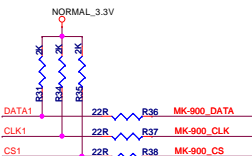
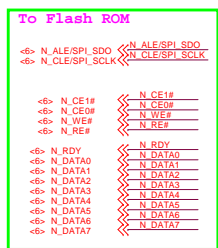
All Connector Using Co-Layout For down export and Back export



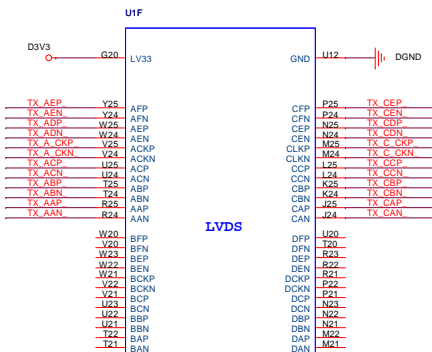
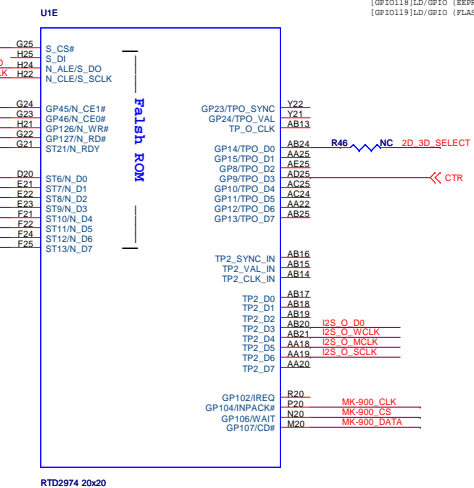
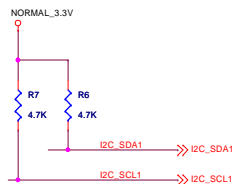
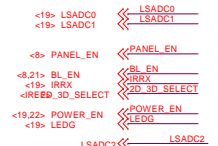
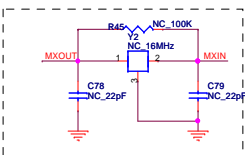
IF+ % IF- exchange for Layout



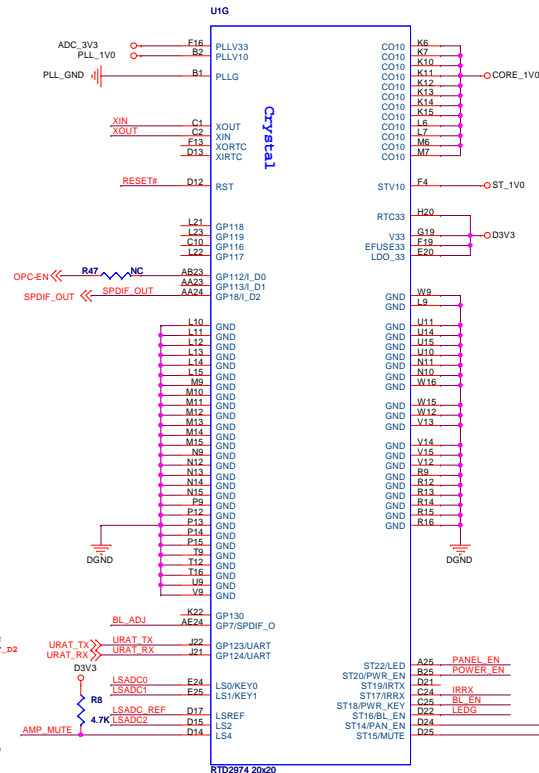
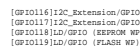
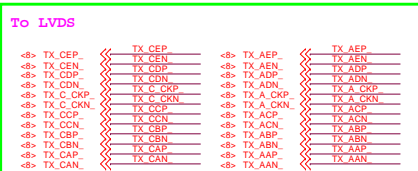




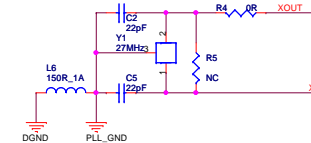
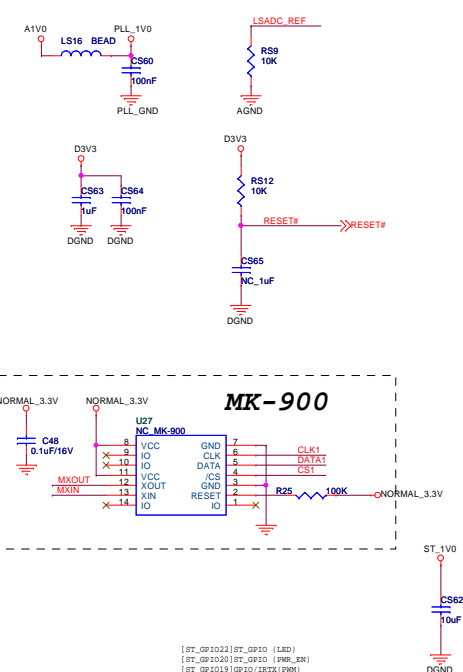
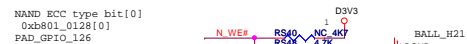
16MHz for MK900



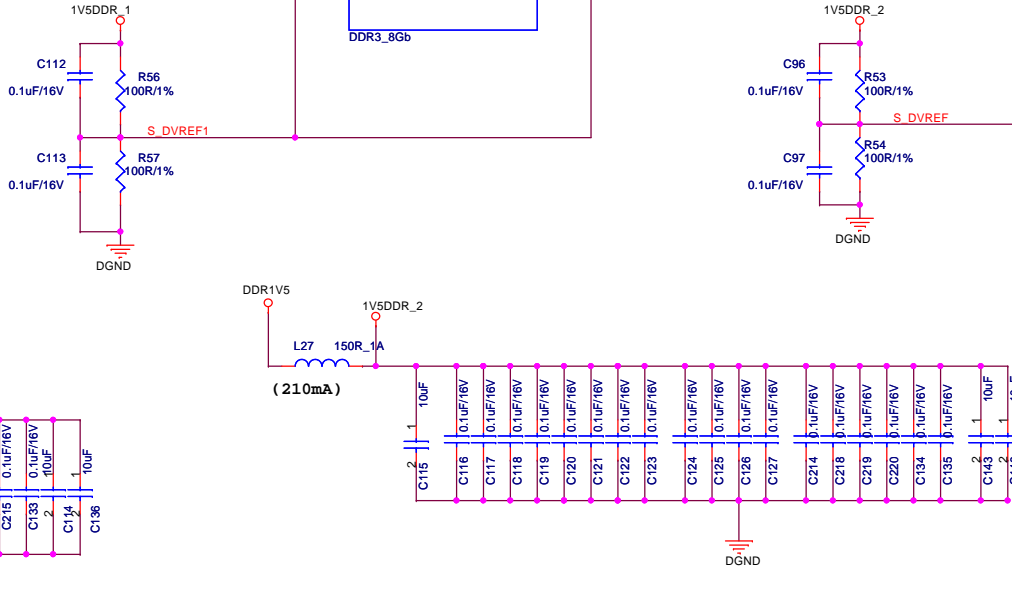
RTD2974 20x20



Power-On-Latch (POL)



## From RTD2974



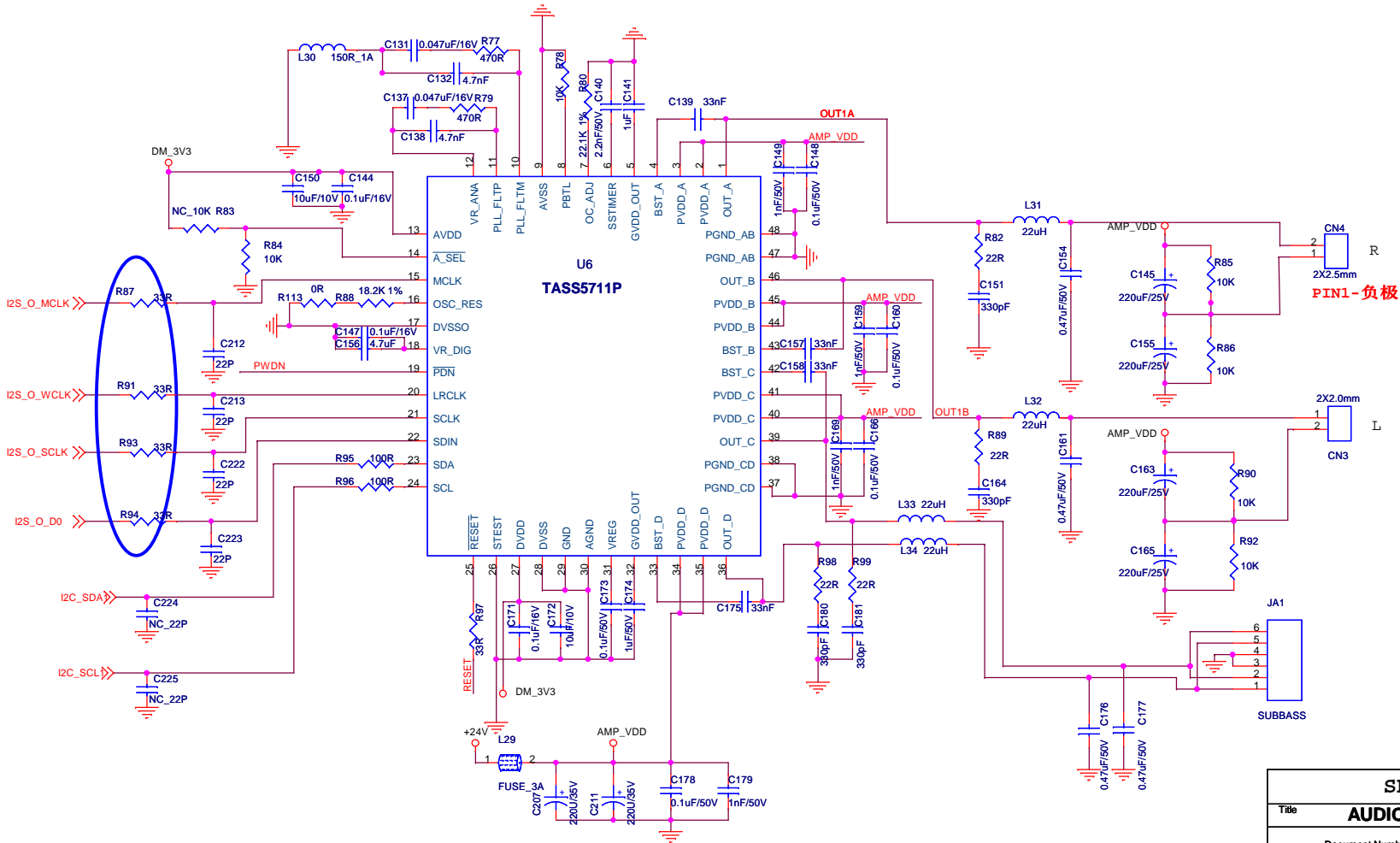
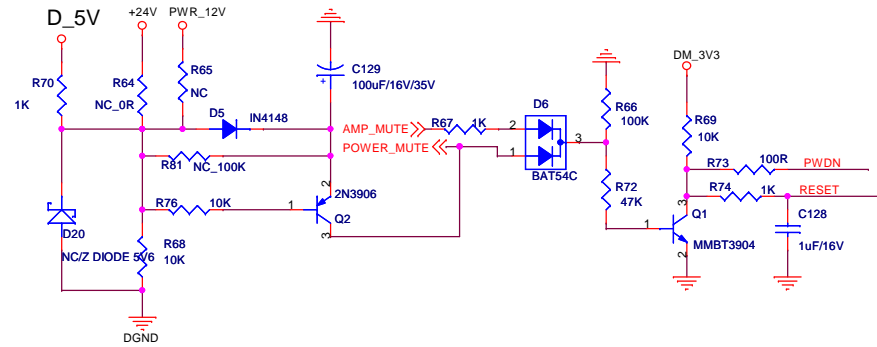
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<div style="text-align: center;"> <h3>Document Number</h3> </div>				
Size A3	Design	Check	Auditing	Rev A
		Check of name	Auditing of name	
Date:	Tuesday, January 17, 2012	Sheet	4 of 17	

# From RTD2974

<3,19> I2S\_O\_D0 >> I2S\_O\_D0

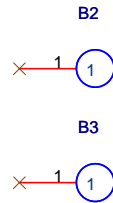
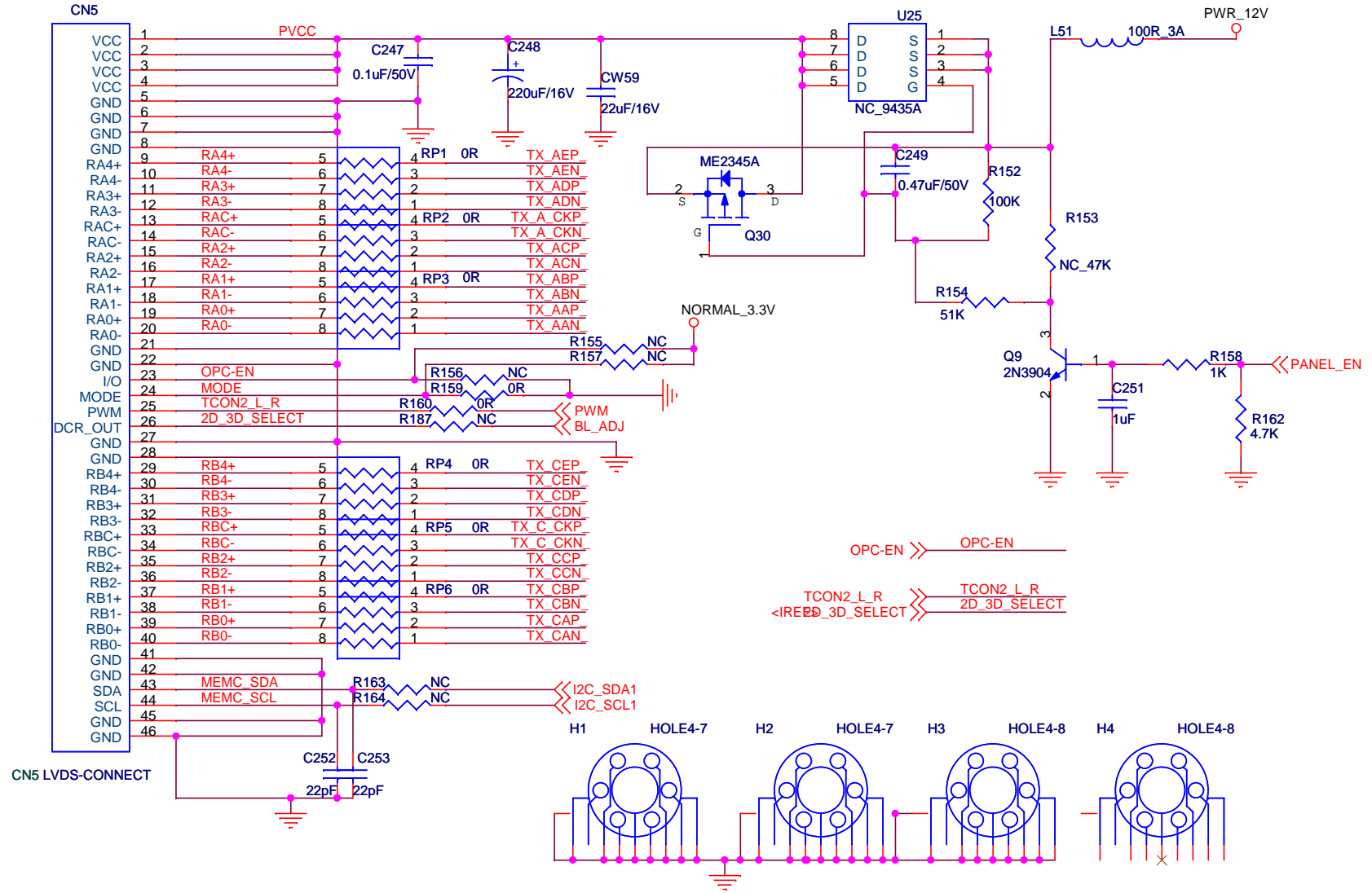
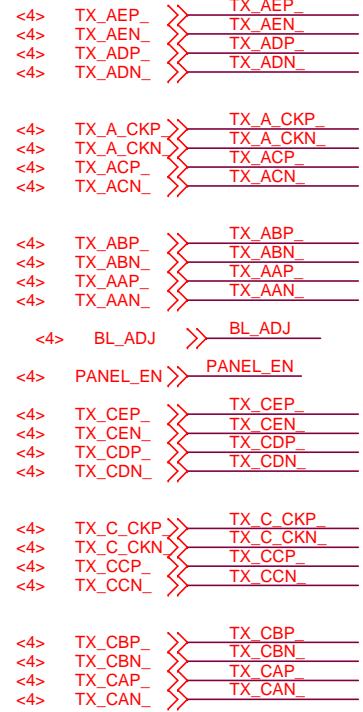
<3,19> I2S\_O\_WCLK >> I2S\_O\_WCLK  
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<4> AMP\_MUTE >> AMP\_MUTE

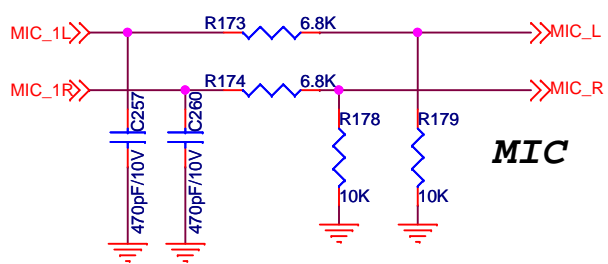
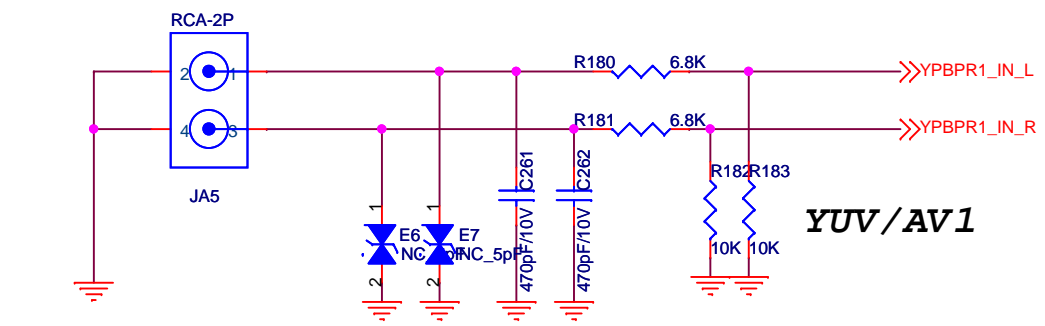
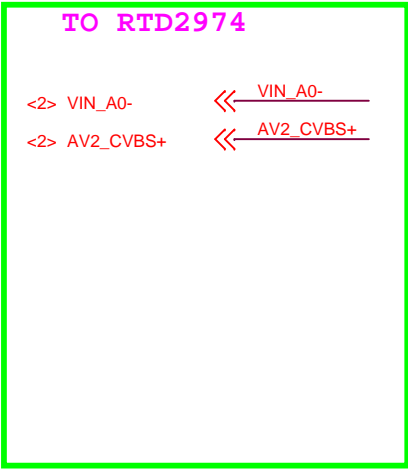
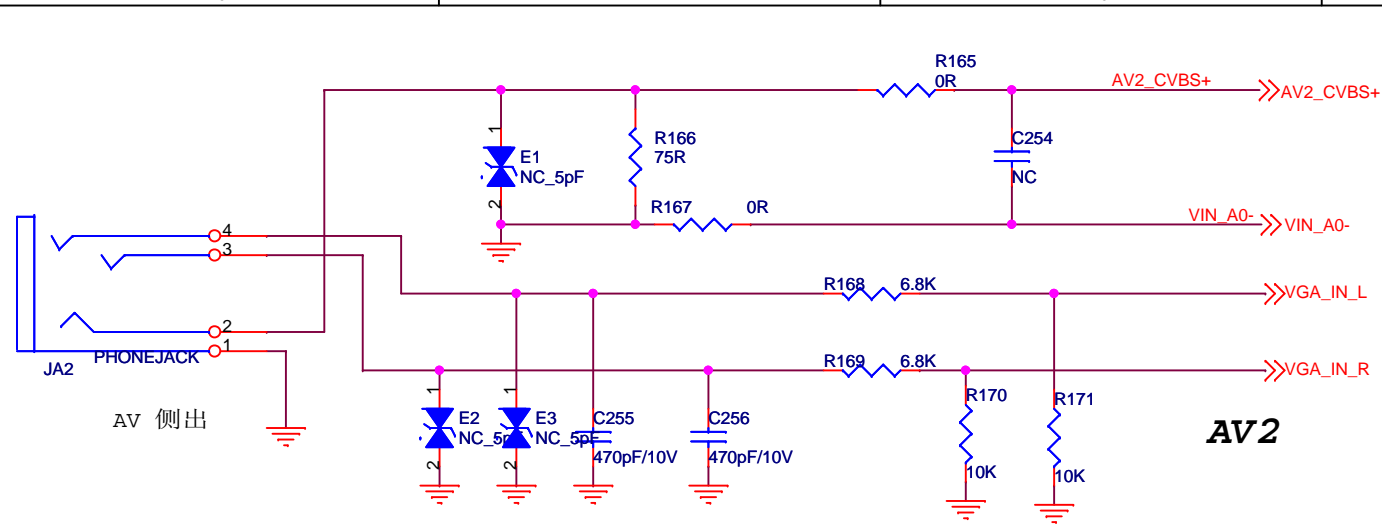


SKYWORTH 创维					
Title					
AUDIO AMP					
Document Number					
Size	Design	Check	Auditing	Modify Rev	Rev
Custom		Check of name	Auditing of name	Modify Rev	A
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# From RTD2974 for LVDS







SKYWORTH 创维					
Title AUDIO INPUT					
Document Number					
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Sheet 8		of 17			

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TO RTD2974

<2> HDMI0_D2+ << HDMI0_D2+
<2> HDMI0_D2- << HDMI0_D2-
<2> HDMI0_D1+ << HDMI0_D1+
<2> HDMI0_D1- << HDMI0_D1-
<2> HDMI0_D0+ << HDMI0_D0+
<2> HDMI0_D0- << HDMI0_D0-
<2> HDMI0_CLK+ << HDMI0_CLK+
<2> HDMI0_CLK- << HDMI0_CLK-

<2.20> HDMI0_DDC_SCL << HDMI0_DDC_SCL
<2.20> HDMI0_DDC_SDA << HDMI0_DDC_SDA
<2> HOTPLUG0 << HOTPLUG0

<2> HDMI1_D2+ << HDMI1_D2+
<2> HDMI1_D2- << HDMI1_D2-
<2> HDMI1_D1+ << HDMI1_D1+
<2> HDMI1_D1- << HDMI1_D1-
<2> HDMI1_D0+ << HDMI1_D0+
<2> HDMI1_D0- << HDMI1_D0-
<2> HDMI1_CLK+ << HDMI1_CLK+
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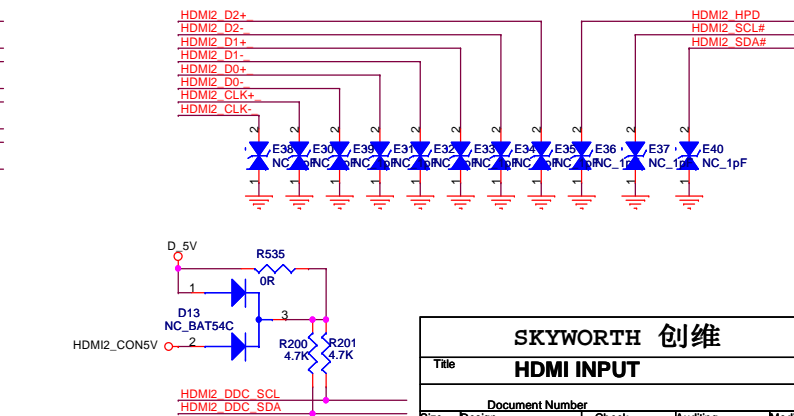
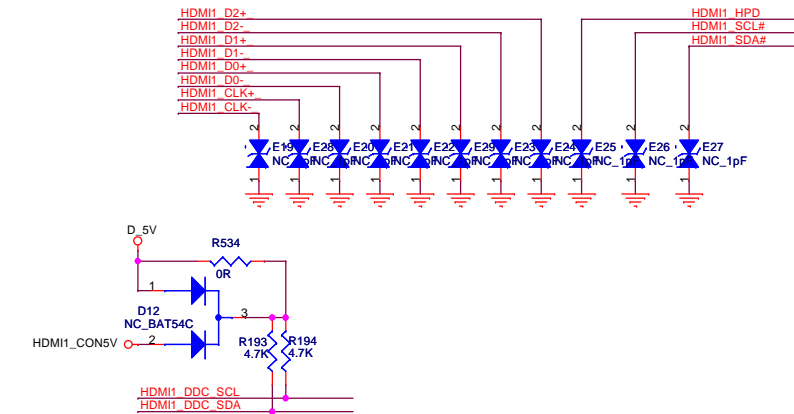
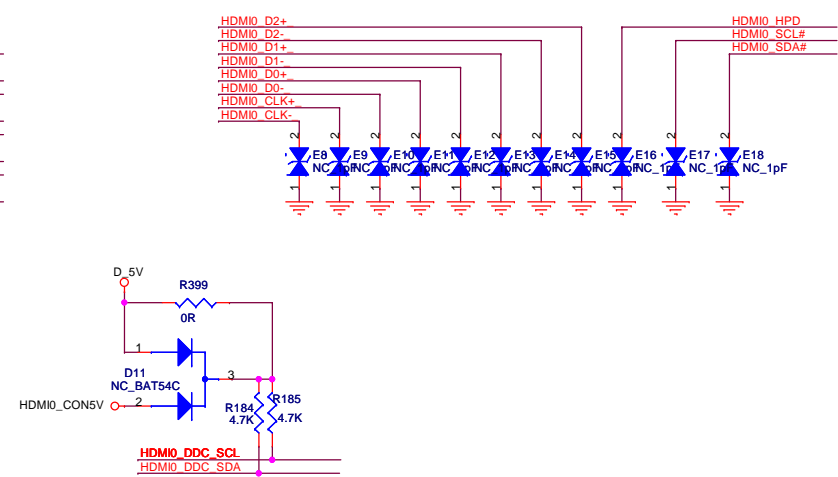
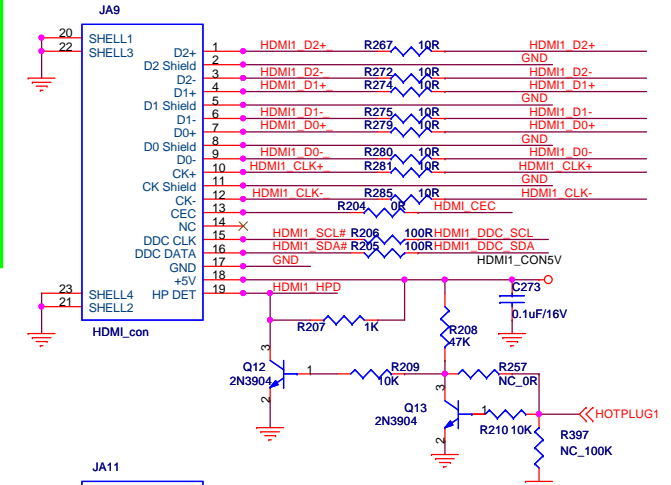
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<2.20> HDMI1_DDC_SDA << HDMI1_DDC_SDA
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<2> HDMI2_D2+ << HDMI2_D2+
<2> HDMI2_D2- << HDMI2_D2-
<2> HDMI2_D1+ << HDMI2_D1+
<2> HDMI2_D1- << HDMI2_D1-
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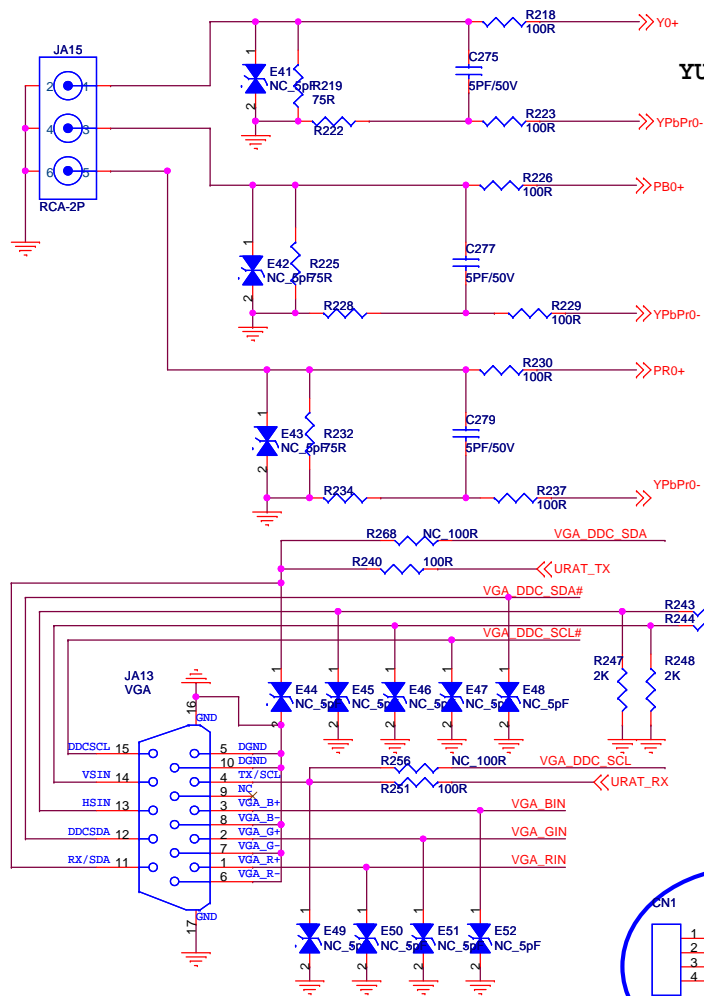
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<2.20> HDMI2_DDC_SDA << HDMI2_DDC_SDA
<2> HOTPLUG2 << HOTPLUG2

<2> HDMI_CEC << HDMI_CEC

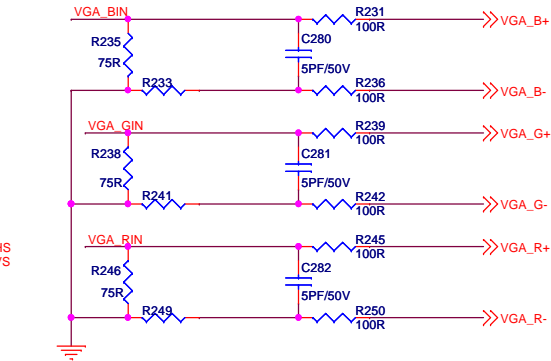
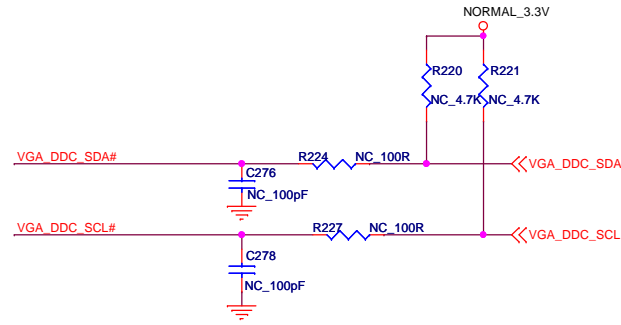
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Document Number				
Size	Design	Check	Auditing	Rev
Custom				A
Date:	Tuesday, January 17, 2012	Check of name	Auditing of name	Modify Rev
		Sheet	9	17

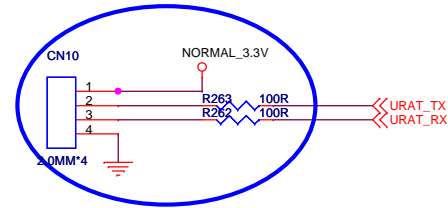
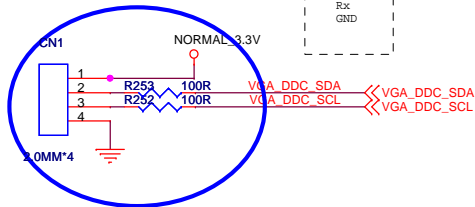


## YUV/AV1



Mark on board

RS232  
3.3V  
Tx  
Rx  
GND



## TO RTD2974

<2> VGA_B+	<<>> VGA_B+
<2> VGA_B-	<<>> VGA_B-
<2> VGA_G+	<<>> VGA_G+
<2> VGA_G-	<<>> VGA_G-
<2> VGA_R+	<<>> VGA_R+
<2> VGA_R-	<<>> VGA_R-
<2> VGA_HS	<<>> VGA_HS
<2> VGA_VS	<<>> VGA_VS
<2,20> VGA_DDC_SCL	<<>> VGA_DDC_SCL
<2,20> VGA_DDC_SDA	<<>> VGA_DDC_SDA
<2> VGA_IN_L	<<>> VGA_IN_L
<2> VGA_IN_R	<<>> VGA_IN_R

Caution: When Mass produce, these components are NC

SKYWORTH 创维

Title					
Y,Pb,Pr, & VGA INPUT					
Document Number					
Size	Design	Check	Auditing	Modify Rev	Rev
Custom	Check of name	Auditing of name	Modify Rev		A
Date:	Tuesday, January 17, 2012	Sheet	10	of	17

From RTD2974

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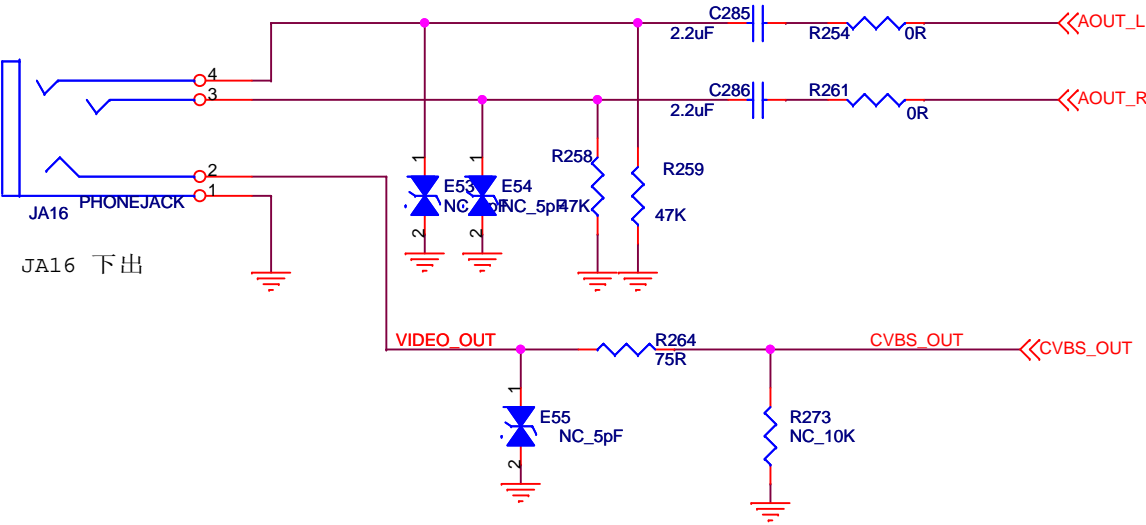
>> CVBS\_OUT

<2> AOUT\_L

>> AOUT\_L

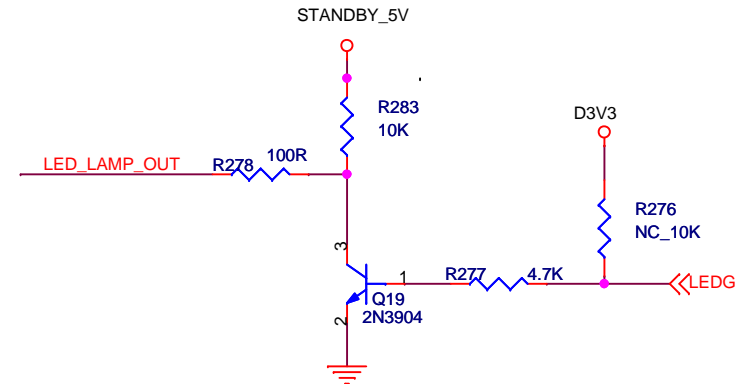
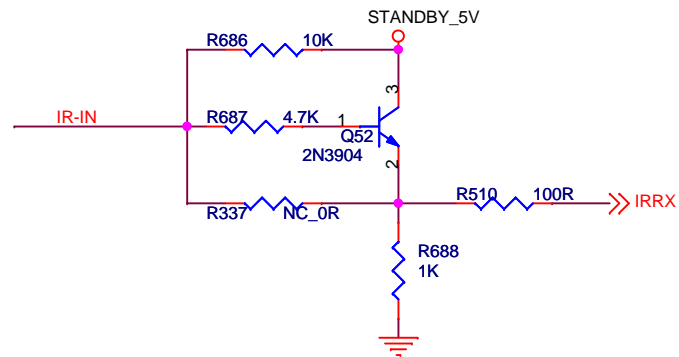
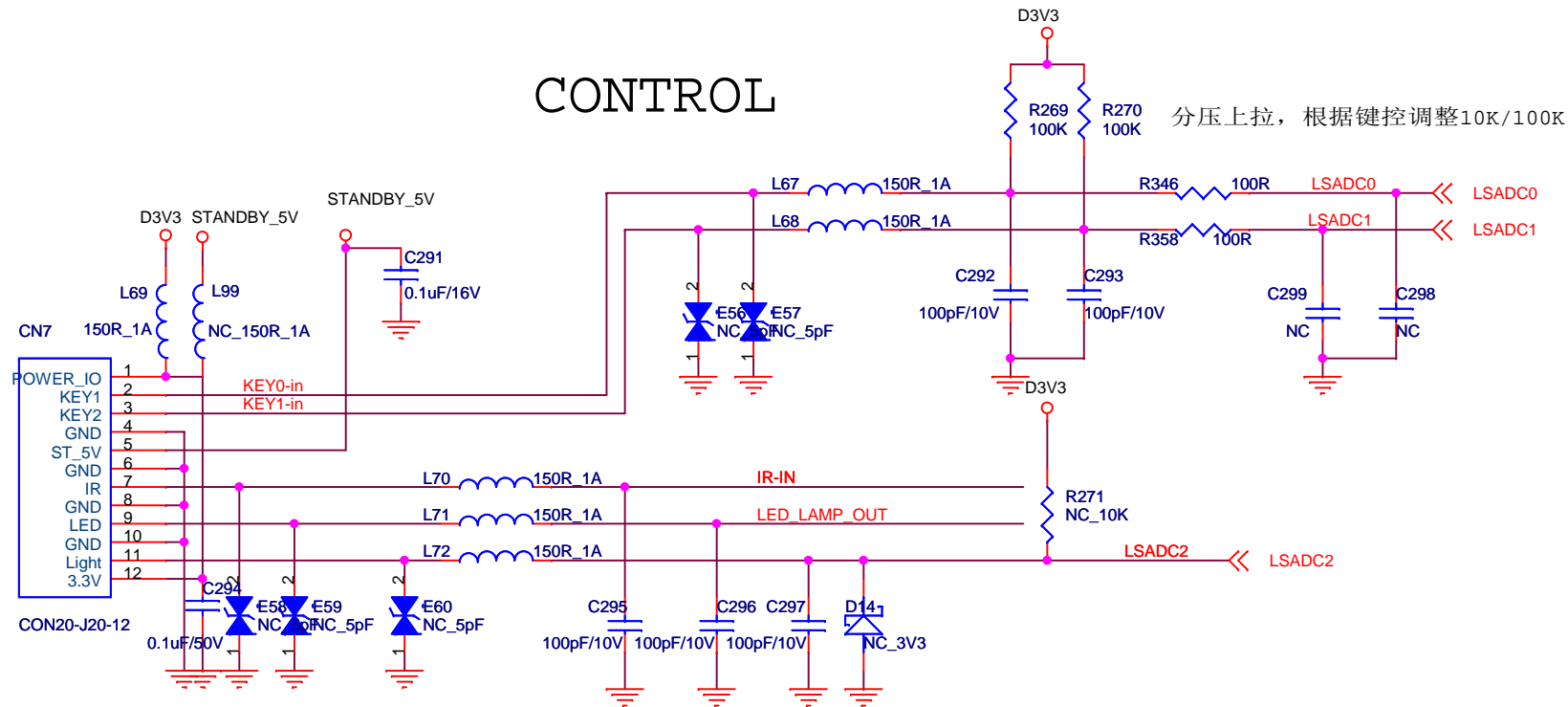
<2> AOUT\_R

>> AOUT\_R



SKYWORTH 创维					
Title VIDEO & AUDIO OUTPUT					
Document Number					
Size	Design	Check	Auditing	Modify Rev	Rev
A4		Check of name	Auditing of name	Modify Rev	A
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## CONTROL



TO RTD2974

<4> LSADC0 <<< LSADC0  
<4> LSADC1 <<< LSADC1  
<4> IRRX <<< IRRX

<4> LEDG      >>> LEDG

SKYWORTH 创维

Title	<b>IR &amp; KEY INPUT</b>
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Document Number

Size A4	Design	Check	Auditing	Modify Rev	Rev A
		Check of name	Auditing of name	Modify Rev	
Date:	Tuesday, January 17, 2012		Sheet	12	of 17

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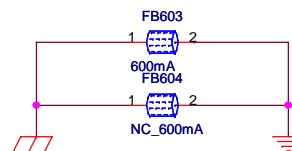
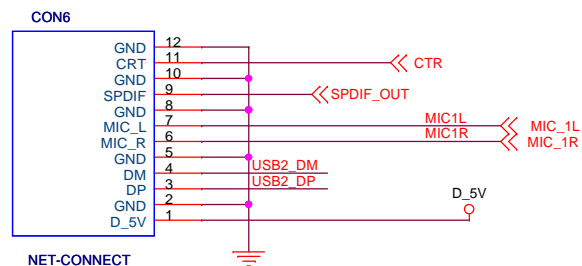
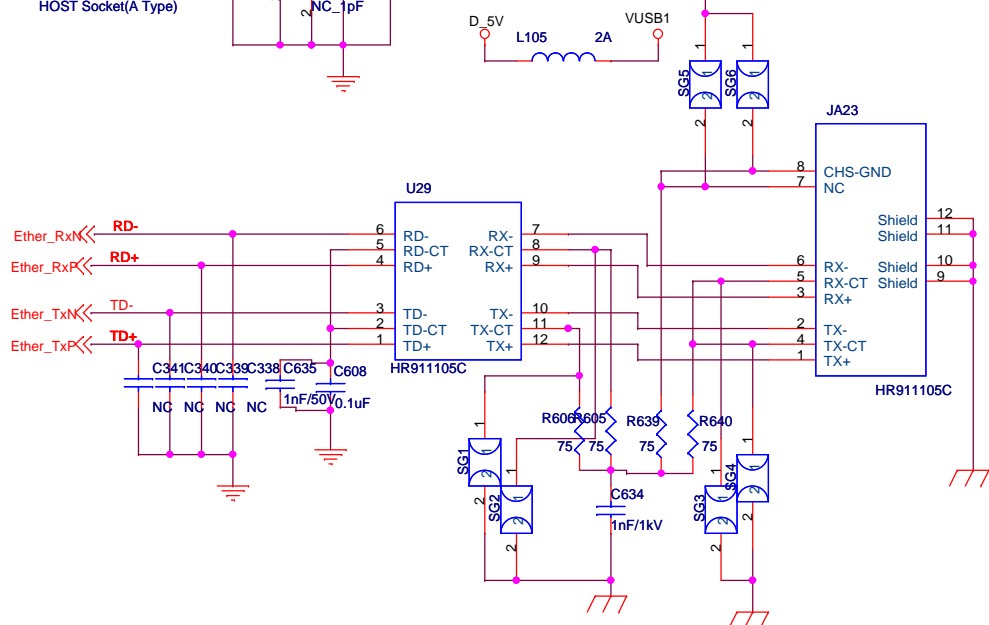
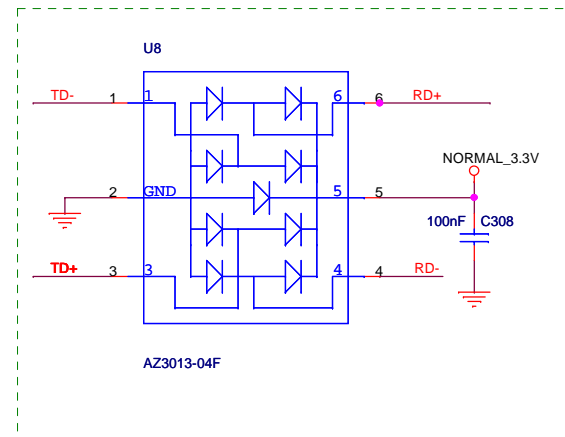
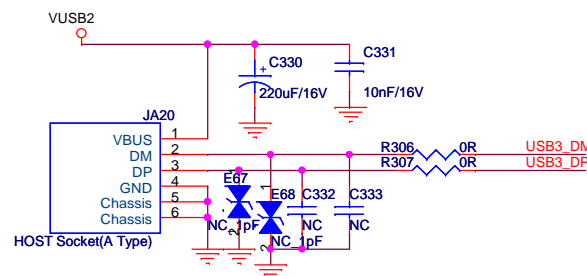
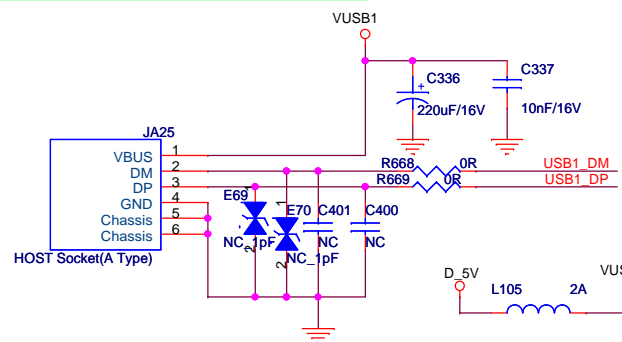
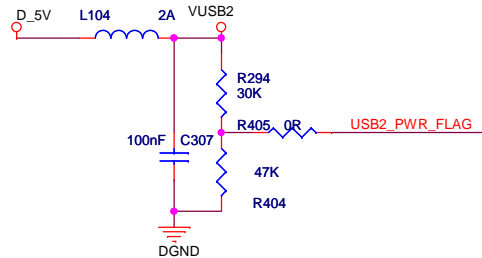
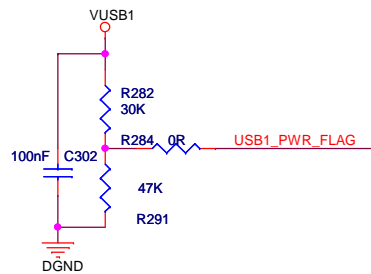
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<3>  USB1_PWR_FLAG<<< USB1_PWR_FLAG

<3>  USB2_PWR_FLAG<<< USB2_PWR_FLAG

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# From RTD2974

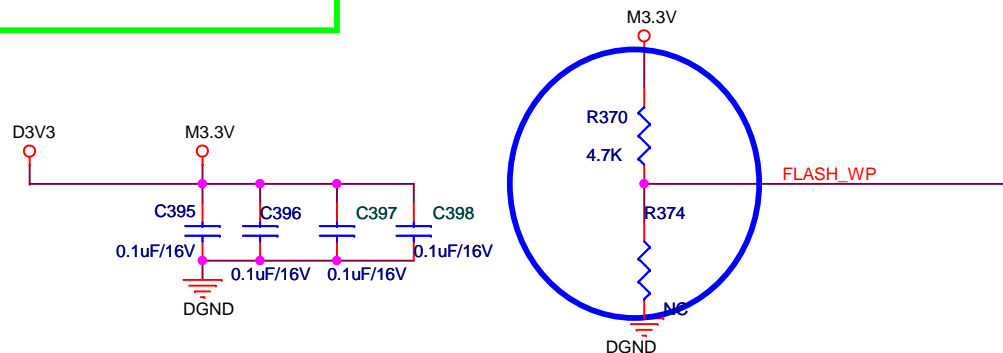
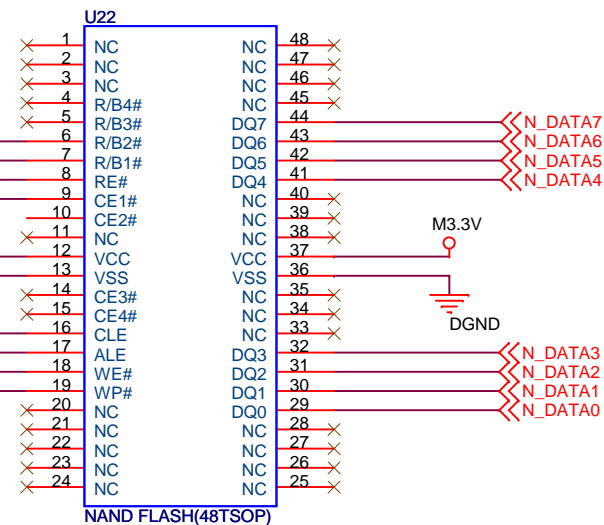
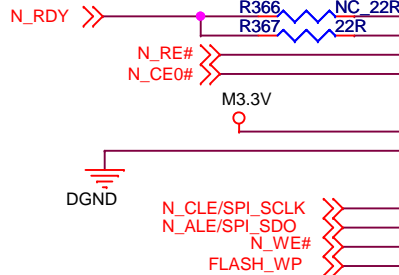
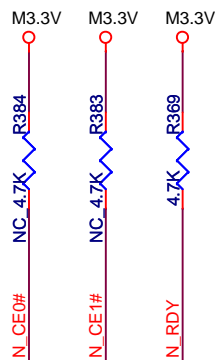
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<4> N\_CLE/SPI\_SCLK

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<4> N\_CE0#  
<4> N\_WE#  
<4> N\_RE#

<4> N\_RDY  
<4> N\_DATA0  
<4> N\_DATA1  
<4> N\_DATA2  
<4> N\_DATA3  
<4> N\_DATA4  
<4> N\_DATA5  
<4> N\_DATA6  
<4> N\_DATA7

N\_CE1#  
N\_CE0#  
N\_WE#  
N\_RE#

N\_RDY  
N\_DATA0  
N\_DATA1  
N\_DATA2  
N\_DATA3  
N\_DATA4  
N\_DATA5  
N\_DATA6  
N\_DATA7



Caution: When Mass produce, R370 change to NC, R374 change to 4.7K.