

VERTICAL DEFLECTION BOOSTER

ADVANCE DATA

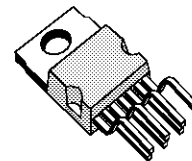
- POWER AMPLIFIER
- FLYBACK GENERATOR
- THERMAL PROTECTION
- OUTPUT CURRENT UP TO 2.0A_{PP}
- FLYBACK VOLTAGE UP TO 90V (on Pin 5)
- INTERNAL REFERENCE VOLTAGE

DESCRIPTION

Designed for monitors and high performance TVs, the STV9378 vertical deflection booster delivers flyback voltages up to 90V.

The STV9378 operates with supplies up to 42V and provides up to 2A_{PP} output current to drive the yoke.

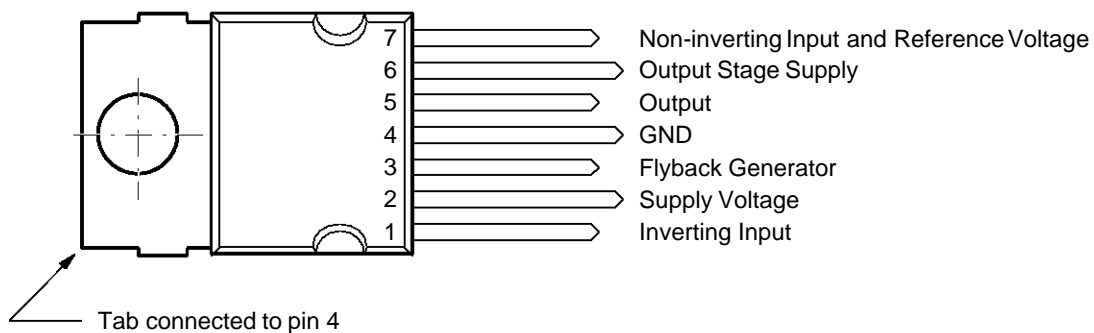
The STV9378 is offered in HEPTAWATT package.



HEPTAWATT
(Plastic Package)

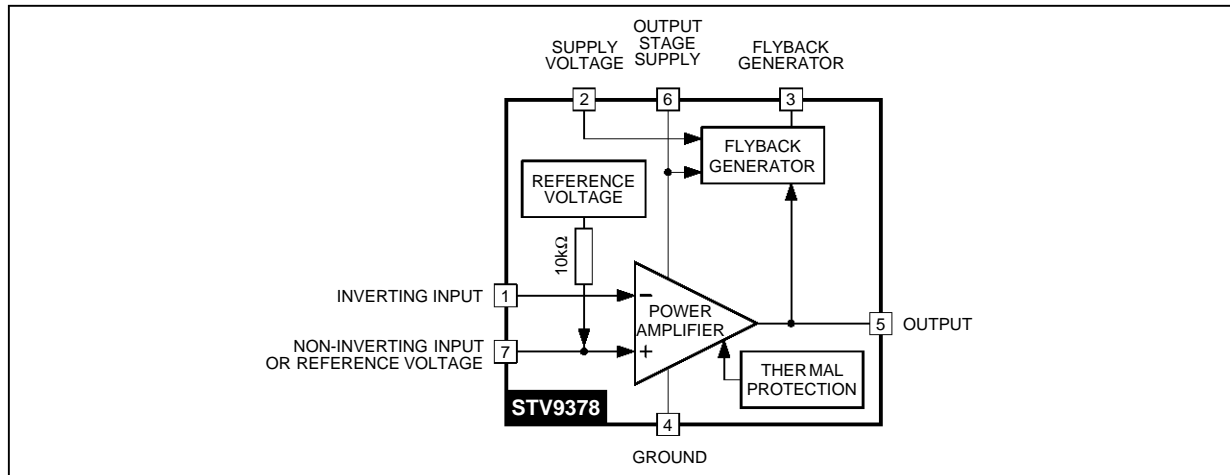
ORDER CODE : STV9378

PIN CONNECTIONS



9378-01.EPS

BLOCK DIAGRAM



9378-02.EPS

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_S	Supply Voltage (Pin 2) (see note 1)	50	V
V_6	Flyback Peak Voltage (Pin 6) (see note 1)	100	V
V_1, V_7	Amplifier Input Voltage (Pins 1-7) (see note 1)	- 0.3, + V_S	V
I_O	Maximum Output Peak Current (see notes 2 and 3)	1.5	A
I_3	Maximum Sink Current (first part of flyback) ($t < 1\text{ms}$)	1.5	A
I_3	Maximum Source Current ($t < 1\text{ms}$)	1.5	A
T_{oper}	Operating Ambient Temperature	- 20, + 75	°C
T_{stg}	Storage Temperature	- 40, + 150	°C
T_j	Junction Temperature	+150	°C

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- Notes :
1. Versus GND.
 2. The output current can reach 4A peak for $t \leq 10\mu\text{s}$ (up to 120Hz).
 3. Provided SOAR is respected (see Figures 1 and 2).

THERMAL DATA

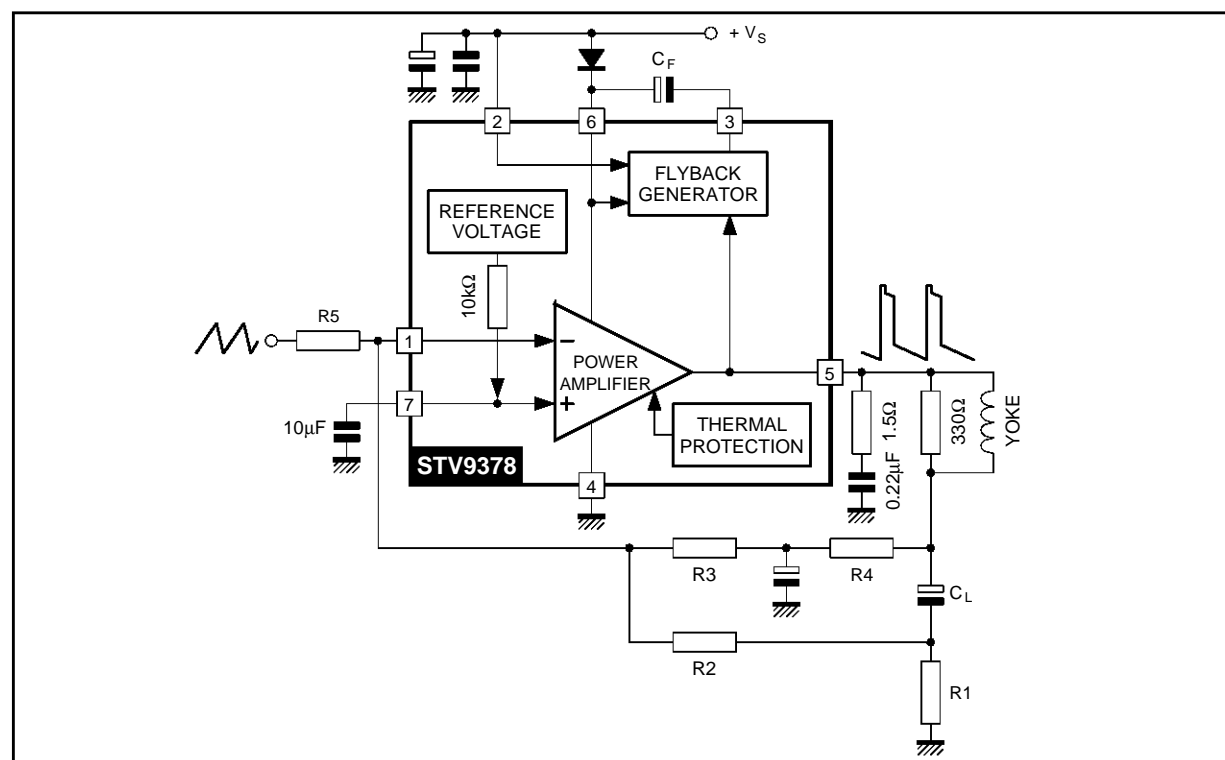
Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	Junction-case Thermal Resistance Max.	3	°C/W
T_t	Temperature for Thermal Shutdown	150	°C
ΔT_t	Hysteresis on T_t	10	°C
T_{jr}	Recommended Max. Junction Temperature	120	°C

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ELECTRICAL CHARACTERISTICS(V_S = 42V, T_A = 25°C, unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _S	Operating Supply Voltage Range		10		42	V
I ₂	Pin 2 Quiescent Current	I ₃ = 0, I ₅ = 0		10	20	mA
I ₆	Pin 6 Quiescent Current	I ₃ = 0, I ₅ = 0	5	10	30	mA
I _O	Max. Peak Output Current				1	A
I ₁	Amplifier Bias Current	V ₁ = 1V		- 0.15	- 1	μA
V ₇	Internal Reference Voltage		2.2	2.3	2.4	V
$\frac{\Delta V_7}{\Delta V_S}$	Reference Voltage Drift versus V _S	V _S = 24 to 42V		2	4	mV/V
K _t	Reference Voltage Drift versus T _j			100	150	ppm/°C
GV	Voltage Gain		80			dB
V _{5L}	Output Saturation Voltage to GND (Pin 4)	I ₅ = 1A		1	1.5	V
V _{5H}	Output Saturation Voltage to Supply (Pin 6)	I ₅ = -1A		1.6	2.1	V
V _{D5-6}	Diode Forward Voltage between Pins 5-6	I ₅ = 1A		1.5	2	V
V _{D3-2}	Diode Forward Voltage between Pins 3-2	I ₃ = 1A		1.5	2	V
V _{3L}	Saturation Voltage on Pin 3	I ₃ = 20mA		0.8	1.2	V
V _{3SH}	Saturation Voltage to Pin 2 (2nd part of flyback)	I ₃ = -1A		2.1	2.9	V

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APPLICATION CIRCUIT

9378-03.EPS

Figure 1 : Output Transistors SOA
(for secondary breakdown)

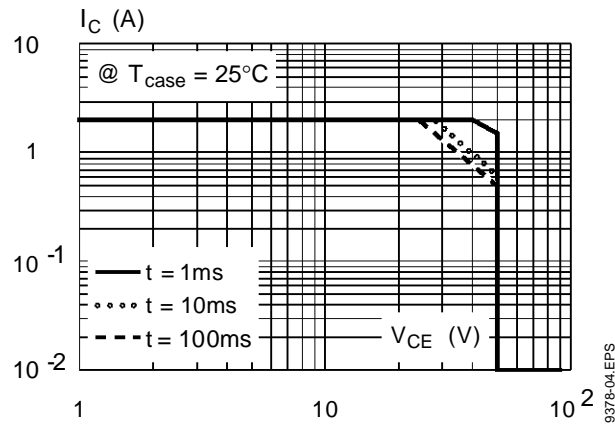
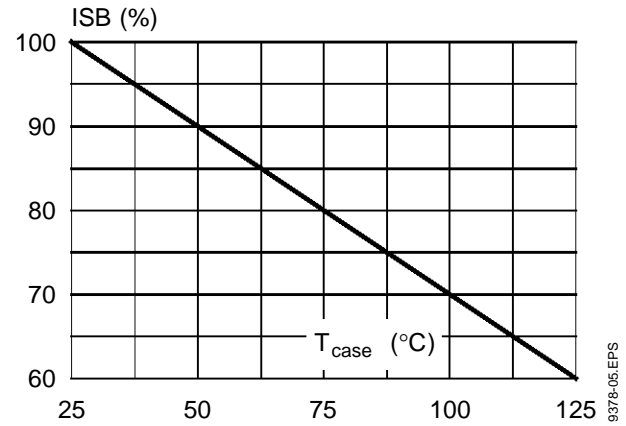
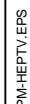


Figure 2 : Secondary Breakdown Temperature
Derating Curve
(ISB = secondary breakdown current)



HEPTV.TBL

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